

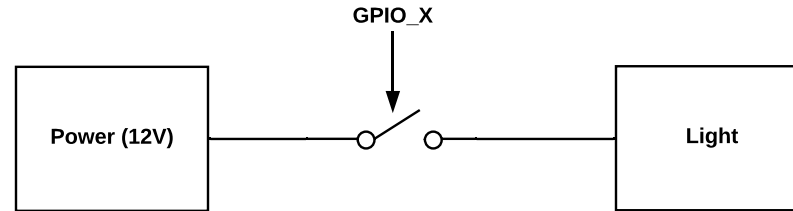
# FOD8343D Optoisolator Circuit Design for Lights

Design by  
Jason Lim and Kathy Nguyen  
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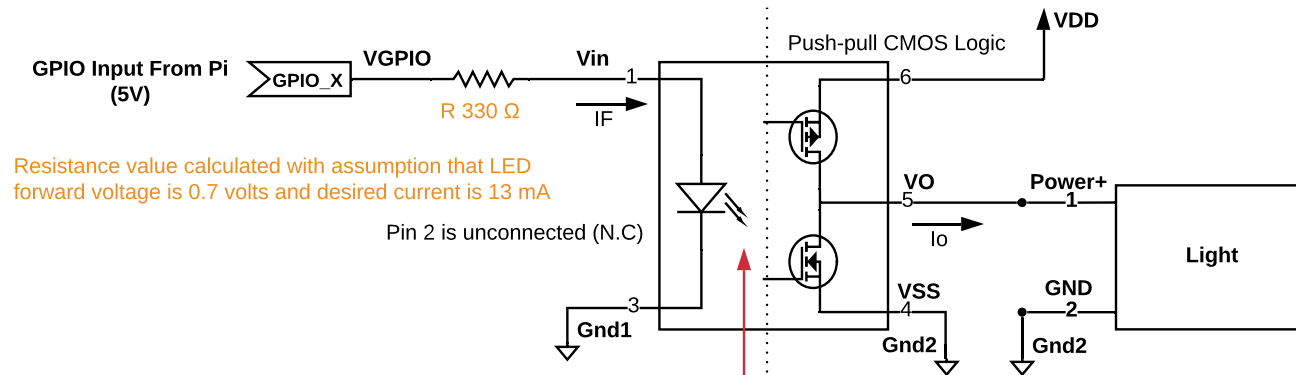
## High Level

GPIO_X	Light
ON	ON
OFF	OFF

```
if(GPIO_X == ON){
    Light = ON;
}
if(GPIO_X == OFF){
    Light = OFF;
}
```



## Low Level



Resistance value calculated with assumption that LED forward voltage is 0.7 volts and desired current is 13 mA

Pin 2 is unconnected (N.C.)

Gnd1 and Gnd2 references do not need to be at the same voltage  
GPIO signal is electrically isolated from 12V power supply  
Logic is not inverted, high at the input creates high at the output

Double inversion details unclear (Internal circuit incomplete)

### Requirements for Operation.

For safe operation,  $I_o$ ,  $V_{DD}$ , and  $I_F$ , should not be close to their max/min values

$$\begin{cases} V_{GPIO} - Gnd1 = 5 \text{ Volts} \\ V_{DD} - Gnd2 = 12 \text{ Volts} \\ -0.5 \text{ Volts} < V_{DD} < 35 \text{ Volts} \\ 0 \text{ A} < I_o < 4 \text{ A} \\ 0 \text{ A} < I_F < 25 \text{ mA} \end{cases}$$

## ON Semiconductor Datasheet Snippets for FOD8343D Optoisolator

### Pin Configuration

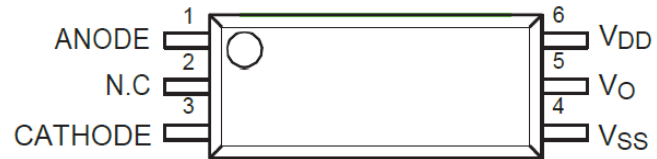


Figure 3. Pin Configuration

### Functional Schematic

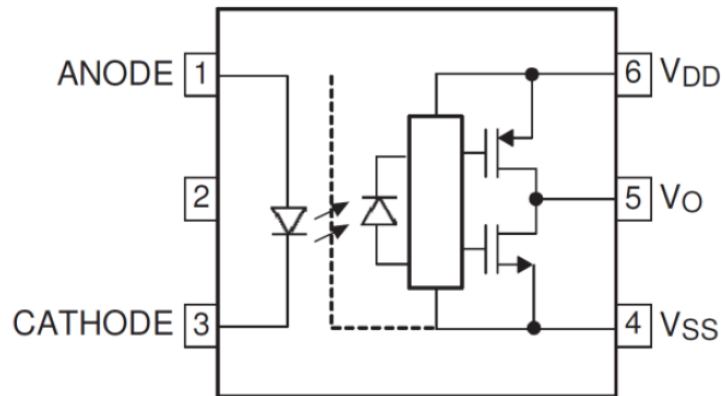


Figure 1. Schematic

### Pin Definitions

Pin #	Name	Description
1	ANODE	LED Anode
2	N.C	Not Connection
3	CATHODE	LED Cathode
4	V <sub>SS</sub>	Negative Supply Voltage
5	V <sub>O</sub>	Output Voltage
6	V <sub>DD</sub>	Positive Supply Voltage

## Absolute Maximum Ratings (T<sub>A</sub> = 25°C unless otherwise specified.)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameter	Value	Unit
T <sub>STG</sub>	Storage Temperature	-40 to +125	°C
T <sub>OPR</sub>	Operating Temperature	-40 to +100	°C
T <sub>J</sub>	Junction Temperature	-40 to +125	°C
T <sub>SOL</sub>	Lead Solder Temperature (Refer to Reflow Temperature Profile)	260 for 10 sec	°C
I <sub>F(AVG)</sub>	Average Input Current	25	mA
V <sub>R</sub>	Reverse Input Voltage	5.0	V
I <sub>O(PEAK)</sub>	Peak Output Current <sup>(1)</sup>	4	A
V <sub>DD</sub>	Supply Voltage	-0.5 to 35	V
V <sub>O(PEAK)</sub>	Peak Output Voltage	0 to V <sub>DD</sub>	V
t <sub>R(IN)</sub> , t <sub>F(IN)</sub>	Input Signal Rise and Fall Time	250	ns
PD <sub>I</sub>	Input Power Dissipation <sup>(2)(4)</sup>	45	mW
PD <sub>O</sub>	Output Power Dissipation <sup>(3)(4)</sup>	500	mW

### Notes:

1. Maximum pulse width = 10 μs
2. No derating required across operating temperature range.
3. Derate linearly from 25°C at a rate of 5.2mW/°C.
4. Functional operation under these conditions is not implied. Permanent damage may occur if the device is subjected to conditions outside these ratings.

## Recommended Operating Conditions

The Recommended Operating Conditions table defines the conditions for actual device operation. Recommended operating conditions are specified to ensure optimal performance to the datasheet specifications. ON does not recommend exceeding them or designing to absolute maximum ratings.

Symbol	Parameter	Min.	Max.	Unit
T <sub>A</sub>	Ambient Operating Temperature	-40	+100	°C
V <sub>DD</sub> – V <sub>SS</sub>	Supply Voltage	10	30	V
I <sub>F(ON)</sub>	Input Current (ON)	10	16	mA
V <sub>F(OFF)</sub>	Input Voltage (OFF)	-3.0	0.8	V