ALTIUM
Designer

Design Rule Verification Report

Date:	2/22/2023
Time:	8:05:33 PM
Elapsed Time:	00:00:02
Filename:	D:\LocalDocuments\Projects\SeniorDesign-Group9-Schematics\Power\power.PcbDoc

Warnings:	24
Rule Violations:	0

Summary

Warnings	Count
Zero hole size multi-layer pad(s) detected	10
Unplated multi-layer pad(s) detected	4
Multilayer Pads with 0 size Hole found	10
Total	**24**

Rule Violations	Count
Clearance Constraint (Gap=10mil) (All),(All)	0
Clearance Constraint (Gap=0mil) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=6mil) (Max=36mil) (Preferred=10mil) (All)	0
Power Plane Connect Rule(Relief Connect)(Expansion=20mil) (Conductor Width=10mil) (Air Gap=10mil) (Entries=4) (All)	0
Minimum Annular Ring (Minimum=5.118mil) (All)	0
Hole Size Constraint (Min=7.874mil) (Max=248.031mil) (All)	0

[Hole To Hole Clearance (Gap=10mil) (All),(All)](#)	0
[Minimum Solder Mask Sliver (Gap=0.1mil) (All),(All)](#)	0
[Silk To Solder Mask (Clearance=5.906mil) (All),(All)](#)	0
[Silk to Silk (Clearance=10mil) (All),(All)](#)	0
[Net Antennae (Tolerance=0mil) (All)](#)	0
[Height Constraint (Min=0mil) (Max=1000mil) (Prefered=500mil) (All)](#)	0
Total	**0**

Warnings

Zero hole size multi-layer pad(s) detected

[Pad Free-TP3(3830mil,5375mil) on Multi-Layer](#) on [Net +3V3](#)

[Pad Free-TP10(3600mil,5840mil) on Multi-Layer](#) on [Net GND](#)

[Pad Free-TP9(4175mil,5375mil) on Multi-Layer](#) on [Net NetJ1_4](#)

[Pad Free-TP8(5690mil,5205mil) on Multi-Layer](#) on [Net NetR8_1](#)

[Pad Free-TP7(6235mil,4705mil) on Multi-Layer](#) on [Net NetC21_1](#)

[Pad Free-TP6(4560mil,5840mil) on Multi-Layer](#) on [Net +12](#)

[Pad Free-TP4(5640mil,5835mil) on Multi-Layer](#) on [Net NetBT1_1](#)

[Pad Free-TP5(4075mil,5375mil) on Multi-Layer](#) on [Net +5](#)

[Pad Free-TP2(4445mil,5840mil) on Multi-Layer](#) on [Net VBAT](#)

[Pad Free-TP1(5055mil,5840mil) on Multi-Layer](#) on [Net VSOLAR](#)

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Unplated multi-layer pad(s) detected

[Pad L4-1(4489.016mil,1600mil) on Multi-Layer](#) on [Net NetCboot_2_1](#)

[Pad L4-2(4740.984mil,1600mil) on Multi-Layer](#) on [Net NetCboot_4_1](#)

[Pad L3-1(1140mil,224.016mil) on Multi-Layer](#) on [Net NetCboot_3_1](#)

Pad L3-2(1140mil,475.984mil) on Multi-Layer on Net +5

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Multilayer Pads with 0 size Hole found

Pad Free-TP3(3830mil,5375mil) on Multi-Layer

Pad Free-TP10(3600mil,5840mil) on Multi-Layer

Pad Free-TP9(4175mil,5375mil) on Multi-Layer

Pad Free-TP8(5690mil,5205mil) on Multi-Layer

Pad Free-TP7(6235mil,4705mil) on Multi-Layer

Pad Free-TP6(4560mil,5840mil) on Multi-Layer

Pad Free-TP4(5640mil,5835mil) on Multi-Layer

Pad Free-TP5(4075mil,5375mil) on Multi-Layer

Pad Free-TP2(4445mil,5840mil) on Multi-Layer

Pad Free-TP1(5055mil,5840mil) on Multi-Layer

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