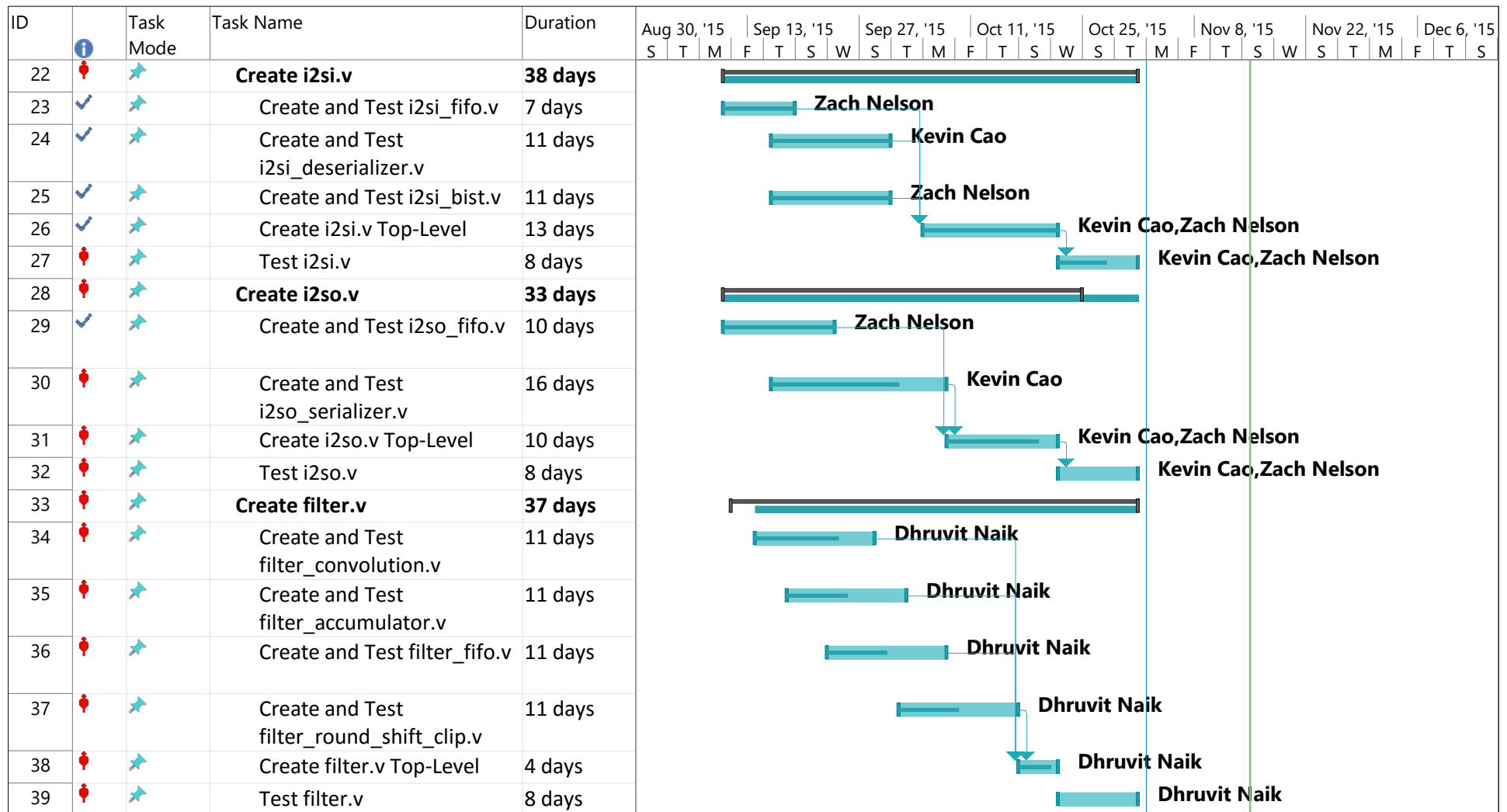
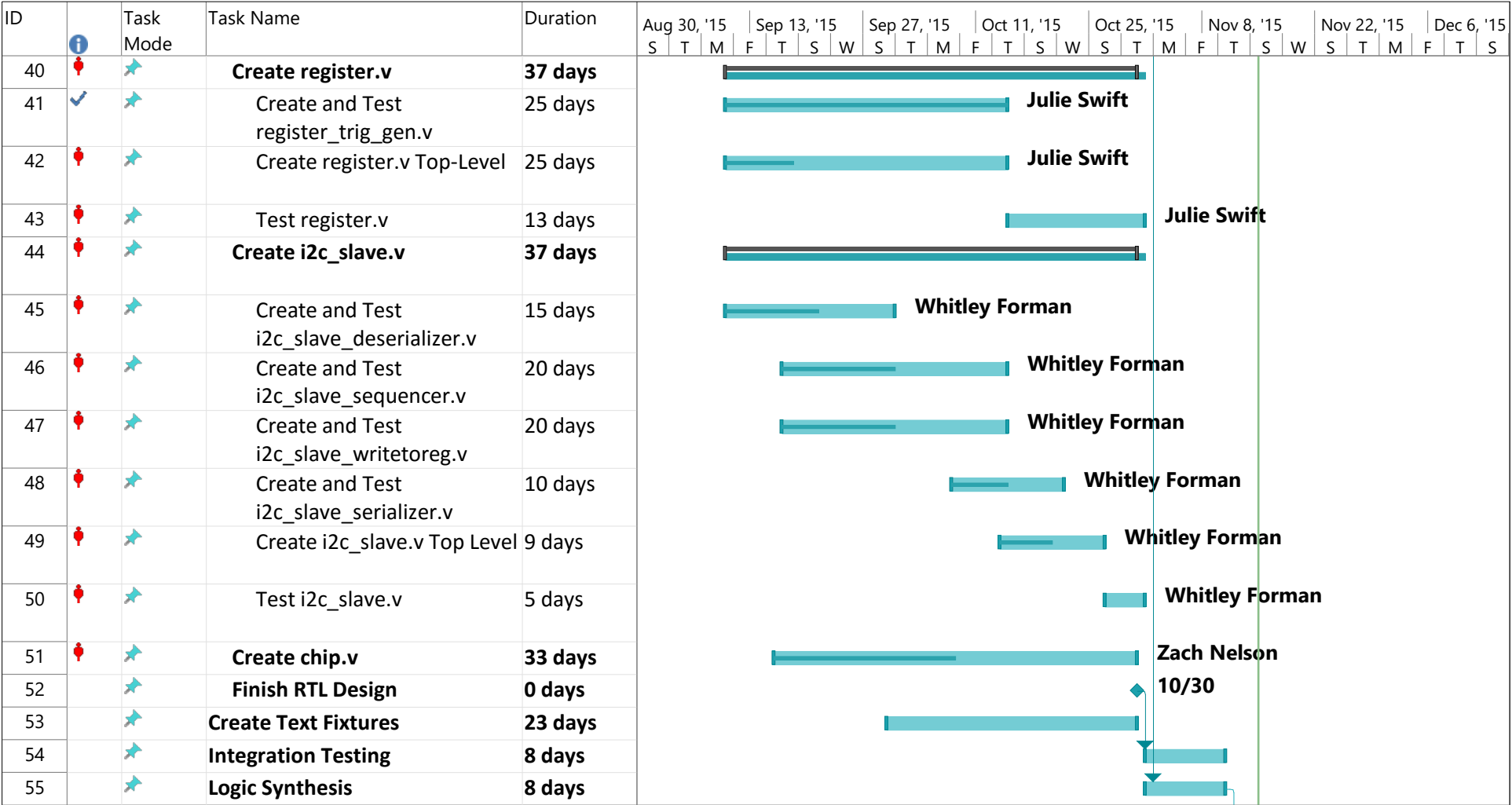


| | | | | | | |
|--|--------------------|--|-----------------------|--|--------------------|--|
| Project: Chip Design Date: Sun 11/15/15 | Task | | Inactive Summary | | External Tasks | |
| | Split | | Manual Task | | External Milestone | |
| | Milestone | | Duration-only | | Deadline | |
| | Summary | | Manual Summary Rollup | | Progress | |
| | Project Summary | | Manual Summary | | Manual Progress | |
| | Inactive Task | | Start-only | | | |
| | Inactive Milestone | | Finish-only | | | |



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|--|--------------------|--|-----------------------|--|--------------------|--|
| Project: Chip Design Date: Sun 11/15/15 | Task | | Inactive Summary | | External Tasks | |
| | Split | | Manual Task | | External Milestone | |
| | Milestone | | Duration-only | | Deadline | |
| | Summary | | Manual Summary Rollup | | Progress | |
| | Project Summary | | Manual Summary | | Manual Progress | |
| | Inactive Task | | Start-only | | | |
| | Inactive Milestone | | Finish-only | | | |



Project: Chip Design
Date: Sun 11/15/15

Task

Split

Milestone

Summary

Project Summary

Inactive Task

Inactive Milestone

Inactive Summary

Manual Task

Duration-only

Manual Summary Rollup

Manual Summary

Start-only

Finish-only

External Tasks

External Milestone

Deadline

Progress

Manual Progress

| ID | | Task Mode | Task Name | Duration | Aug 30, '15 | Sep 13, '15 | Sep 27, '15 | Oct 11, '15 | Oct 25, '15 | Nov 8, '15 | Nov 22, '15 | Dec 6, '15 |
|----|--|-----------|-------------------------------------|----------|-------------|-------------|-------------|-------------|-------------|------------|-------------|------------|
| | | | | | S | T | M | F | T | S | W | S |
| 56 | | | Floor Planning | 6 days | | | | | | | | |
| 57 | | | Place and Route Gates | 5 days | | | | | | | | |
| 58 | | | Board Design | 26 days | | | | | | | | |
| 62 | | | Have a Functional FPGA | 0 days | | | | | | | | |
| 63 | | | Write Final Report and Presentation | 5 days | | | | | | | | |
| 64 | | | Verification | 1 day | | | | | | | | |
| 65 | | | MOSIS Fabrication Deadline | 0 days | | | | | | | | |



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|---|--------------------|--|-----------------------|--|--------------------|--|
| <div>Project: Chip Design</div> <div>Date: Sun 11/15/15</div> | Task | | Inactive Summary | | External Tasks | |
| | Split | | Manual Task | | External Milestone | |
| | Milestone | | Duration-only | | Deadline | |
| | Summary | | Manual Summary Rollup | | Progress | |
| | Project Summary | | Manual Summary | | Manual Progress | |
| | Inactive Task | | Start-only | | | |
| | Inactive Milestone | | Finish-only | | | |