








ID		Task Name	% Complete	Jan 17, '16							Jan 31, '16				Feb 14, '16				Feb 28, '16				Mar 13, '16				Mar 27, '16				Apr 10, '16				Apr 24, '16			
				S	T	M	F	T	S	W	S	T	M	F	T	S	W	S	T	M	F	T	S	W	S	T	M	F	T	S	W							
1		System Design	100%																																			
13		Coding	100%																																			
45		Install EDA Tools	90%	<div><div></div></div>																																		
46		Determine EDA Requirments	100%																																			
47		Install OS	100%																																			
48		Install EDA Tool	100%																																			
49		Install Libraries	70%	<div><div></div></div>																																		
50		Create a Simple Test Case	100%																																			
51		Simulation/Verification	50%	<div><div></div></div>																																		
52		Test i2s_in.v	76%	<div><div></div></div>																																		
53		Test i2s_out.v	76%	<div><div></div></div>																																		
54		Test filter.v	76%	<div><div></div></div>																																		
55		Test chip_reg.v	76%	<div><div></div></div>																																		
56		Test i2c.v	76%	<div><div></div></div>																																		
57		Test Control Flow Subsystem	0%	<div><div></div></div>																																		
58		Test Data Flow Subsystem	0%	<div><div></div></div>																																		
59		Test chip.v	0%	<div><div></div></div>																																		
60		Synthesis	70%	<div><div></div></div>																																		
61		Synthesize i2s_in.v	84%	<div><div></div></div>																																		
62		Synthesize i2s_out.v	84%	<div><div></div></div>																																		
63		Synthesize filter.v	84%	<div><div></div></div>																																		
64		Synthesize chip_reg.v	84%	<div><div></div></div>																																		
65		Synthesize i2c.v	84%	<div><div></div></div>																																		

Project: Chip Design
Date: Fri 2/5/16

Task



Inactive Summary



External Tasks



Split



Manual Task



External Milestone



Milestone



Duration-only



Deadline



Summary



Manual Summary Rollup



Progress



Project Summary



Manual Summary



Manual Progress



Inactive Task



Start-only

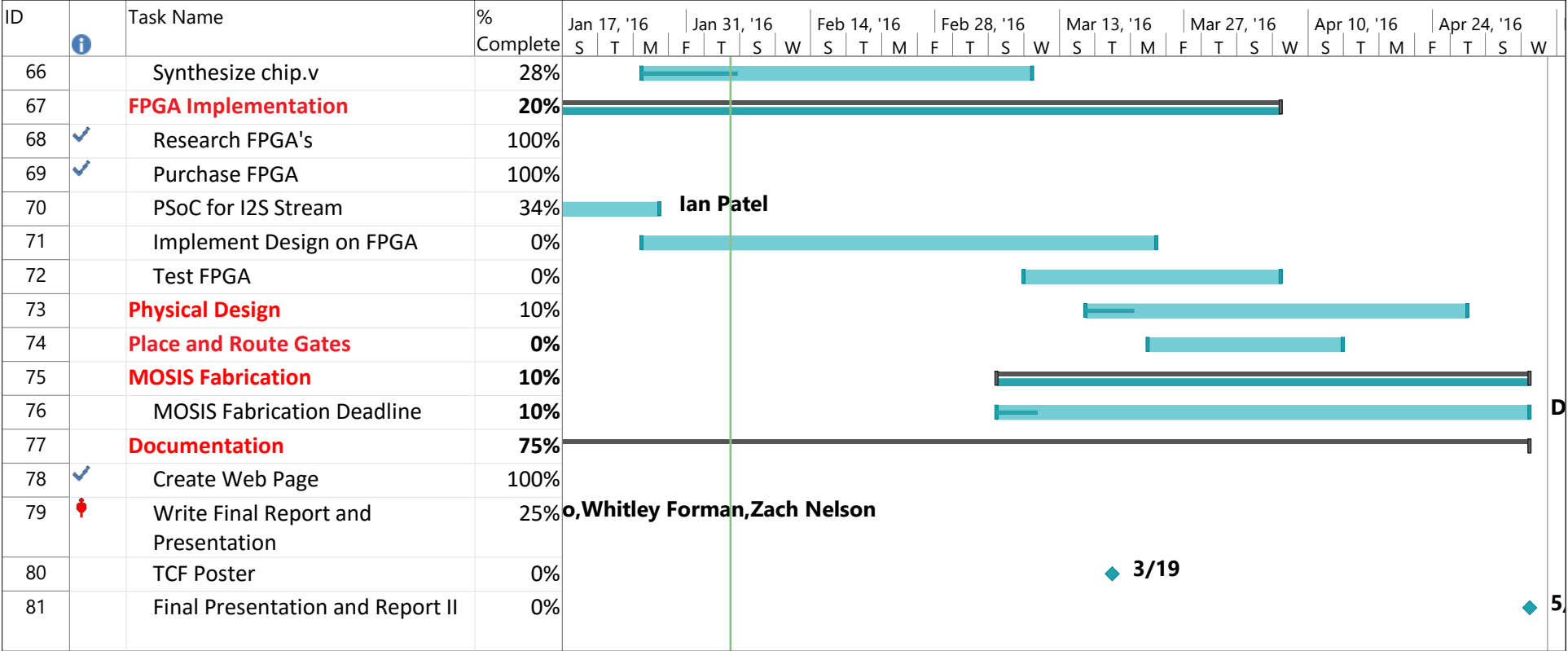


Inactive Milestone



Finish-only





Project: Chip Design
Date: Fri 2/5/16

Task

Split

Milestone

Summary

Project Summary

Inactive Task

Inactive Milestone

Inactive Summary

Manual Task

Duration-only

Manual Summary Rollup

Manual Summary

Start-only

Finish-only

External Tasks

External Milestone

Deadline

Progress

Manual Progress