






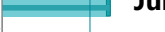
















ID		Task Mode	Task Name	Duration	Start	Aug 30, '15	Sep 13, '15	Sep 27, '15	Oct 11, '15	Oct 25, '15	Nov 8, '15	Nov 22, '15	Dec 6, '15
						W	S	T	M	F	T	S	W
1	✓	✈	<b>System Design</b>	<b>88 days</b>	<b>Thu 5/14/15</b>								
2	✓	✈	General Planning	26 days	Thu 5/14/15								
3	✓	✈	Complete I2S In Document	60 days	Thu 6/18/15								
4	✓	✈	Complete I2S Out Document	44 days	Sat 7/11/15								
5	✓	✈	Complete Filter Document	60 days	Thu 6/18/15								
6	✓	✈	Complete I2C Document	60 days	Thu 6/18/15								
7	✓	✈	Complete Register Document	60 days	Thu 6/18/15								
8	✓	✈	Finish System Design	0 days	Wed 9/9/15								
9	✓	✈	Document System in CORE	60 days	Thu 6/18/15								
10	✓	✈	RTS/RTR and XFC Protocols	7 days	Tue 9/8/15								
11	✓	✈	<b>Create a Detailed Schedule</b>	<b>9 days</b>	<b>Wed 9/2/15</b>								
12	✓	✈	<b>Install Microsoft Project</b>	<b>9 days</b>	<b>Wed 9/2/15</b>								
13	⚠	✈	<b>Install EDA Tools</b>	<b>26 days</b>	<b>Wed 9/2/15</b>								
14	✓	✈	Determine EDA Requiriments	4 days	Wed 9/2/15								
15	✓	✈	Install OS	13 days	Wed 9/9/15								
16	⚠	✈	Install EDA Tool	2 days	Mon 9/28/15								
17		✈	Create a Simple Test Case	5 days	Tue 9/29/15								
18	⚠	✈	Place and Route Test	2 days	Tue 10/6/15								
19		✈	Finish EDA Installation	0 days	Wed 10/7/15								
20	⚠	✈	<b>Create Web Page</b>	<b>10 days</b>	<b>Mon 9/21/15</b>								
21	⚠	✈	<b>RTL Design and Testing</b>	<b>38 days</b>	<b>Thu 9/10/15</b>								
22	⚠	✈	<b>Create i2si.v</b>	<b>38 days</b>	<b>Thu 9/10/15</b>								

Project: Chip Design  
Date: Sun 10/4/15

Task

Split

Milestone

Summary

Project Summary

Inactive Task

Inactive Milestone

Inactive Summary

Manual Task

Duration-only

Manual Summary Rollup

Manual Summary

Start-only

Finish-only

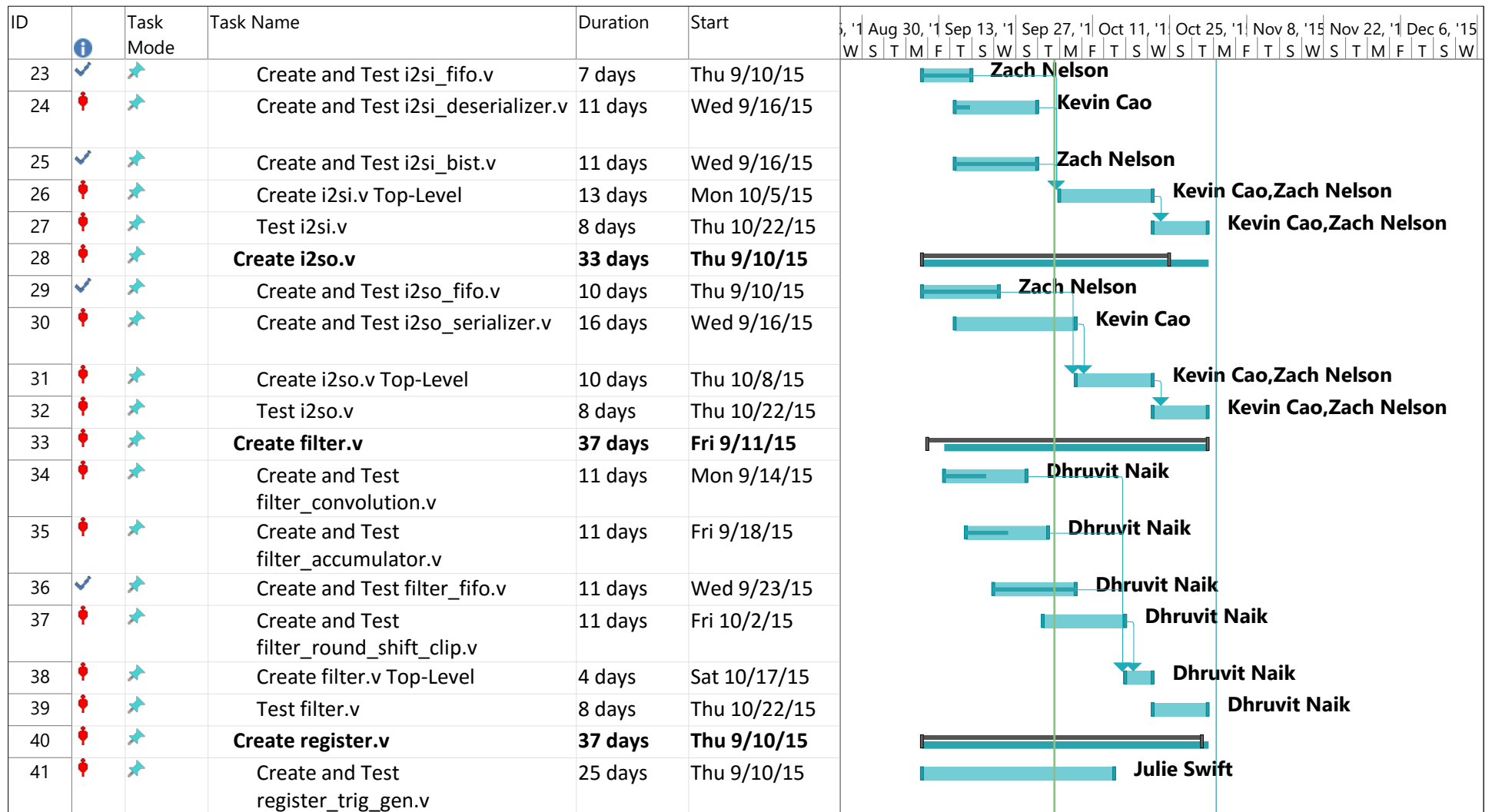
External Tasks

External Milestone

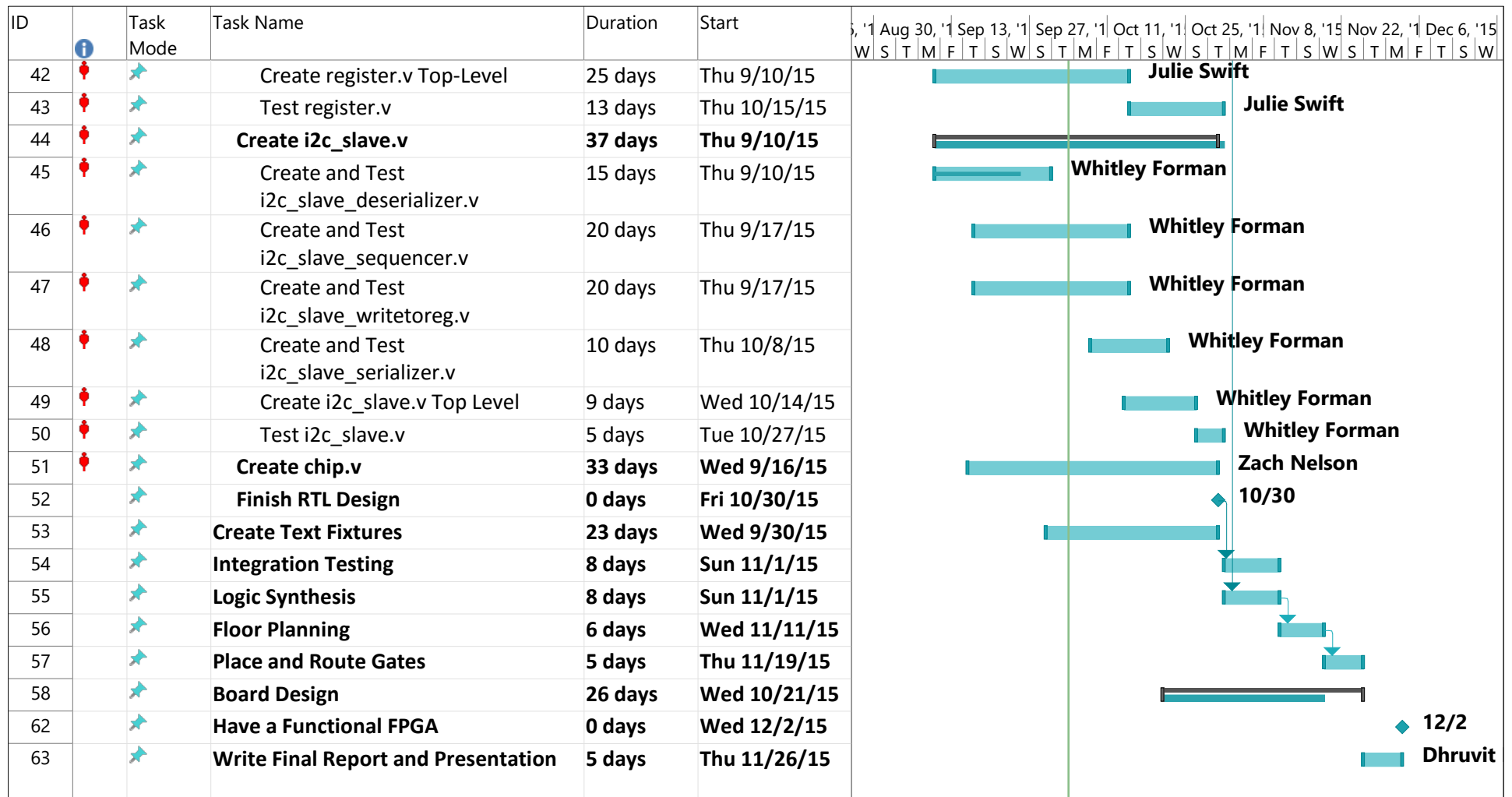
Deadline

Progress

Manual Progress



Project: Chip Design Date: Sun 10/4/15	Task		Inactive Summary		External Tasks	
	Split		Manual Task		External Milestone	
	Milestone		Duration-only		Deadline	
	Summary		Manual Summary Rollup		Progress	
	Project Summary		Manual Summary		Manual Progress	
	Inactive Task		Start-only			
	Inactive Milestone		Finish-only			



Project: Chip Design Date: Sun 10/4/15	Task		Inactive Summary		External Tasks	
	Split		Manual Task		External Milestone	
	Milestone		Duration-only		Deadline	
	Summary		Manual Summary Rollup		Progress	
	Project Summary		Manual Summary		Manual Progress	
	Inactive Task		Start-only			
	Inactive Milestone		Finish-only			

