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Vishay Siliconix

# Automotive N-Channel 60 V (D-S) 175 °C MOSFET



Marking code: 9Jxxx

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	60			
$R_{DS(on)}$ ( $\Omega$ ) at $V_{GS} = 1.5 \text{ V}$	0.245			
I <sub>D</sub> (A)	2			
Configuration	Single			
Package	SOT-23			

#### **FEATURES**

- TrenchFET® power MOSFET
- AEC-Q101 qualified
- 100 % Rg and UIS tested
- Typical ESD protection 800 V
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912





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N-Channel MOSFET	ģ

ORDERING INFORMATION	
Package	SOT-23
Lead (Pb)-free and halogen-free	SQ2364EES-T1 (for detailed order number please see <a href="https://www.vishay.com/doc?79771">www.vishay.com/doc?79771</a> )

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>C</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V <sub>DS</sub>	60	V	
Gate-source voltage		$V_{GS}$	± 8	V	
Continuous drain current	T <sub>C</sub> = 25 °C	- I <sub>D</sub>	2		
Continuous drain current	T <sub>C</sub> = 125 °C		1.3		
Continuous source current (diode conductio	I <sub>S</sub>	2	Α		
Pulsed drain current <sup>a</sup>	I <sub>DM</sub>	8			
Single pulse avalanche current	L = 0.1 mH	I <sub>AS</sub>	5		
Single pulse avalanche energy	L = 0.1 min	E <sub>AS</sub>	1.25	mJ	
Maximum power dissipation <sup>a</sup>	T <sub>C</sub> = 25 °C		3	W	
	T <sub>C</sub> = 125 °C	P <sub>D</sub>	1	] vv	
Operating junction and storage temperature	T <sub>J</sub> , T <sub>stg</sub>	-55 to +175	°C		

THERMAL RESISTANCE RATINGS				
PARAMETER		SYMBOL	LIMIT	UNIT
Junction-to-ambient	PCB mount <sup>b</sup>	$R_{thJA}$	166	°C/W
Junction-to-foot (drain)		$R_{thJF}$	50	C/VV

#### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%$
- b. When mounted on 1" square PCB (FR4 material)



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PARAMETER	SYMBOL	TES	TEST CONDITIONS		TYP.	MAX.	UNIT
Static		_		,	l .	•	
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub>	$V_{GS} = 0$ , $I_D = 250 \mu A$		-	-	V
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> =	$V_{DS} = V_{GS}, I_{D} = 250 \mu\text{A}$		0.6	1	v
Gate-source leakage		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 3 \text{ V}$		-	-	± 100	nA
Gate-source leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 8 V		-	-	± 5.5	
		V <sub>GS</sub> = 0 V	<sub>GS</sub> = 0 V V <sub>DS</sub> = 60 V -		-	1	Ī [
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>GS</sub> = 0 V	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 125 °C	-	-	50	- μA -
		$V_{GS} = 0 V$	V <sub>DS</sub> = 60 V, T <sub>J</sub> = 175 °C	-	-	150	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>GS</sub> = 4.5 V	$V_{DS} \ge 5 \text{ V}$	2	-	-	Α
		V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 2 A, T <sub>J</sub> = 25 °C	-	0.190	0.240	
Drain-source on-state resistance a	В	V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 2 A, T <sub>J</sub> = 125 °C	-	-	0.460	Ω
Drain-source on-state resistance ~	R <sub>DS(on)</sub>	V <sub>GS</sub> = 4.5 V	I <sub>D</sub> = 2 A, T <sub>J</sub> = 175 °C	-	-	0.600	
		V <sub>GS</sub> = 1.5 V	I <sub>D</sub> = 2 A	-	0.195	0.245	
Forward transconductance b	9 <sub>fs</sub>	V <sub>DS</sub>	V <sub>DS</sub> = -15 V, I <sub>D</sub> = 1 A		8.8	-	S
Dynamic <sup>b</sup>							
Input capacitance	C <sub>iss</sub>			-	263	330	pF
Output capacitance	C <sub>oss</sub>	$V_{GS} = 0 V$	$V_{DS} = 25 \text{ V}, f = 1 \text{ MHz}$	-	28	35	
Reverse transfer capacitance	C <sub>rss</sub>	7		-	15	19	
Total gate charge <sup>c</sup>	Qg			-	2	2.5	
Gate-source charge <sup>c</sup>	Q <sub>gs</sub>	$V_{GS} = 4.5 \text{ V}$	$V_{DS} = 30 \text{ V}, I_{D} = 1.5 \text{ A}$	-	0.3	-	nC
Gate-drain charge c	Q <sub>gd</sub>	7			0.6	-	1 '
Gate resistance	Rg	f = 1 MHz		2.5	4.1	6.6	Ω
Turn-on delay time <sup>c</sup>	t <sub>d(on)</sub>			-	6	7.2	
Rise time <sup>c</sup>	t <sub>r</sub>	$V_{DD}=30$ V, $R_L=15$ $\Omega$ $I_D\cong 1.5$ A, $V_{GEN}=10$ V, $R_g=1$ $\Omega$		-	11	14	
Turn-off delay time <sup>c</sup>	t <sub>d(off)</sub>			-	26	32	ns -
Fall time <sup>c</sup>	t <sub>f</sub>			-	13	16	
Source-Drain Diode Ratings and Char	racteristics <sup>b</sup>						
Pulsed current <sup>a</sup>	I <sub>SM</sub>					8	Α
Forward voltage	$V_{SD}$	I <sub>F</sub> = 2 A, V <sub>GS</sub> = 0		-	0.8	1.2	V
						•	•

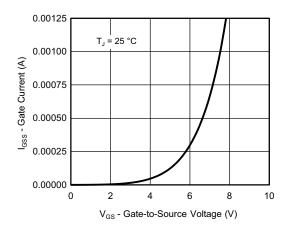
#### Notes

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

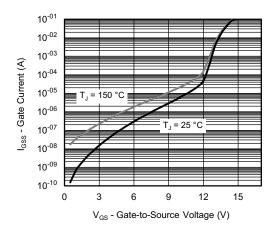
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



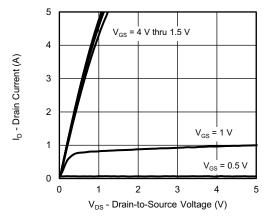
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



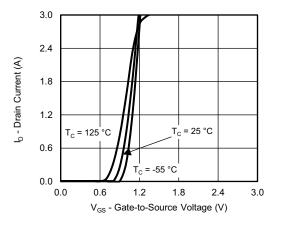
Gate Current vs. Gate-Source Voltage



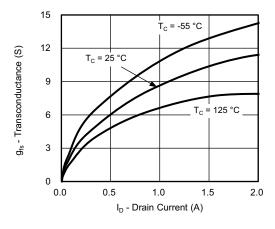
Gate Current vs. Gate-Source Voltage



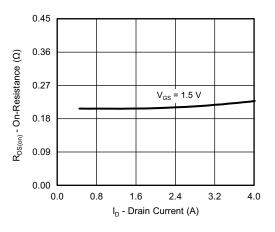
**Output Characteristics** 



**Transfer Characteristics** 



Transconductance

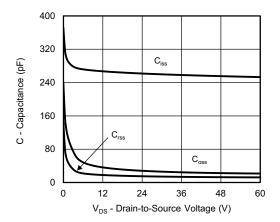


On-Resistance vs. Drain Current

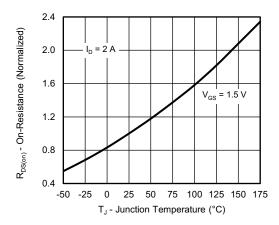
For technical questions, contact: automostech



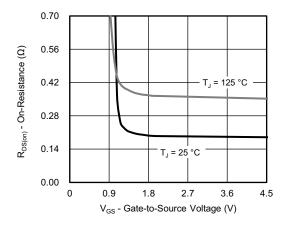
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



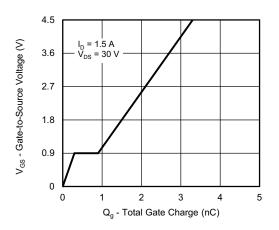
#### Capacitance



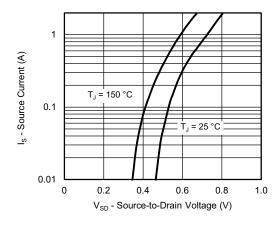
On-Resistance vs. Junction Temperature



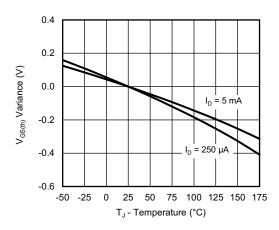
On-Resistance vs. Gate-Source Voltage



**Gate Charge** 



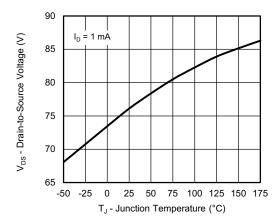
Source-Drain Diode Forward Voltage



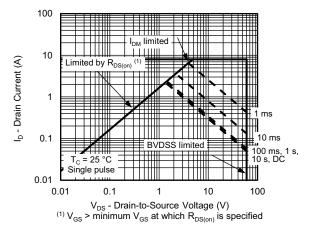
**Threshold Voltage** 



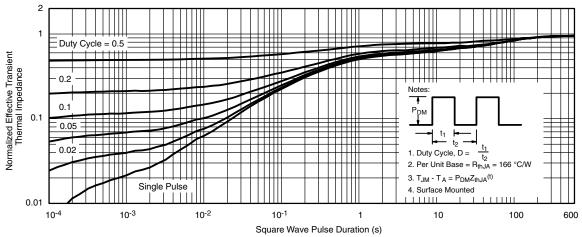
### **TYPICAL CHARACTERISTICS** (T<sub>A</sub> = 25 °C, unless otherwise noted)



Drain-Source Breakdown vs. Junction Temperature



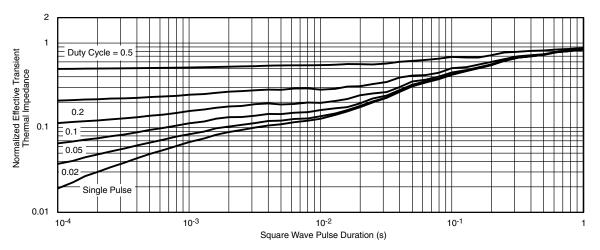
**Safe Operating Area** 



Normalized Thermal Transient Impedance, Junction-to-Ambient



### THERMAL RATINGS (T<sub>A</sub> = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

#### Note

- The characteristics shown in the two graphs
  - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
  - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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## SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	IETERS	INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A <sub>1</sub>	0.01	0.10	0.0004	0.004	
A <sub>2</sub>	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E <sub>1</sub>	1.20	1.40	0.047	0.055	
е	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90	BSC	0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L <sub>1</sub>	0.64 Ref		0.025	i Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
ECN: S-03946-Rev. K. 09-	Jul-01				

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



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