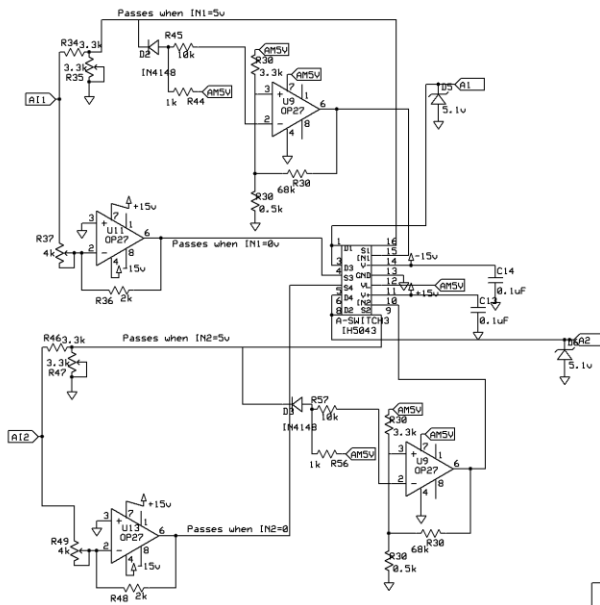
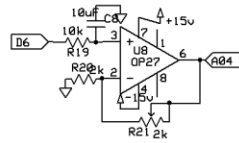


Wired correctly on the PCB 4/20

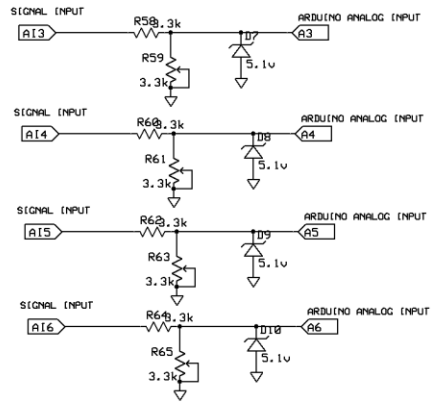


<-rename components to avoid netlist problems (linking with pcb)

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Schematic Name		
Designer's name	Rev 1.0 3/16/2016	Page # or name

NO OP AMP NEEDED
BC NO LOADING EFFECT BY A-INPUT OF MEGA



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Company Name		
Schematic Name		
Designer's name	Rev 1.0 3/16/2016	Page # or name