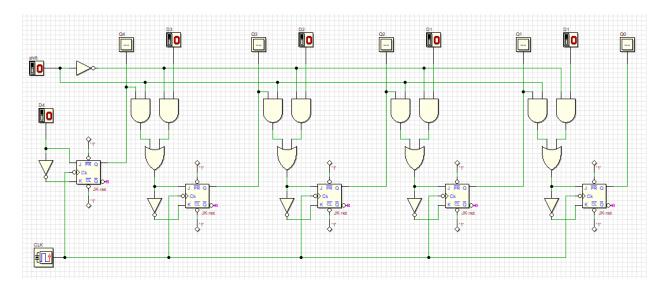
Nama : Septian Bagus Jumantoro

NRP : 3221600039

Kelas : 1D4 Teknik Komputer B

Latihan Soal Shift Register

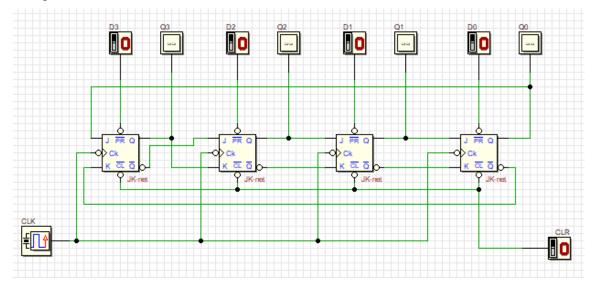
1. Rangkaian



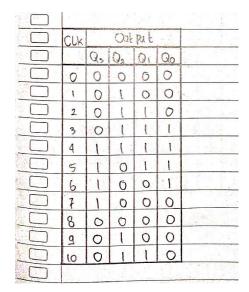
Tabel Kebenaran

51.		2,00						
	clk	Q ₄	Qz	Q,	Qi	Qo	Serial Output	
	0	1	0	0	1	0		
	1	0	1	0	0	1	0	
	. 2	0	0.	1 -	0	0	10	- 4
	3	0	0	0	1	0	010	100
	9	0	0	0	0	1	0010-	
	5	0	0	0	0	0	10010	
	6	0	0	0	0	0	01001	
	7	0	0	0	0	0	00100	
	8	0	0	0	0	0	00010	
	9	0	0	0	0	0	00001	
	lo	0	0	0	0	0	00000	281
	-						1	4.1

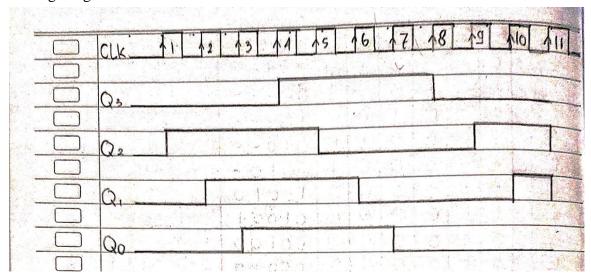
2. Rangkaian



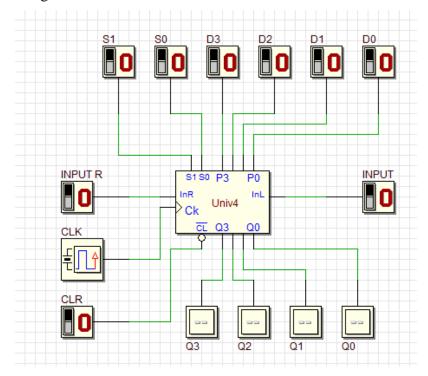
Tabel Kebenaran



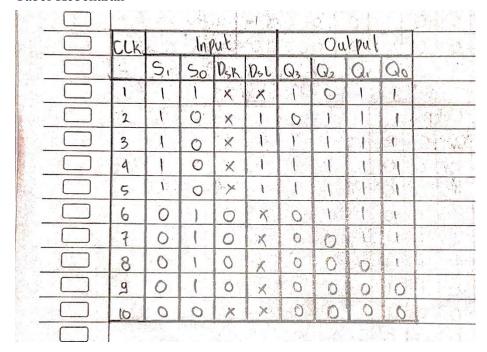
Timing Diagram



3. Rangkaian



Tabel Kebenaran



Timing Diagram

CLK_	14:	3	A	54	64.13	1. 181	197	lov
- Q				100		No.	W. Now, or	
					-			
] Q2								
						-		
$\supset Q_1$								1
							7. 1.9	
) Q0_		1-34	5 - 1					
7 / 20		and the same	37 (7.8.5)		2	- 1 1 1 m		1 3