

Control frequency, Time lag

Mechatronics, Lecture 10

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- Quantization
- Discretization
- Update rate
- Time lag

Digital systems are characterized, among other things, by:

- quantization;
- discretization;
- update frequency;
- time lag.

Digital systems encode values using rational numbers, specifically - decimal fractions with a finite number of digits. This implies a minimum possible difference between non-equal numbers Δx .

For example, if we use 8 bits to encode voltage values from 0 to 12 Volts, then a minimal difference between non-equal voltages is 0.047 V. We can call this number (0.047 V) *resolution* of this encoding.

Quantization is a process of encoding a sequence of numbers given range of values and resolution.

Mathematically, quantization is defined as:

$$\text{quant}(x) = \delta \cdot \text{trunc} \left(\frac{x}{\delta} \right) \quad (1)$$

where $\text{trunc}(\cdot)$ is a truncation to the whole number, $\delta = \frac{1}{n}$ is a resolution, and n is the number of unique values $\text{quant}(x)$ can have.

For binary encodings $n = 2^{\text{bit}}$; e.g. 8 bit gives us $n = 256$ and 16 bit gives us $n = 65536$. Keep in mind that "number of bits used to encode a number" isn't the same as amount of memory allocated to store a variable.

Number of bits available to encode a value usually provides a good description of a sensor; also sensors usually have comparatively low resolution. On the other hand, variables stored in microprocessor memory are better described by their type: "int", "float", "double" etc. These usually have very high resolution, and less notable quantization effects.

Quantization potentially affects the following values in mechatronic system:

- Desired output voltage of a controller, generated with pulse-width modulation (PWM). Resolution is limited by PWM frequency.
- Sensor readings. Resolution is limited by digital sensor resolution.
- State estimates. Resolution is limited by variable resolution used by the microcontroller.

We do not have specific tools widely used to deal with quantization problems.

However, usually the effects of quantization are small enough to ignore on the level of a single mechatronic module (given a sensor with high-enough resolution, which is typical).

DISCRETIZATION, 1

Continuous-time (CT) signal $f(t)$ is defined for any time t .

Discrete-time (DT) signal is defined only on a sequence of discrete moments of time t_1, t_2, \dots . Often these moments of times are equally spaced, and are characterized by time step Δt .

Discretization of a CT signal $f(t)$ is a process of producing a discrete sequence which is in some sense equivalent to the original CT signal. For example, it can be done by evaluating the CT signal on a sequence of time moments t_1, t_2, \dots , producing discrete sequence $f(t_1), f(t_2), \dots$.

An "inverse" operation to discretization is *interpolation*. It provides a CT signal $g(t)$, in some sense equivalent to the given DT sequence f_1, f_2, \dots ; There are many ways to do it:

- Zero-order hold: given $t_i \leq t < t_{i+1}$ we assume $g(t) = f_i$.
- Linear model: we assume that $g(t)$ changes from f_i to f_{i+1} linearly on the time interval $t_i \leq t < t_{i+1}$.
- Polynomial model;
- other.

We have plenty of control tools designed specifically for DT systems: there are discrete-time stability criterion on eigenvalues (allowing to use pole placement for control design), discrete-time LQR, discrete-time Kalman filter, etc.

By itself, discretization does not pose significant problems.

We can compute how often a discrete signal changes per unit of time. Assume that a DT signal is defined over time sequence $0, \Delta t, 2\Delta t, 3\Delta t, \dots$; then we can define update rate of this signal:

$$\eta = \frac{1}{\Delta t} \quad (2)$$

It is clear that update rate depends on Δt . For digital systems with a single processor, both values depend on how much time it takes the processor to finish computations and transmit the data.

It is a common assumption that higher update rates improve the performance of the control system. This motivates breaking control system into subsystems with higher update rates of lower level subsystems.

For example, it is common to design current control for motors. This allows high update rates, possible due to the use of local processing (as opposed to centralized computations done by the robot's main computer, these types of loops are often processed entirely by the built-in electronics of the motor).

Time lag is an effect related to the time it takes both process and transmit a signal. It means that there is a time difference between the moment of time the information is acquired and the moment of time the information is available.

Assume that control signal u is a function of the sensor readings $y(t)$: $u = h(y)$. Without time lag, the function can be written as:

$$u(t) = h(y(t)) \quad (3)$$

With time lag τ , it becomes:

$$u(t) = h(y(t - \tau)) \quad (4)$$

TIME LAG - CT EXAMPLE

Consider a simple example: a linear system $\dot{x} = Ax + Bu$, where full state of the system is being measured $y = x$ and control law is linear $u = -Kx$. Then in the absence of time lag the closed-loop system becomes:

$$\dot{x} = (A - BK)x \quad (5)$$

which is stable if $A - BK$ is Hurwitz.

Assume that time lag affects the control law $u(t) = -Kx(t - \tau)$. Then the closed-loop system becomes:

$$\dot{x}(t) = Ax(t) - BKx(t - \tau) \quad (6)$$

For this system we do not have a simple stability criterion.

TIME LAG - DT EXAMPLE, 1

Let consider a discrete system $x_{i+1} = Ax_i + Bu_i$ with discretization step Δt . As before we assume that we measure the full state. Then, the control law and closed loop system can be:

$$u_i = -Kx_i \quad (7)$$

$$x_{i+1} = (A - BK)x_i \quad (8)$$

which is stable if $A - BK$ is Schur.

Assume that time lag is equal to desensitization step: $\tau = \Delta t$. Then the control law and the closed loop system become:

$$u_i = -Kx_{i-1} \quad (9)$$

$$x_{i+1} = Ax_i - BKx_{i-1} \quad (10)$$

We can analyze the stability of the closed-loop system

$$x_{i+1} = Ax_i - BKx_{i-1}:$$

$$\begin{bmatrix} x_{i+1} \\ x_i \end{bmatrix} = \begin{bmatrix} A & -BK \\ I & 0 \end{bmatrix} \begin{bmatrix} x_i \\ x_{i-1} \end{bmatrix} \quad (11)$$

where the matrix needs to be Schur.

As we can see, the time lag effectively increased the conditionality of the state of the system.

If the time lag is equal to 3 desensitization steps: $\tau = 3\Delta t$, the closed-loop system is $x_{i+1} = Ax_i - BKx_{i-3}$, which can be re-written as:

$$\begin{bmatrix} x_{i+1} \\ x_i \\ x_{i-1} \\ x_{i-2} \end{bmatrix} = \begin{bmatrix} A & 0 & 0 & -BK \\ I & 0 & 0 & 0 \\ 0 & I & 0 & 0 \\ 0 & 0 & I & 0 \end{bmatrix} \begin{bmatrix} x_i \\ x_{i-1} \\ x_{i-2} \\ x_{i-3} \end{bmatrix} \quad (12)$$

We can see that the size of dimensions of the state grows together with the time lag.

TIME LAG - EFFECTS

Time lag can have a noticeable negative effect on the performance of the control system. It is often associated with communication time - the length of time it takes to transmit information through communication interfaces.

A well-designed motor can minimize lag, by avoiding slow communication protocols, limiting the volume of transmitted data, etc.

Lecture slides are available via Github, links are on Moodle

You can help improve these slides at:
github.com/SergeiSa/Mechatronics-2023

