Logo 8 Modbus-Implementierungstabe					F01 Read Coils		F02 Read Discr	oto Innut	E03 Boad Hold	ina Pogistor	End Road Inni	it Ponistor
F01 Read Coils F05 Write Single Coil F15 Write Multiple Coils F15 Write Multiple Coils				F05 Write Single Coil		FOZ Read Discrete Iliput		F03 Read Holding Register F06 Write Single Register		F04 Read Input Register		
						F15 Write Multiple Coils				F16 Write Multiple Register		
							Discrete				Input	
Coil	Lo	go	Coil	Logo			Input	Logo	Register	Logo	Register	
	1	V0.0			8257	M1	1	ľ	1	VW0		1 <b>Al1</b>
	2	V0.1	8194	Q2	8258	M2	2	l2	2 2	VW2		2 Al2
	3	V0.2	8195	Q3	8259	M3	3	I.	3	VW4		3 Al3
	4	V0.3	8196	Q4	8260	M4	4	]4	<mark>!</mark>			4 <mark>Al4</mark>
	5	V0.4	8197	Q5	8261	M5	5	I	423	VW844		5 Al5
	6	V0.5	8198	Q6	8262	M6	6	16	424	VW846	)	6 Al6
	7	V0.6	8199	Q7	8263	M7	7	17	425	VW848		7 <b>AI7</b>
	8	V0.7	8200	Q8	8264	M8	8	18	3			8 <mark>AI8</mark>
			8201	Q9			9	19	513	AQ1		
68	801	V850.0	8202	Q10	8313	M57	10	I10	514	AQ2		
68	802	V850.1	8203	Q11	8314	M58	11	I111	515	AQ3		
68	803	V850.2	8204	Q12	8315	M59	12	112	516	AQ4		
68	804	V850.3	8205	Q13	8316	M60	13	113	517	AQ5		
68	805	V850.4	8206	Q14	8317	M61	14	l14	<del>1</del> 518	AQ6		
68	806	V850.5	8207	Q15	8318	M62	15	118	519	AQ7		
68	807	V850.6	8208	Q16	8319	M63	16		520	AQ8		
680	808	V850.7	8209	Q17	8320	M64	17	I17	7			
			8210	Q18			18	I18	529	AM1		
			8211	Q19			19	I19	530	AM2		
		<u> </u>	8212	Q20			20	120	531	AM3		
							21	l2 <sup>*</sup>				
							22	122	590	AM62		
							23	123	591	AM63		
							24		592	AM64		