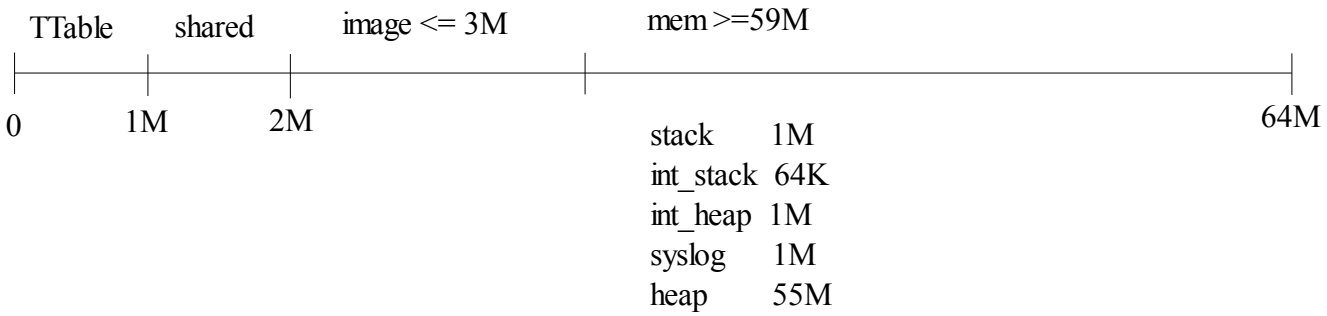
IXDP465.

Memory

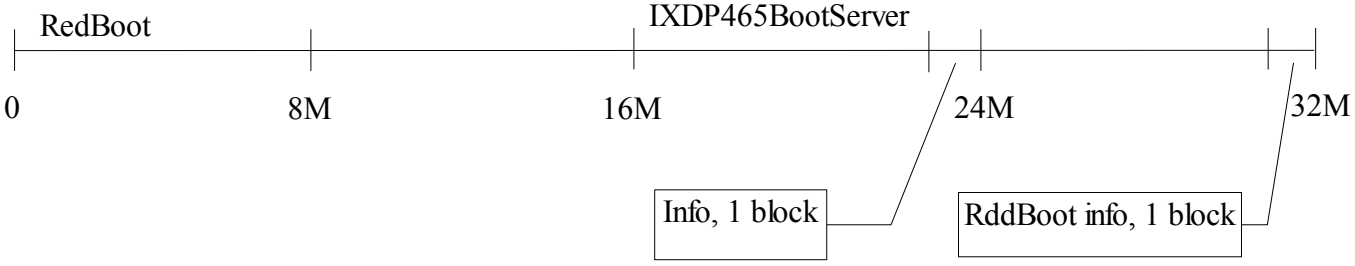


CS0 flash

base = 5000 0000 h

size = 32M

block size = 2 0000 h



CNFG

CNFG0 = 00 FF FF 7F h

CNFG1 = 80 00 01 00 h

CS0 = BFFF 0242 h

CS2 = BFFF 3C03 h

FPGA

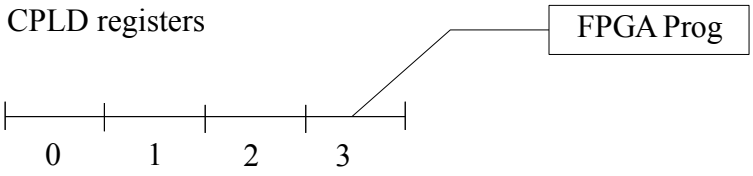
GPIO8	83 output	PCI_INTD
GPIO9	82 output	PCI_INTC
GPIO10	81 output	PCI_INTB
GPIO11	80 output	PCI_INTA

PCI_INTA	Slot0 INTA
PCI_INTB	Slot1 INTB
PCI_INTC	Slot2 INTC
PCI_INTD	Slot3 INTD

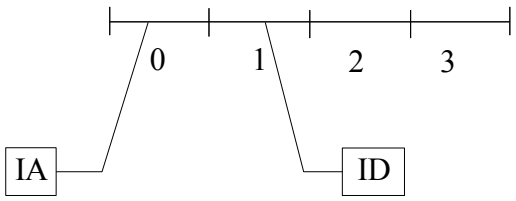
CS2

base = 5400 0000 h

5400 0000 h CPLD registers



5410 0000 h GPIO FPGA registers



Boot