.include "m8535def.inc"

ser r16
out ddra,r16
out portb,r16

fin:

in r17,pinb
ldi r18,\$30
add r17,r18
cpi r17,\$3A
brpl salto
out porta,r17
rjmp fin

salto:

ldi r18,\$07
add r17,r18
out porta,r17
rjmp fin

Name	Add Value	Bits
PORTA	0x1B 0x30	
DDRA	0x1A 0xFF	
≥ PINA	0x19 0x30	
PORTB	0x18 0xFF	
DDRB	0x17	
≋ PINB	0x16 0x00	

Name	Add Value	Bits
₹ PORTA	0x1B 0x31	
DDRA	0x1A 0xFF	
№ PINA	0x19 0x31	
₹ PORTB	0x18 0xFF	
DDRB	0x17	
№ PINB	0x16 0x01	

Name	Add Value	Bits
₽ORTA	0x1B 0x39	
▼ DDRA	0x1A 0xFF	
2 PINA	0x19 0x39	
₽ORTB	0x18 0xFF	
▼ DDRB	0x17	
2 PINB	0x16 0x09	

Name	Add Value	Bits
PORTA	0x1B 0x41	
DDRA	0x1A 0xFF	
₹ PINA	0x19 0x41	
PORTB	0x18 0xFF	
◆ DDRB	0x17	
2 PINB	0x16 0x0A	

Name	Add Value	Bits
₹ PORTA	0x1B 0x46	
DDRA	0x1A 0xFF	
🔁 PINA	0x19 0x46	
🔁 PORTB	0x18 0xFF	
DDRB	0x17	
😤 PINB	0x16 0x0F	

Name	Add	Value	Bits
₽ORTA	0x1B	0x56	
DDRA	0x1A	0xFF	
≥ PINA	0x19	0x56	
₽ORTB	0x18	0xFF	
▼ DDRB	0x17		
2 PINB	0x16	0x1F	