

P3449-B01

Jetson Nano Developer Kit Carrier Board

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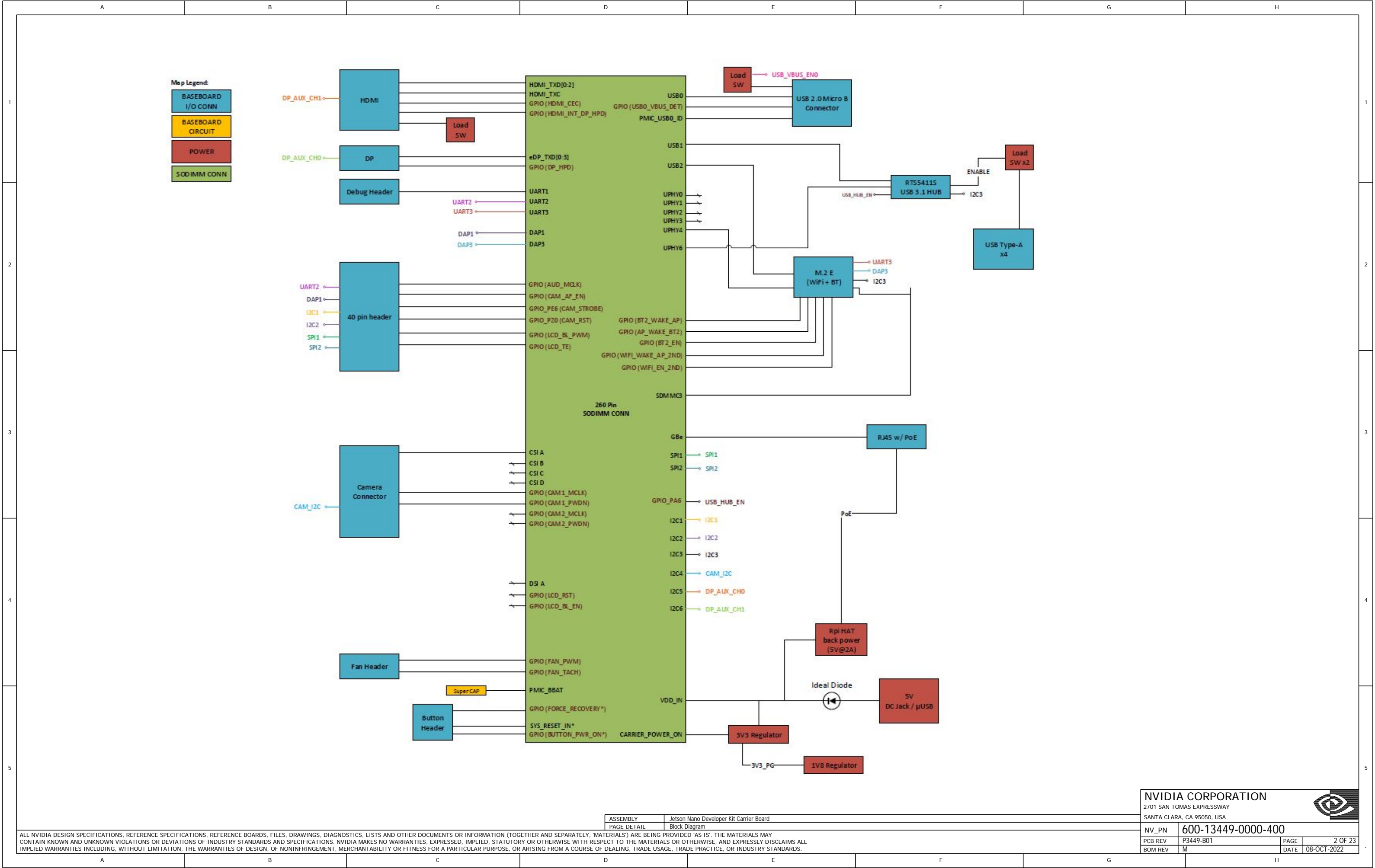
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| Page | Description |
|------|-------------|
|------|-------------|

P3449 I2C Port Assignments

| Slave Device | BUS:Device address |
|---------------------|--------------------|
| BOARDID EEPROM | I2C Gen 3 : 7'57h |
| RTS5411S USB3.1 HUB | CAM I2C : 7'6Ah |
| M.2 KEY-E WIFI/BT | I2C Gen 3 : TBD |

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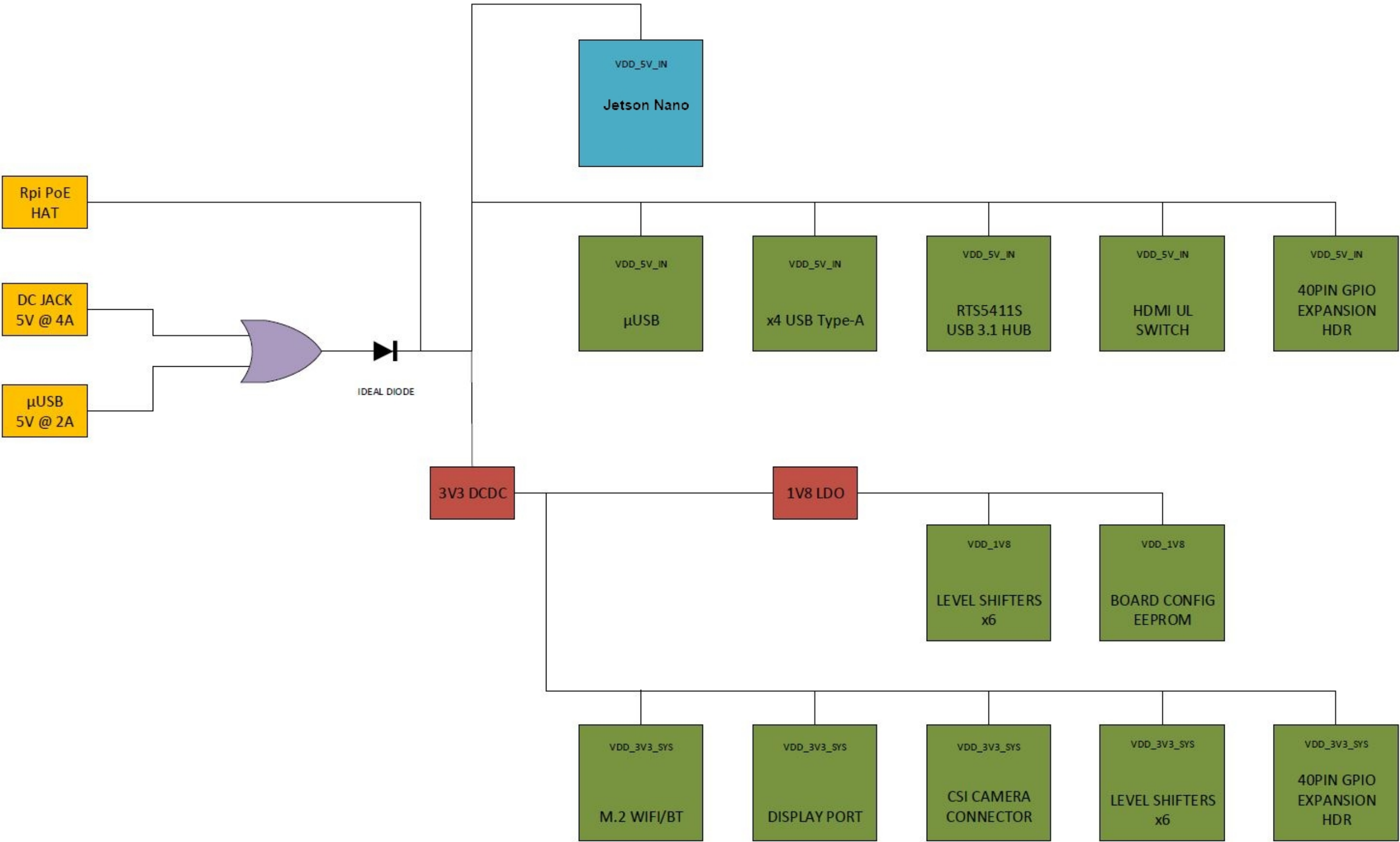
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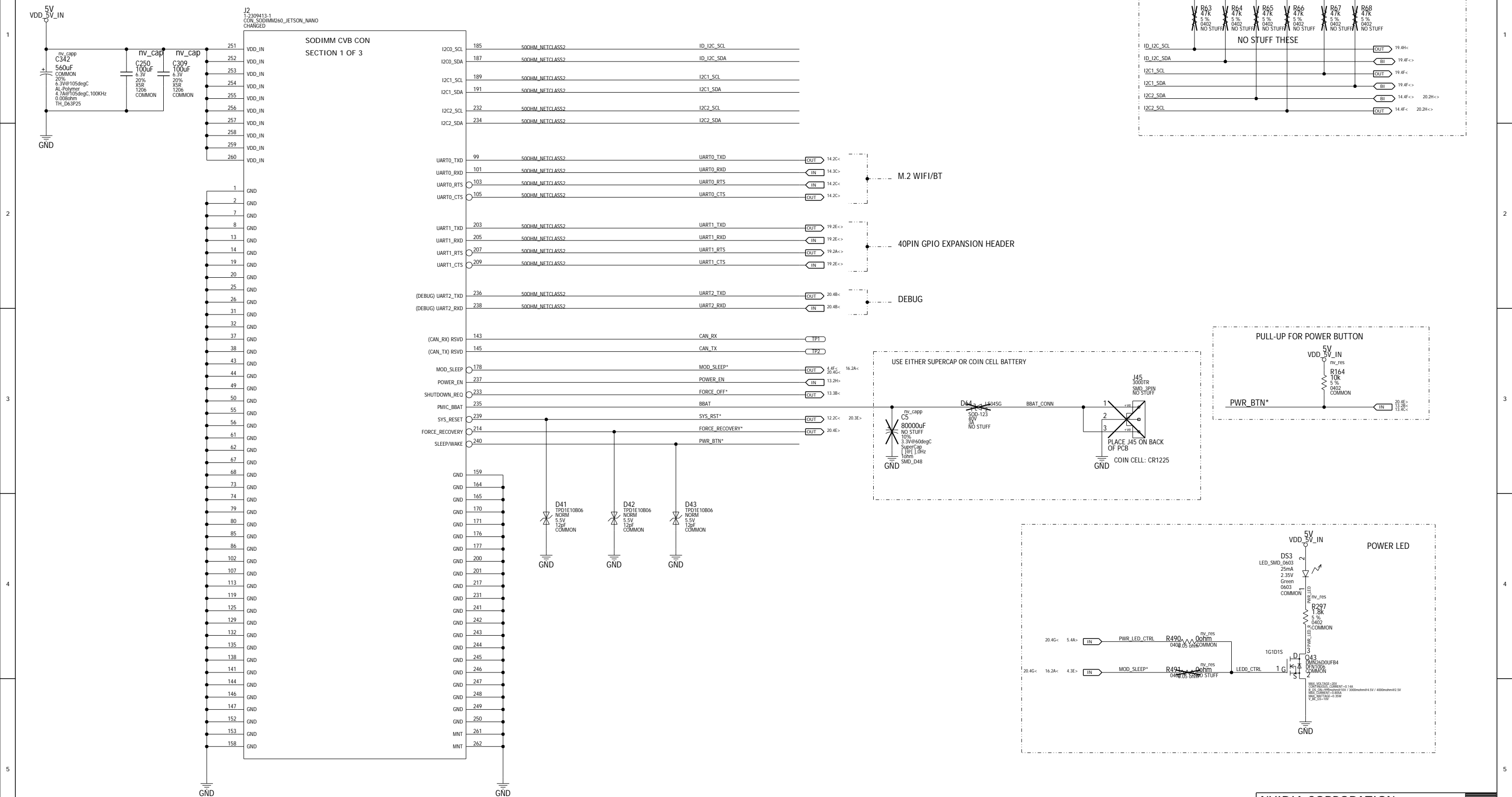
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Power Tree



UART, I2C, CAN and GPIOs

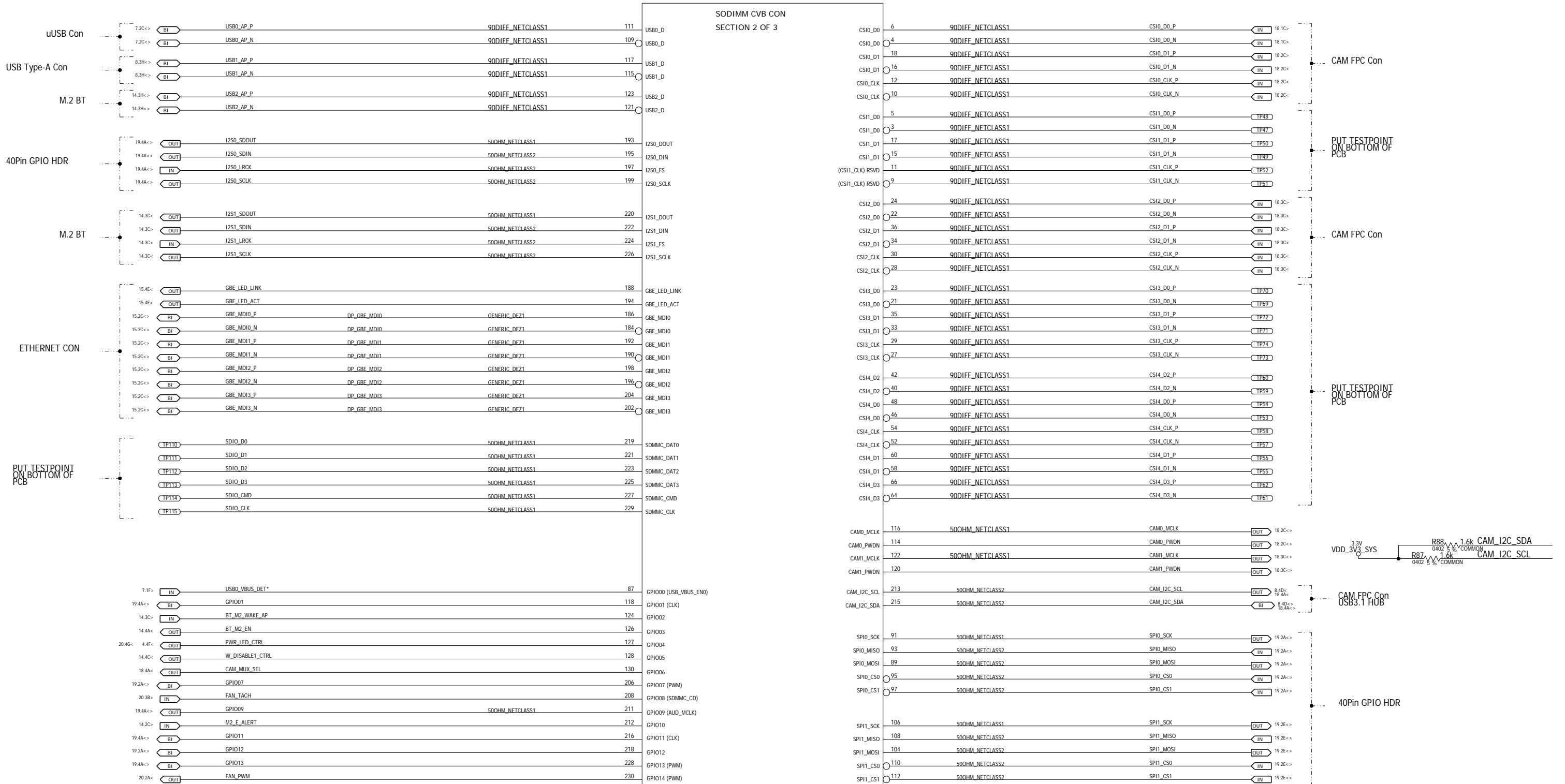


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| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | SODIMM connector 1/3 |

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GPIOs, USB2.0, I2S, SPI, GBe and CSI



| | |
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| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | SODIMM connector 2/3 |

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HDMI, DP, DSI, USBSS and PCIe

PUT TESTPOINT
ON BOTTOM OF
PCB

DP Con

HDMI Con

J2
1-2309413-1
CON_SODIMM260_JETSON_NANO
CHANGED

SODIMM CVB CON
SECTION 3 OF 3

M.2 WIFI

LOOP BACK

USB Type-A Con (USBSS)

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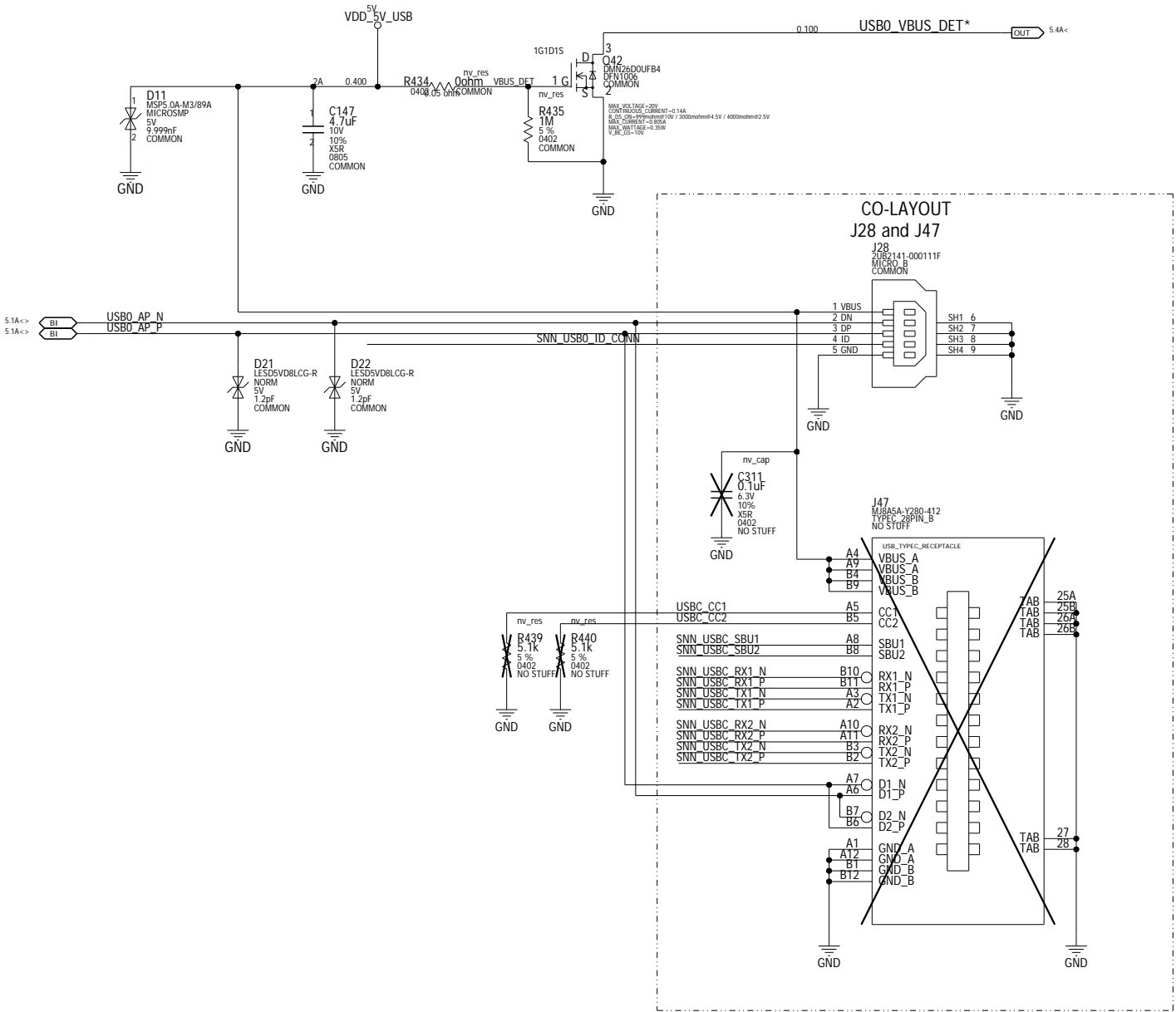
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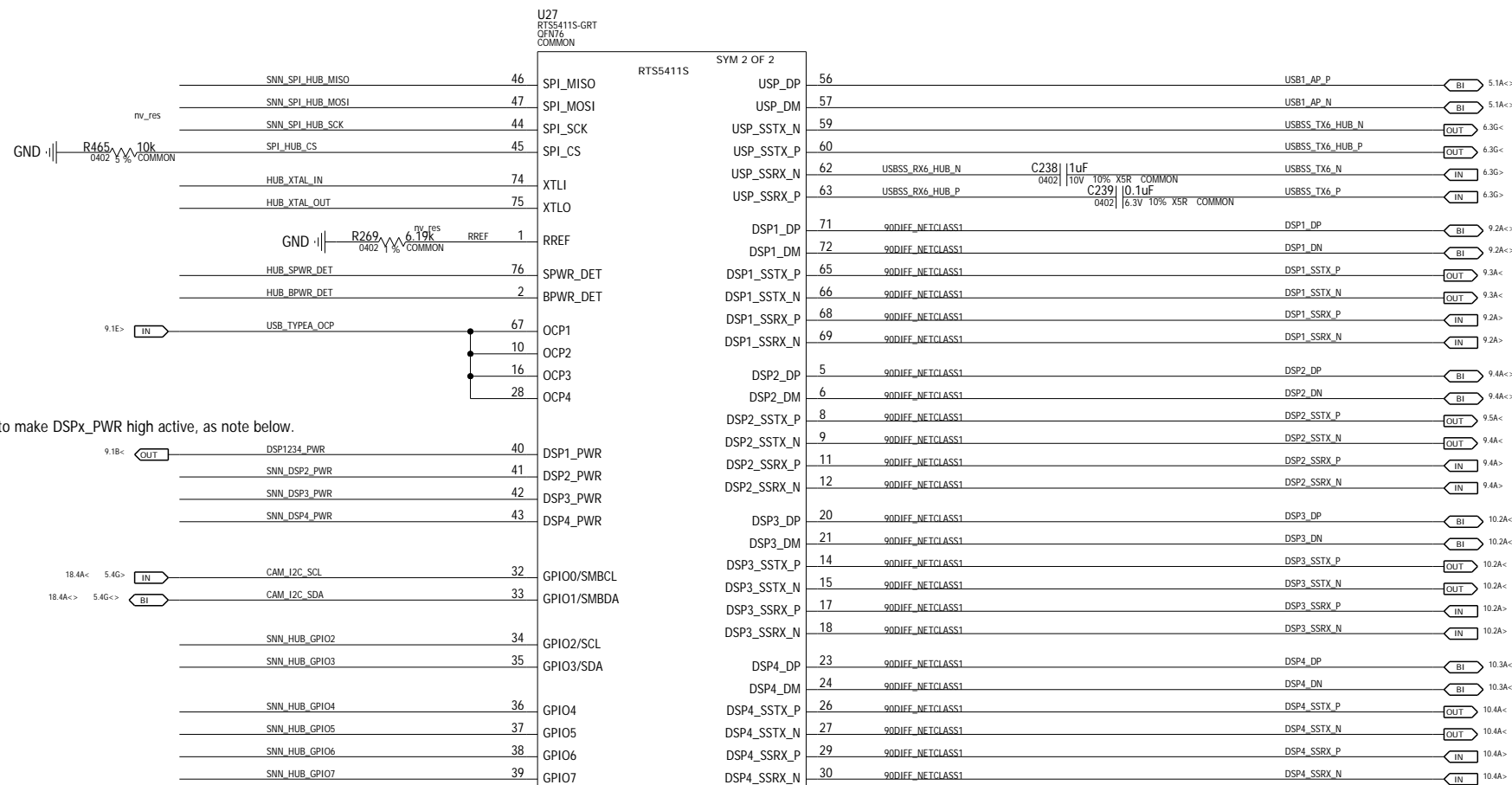
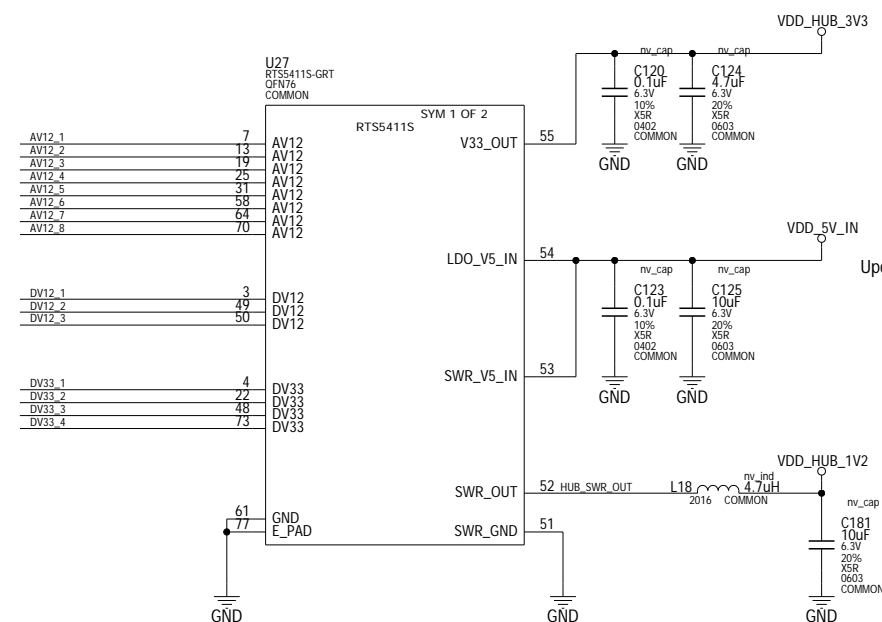
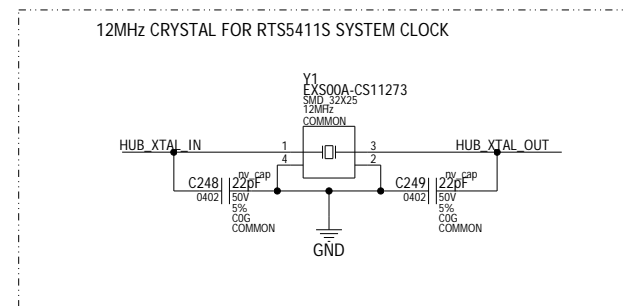
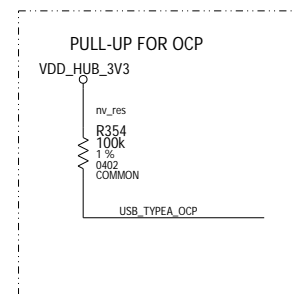
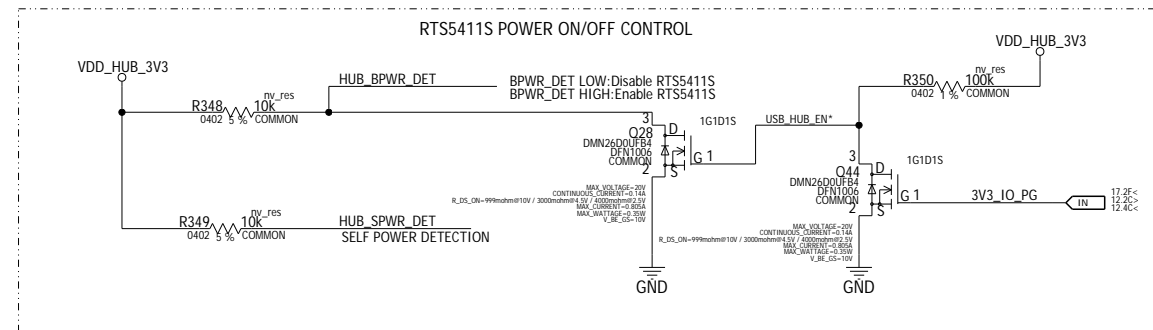
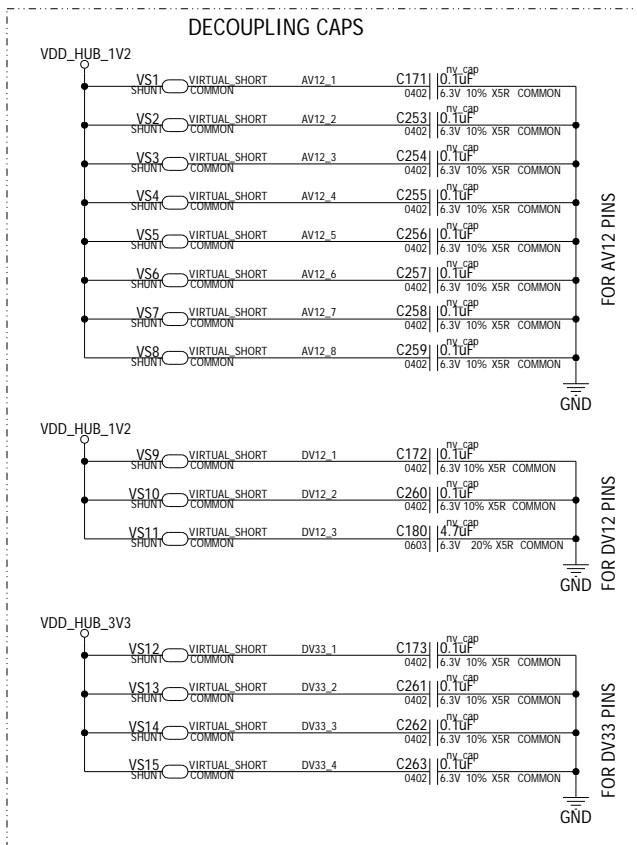
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MICRO AB USB 2.0 CONNECTOR



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USB3.1 HUB x4 PORTS



I2C slave addrs: 7'1101010b = 7'h6A

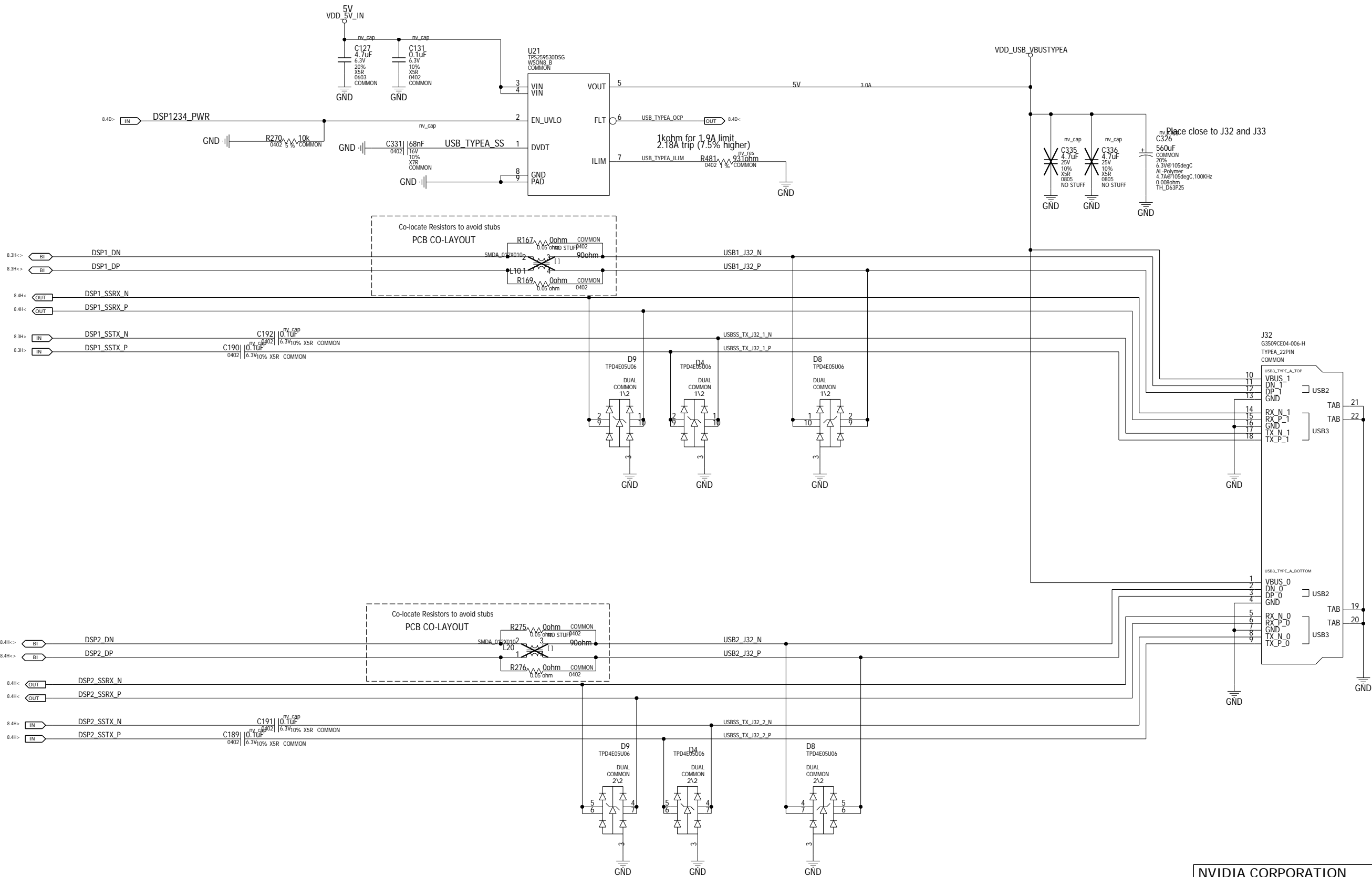
SPI Flash was removed from B01, customize RTS5411 internal eFuse (DSP1/2/3/4_PWR High active, disable charging feature, SSC valid) with Realtek tool.

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| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | USB3.1 x4 HUB |

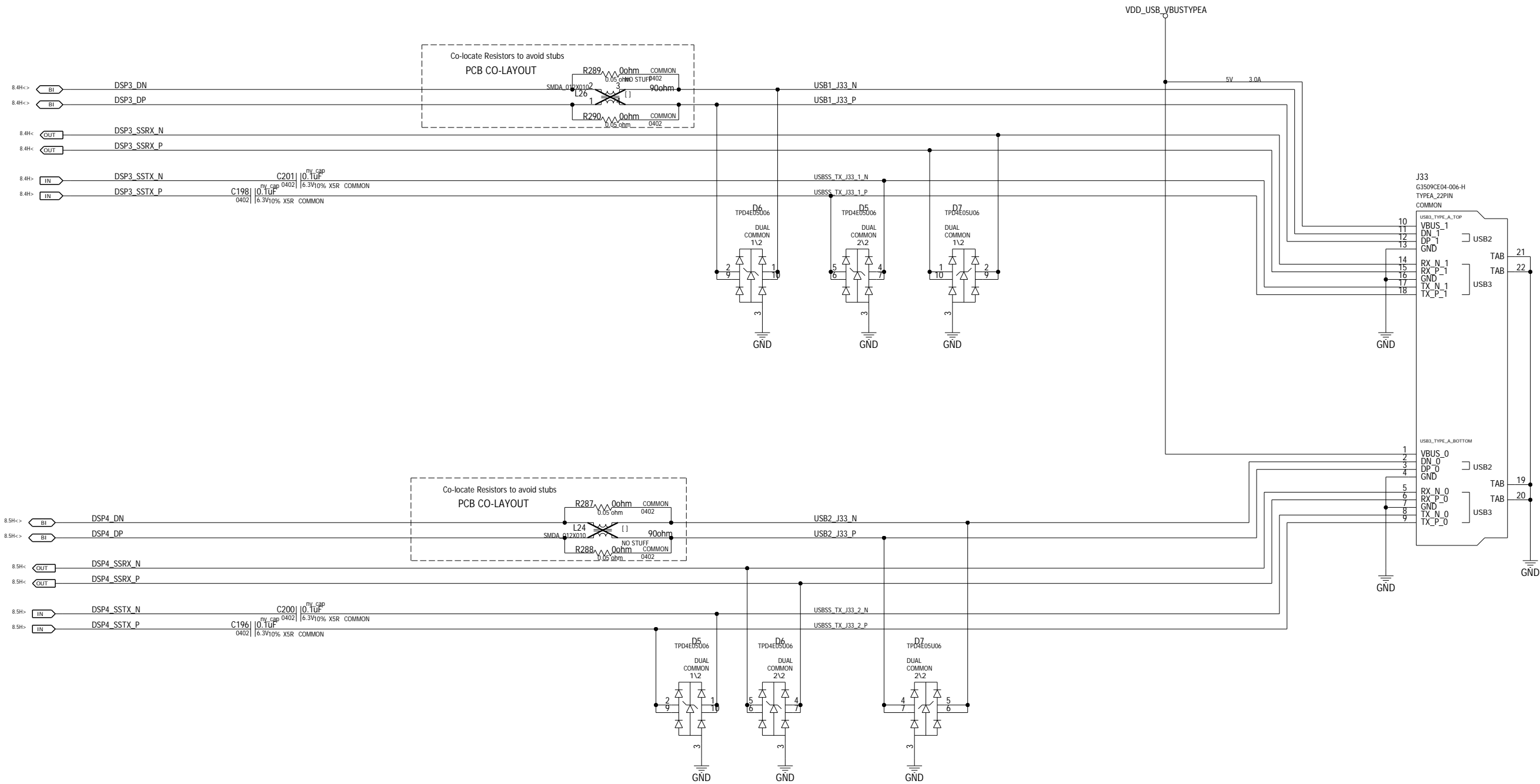
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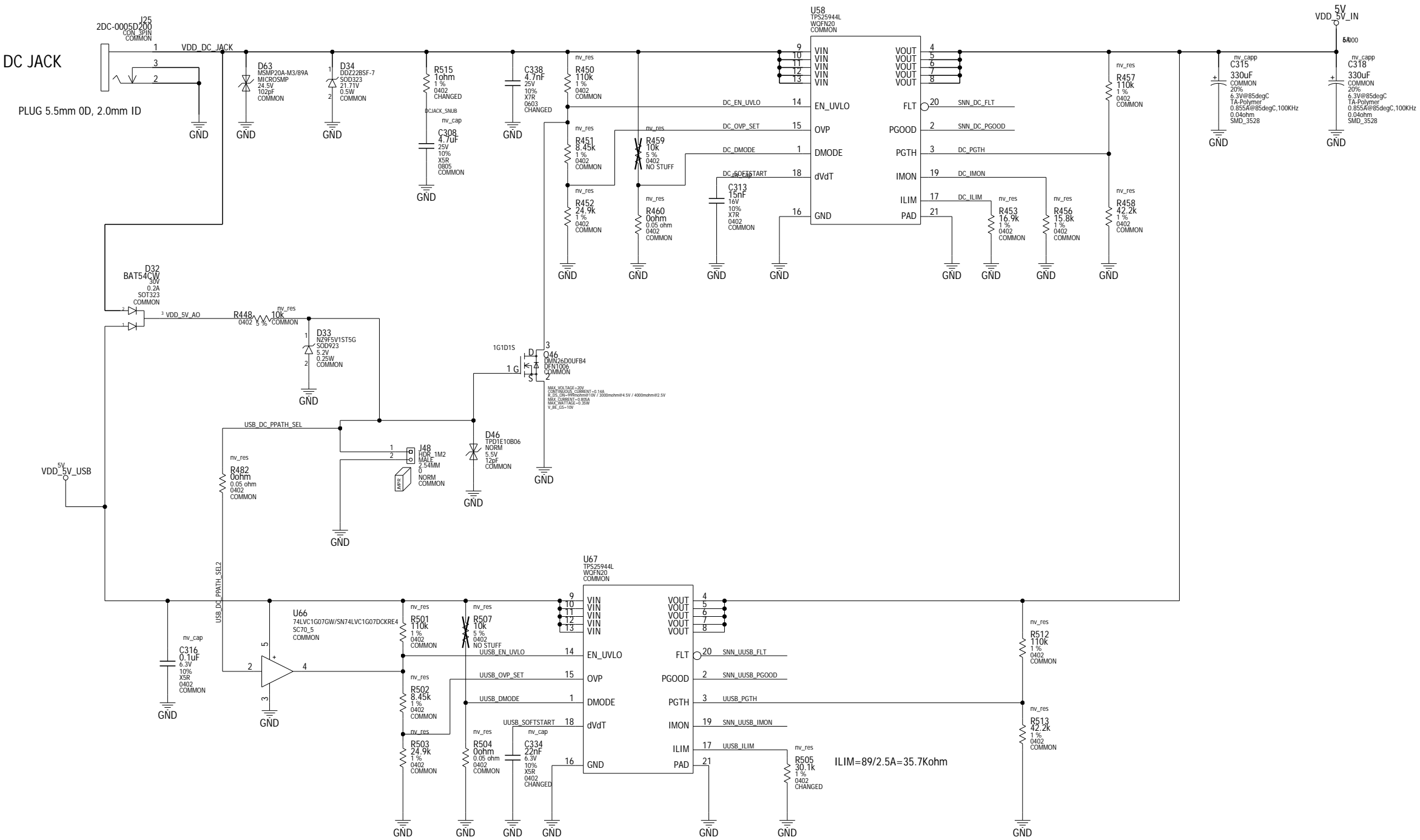
USB 3.0 TYPE A x2 CONNECTOR (1 OF 2)



USB 3.0 TYPE A x2 CONNECTOR (2 OF 2)

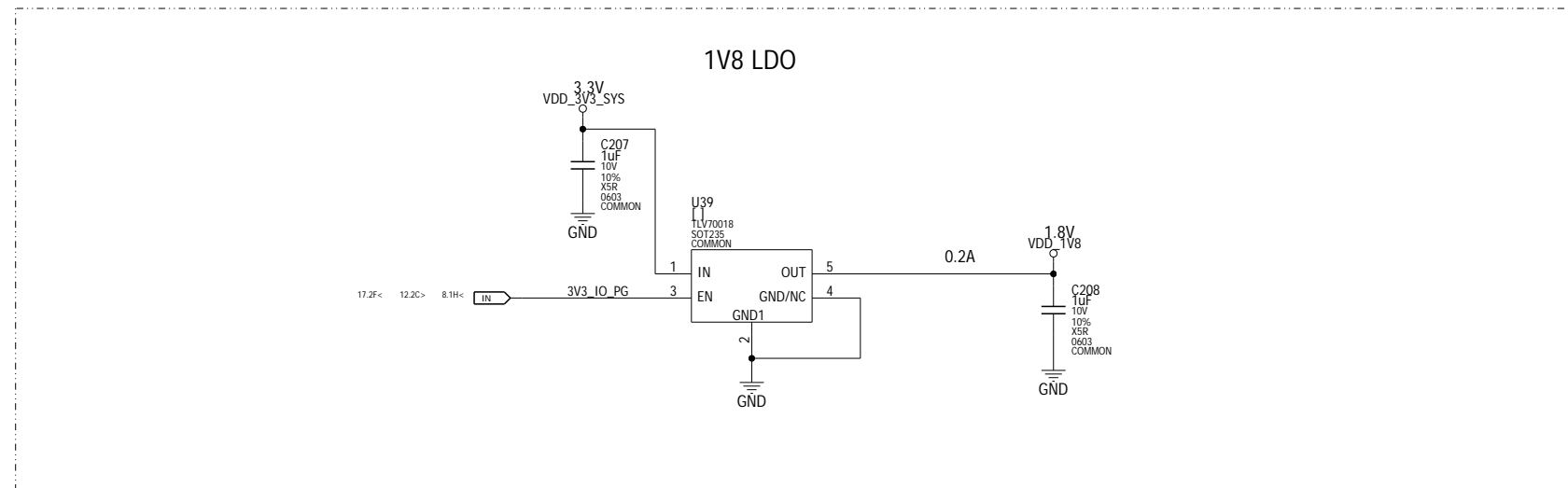
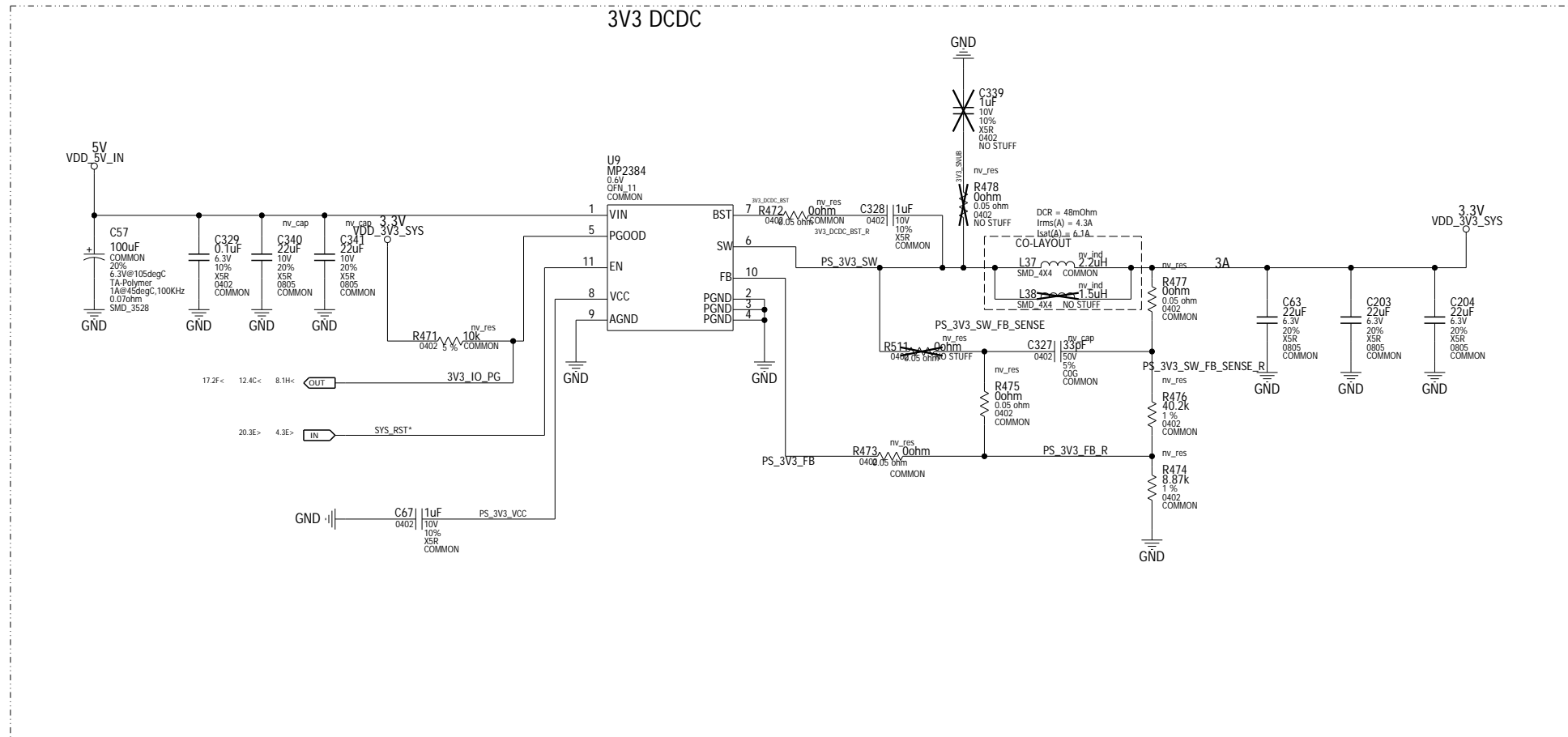


DC JACK AND uUSB POWER SOURCE SELECTION



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3V3 & 1V8 POWER SUPPLIES



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| PAGE DETAIL | 3V3 DCDC and 1V8 LDO |

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The schematic diagram illustrates the power management system for the iECU. It shows the power flow from a 5V input (VDD_5V_IN) through various components including resistors (R444, R442, R441, R375, R373, R365), capacitors (C277, C278, C280), and logic gates (U49, U50, U51, U55). The system includes a 10s button press for force shutdown (PWR_BTN*) and a truth table for the LATCH_SET, LATCH_RESET, and POWER_EN_Q signals.

10s BUTTON PRESS FOR FORCE SHUTDOWN

TRUTH TABLE

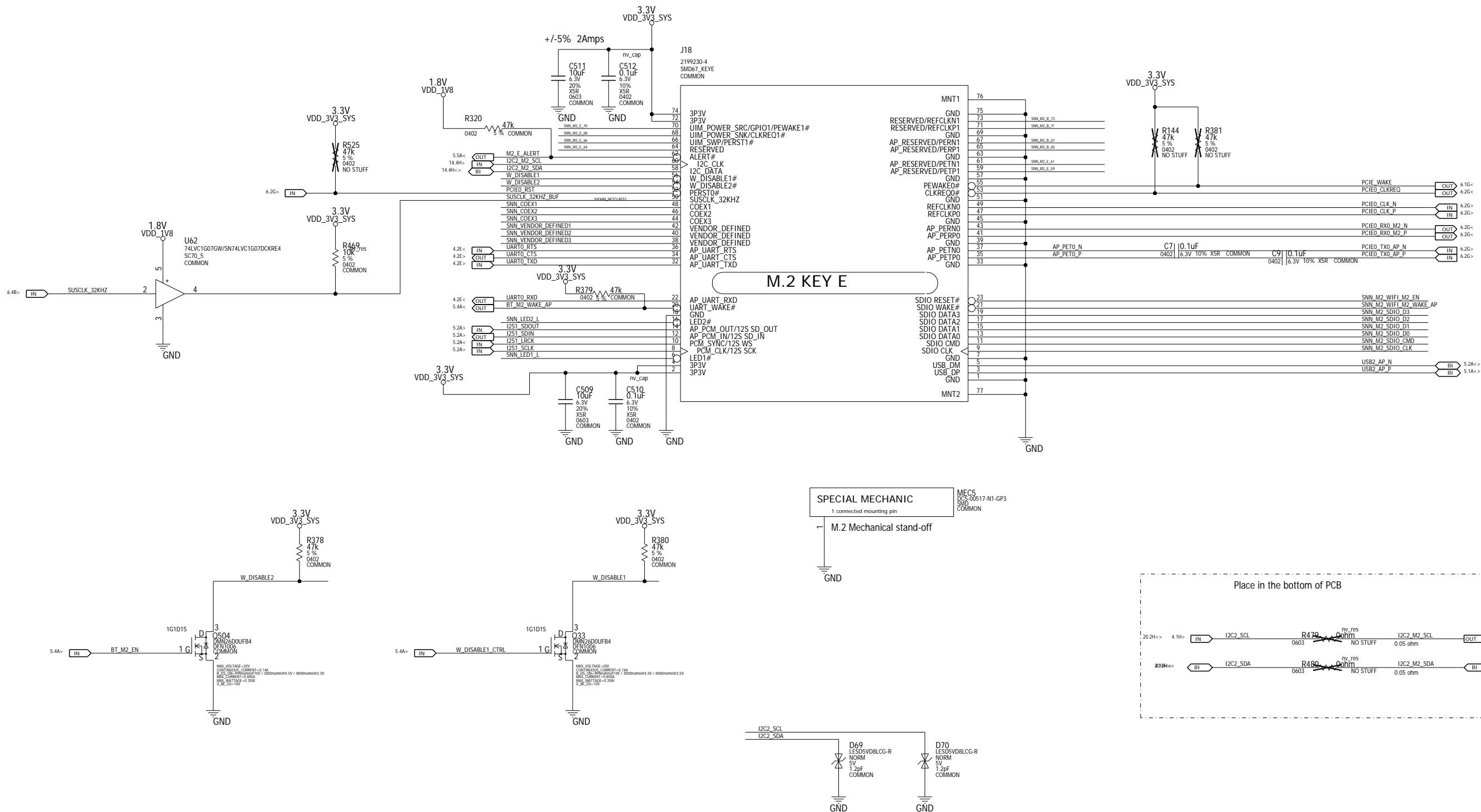
| | LATCH_SET-> | LATCH_RESET-> | POWER_EN_Q-> |
|---|-------------|---------------|--------------|
| S | 1 | 1 | 0 |
| R | 0 | 1 | 1 |
| Q | 0 | 0 | 1 or 0 |

TRUTH TABLE

Timing diagram showing the relationship between LATCH_SET, LATCH_RESET, and POWER_EN_Q signals. The diagram is divided into six time intervals: Set, No Change, Reset, No Change, Invalid, and Unknown States.

| Signal | Set | No Change | Reset | No Change | Invalid | Unknown States |
|---------------------|-----|-----------|-------|-----------|---------|----------------|
| LATCH_SET (S) | 1 | 1 | 0 | 0 | 1 | 1 |
| LATCH_RESET (R) | 0 | 0 | 1 | 1 | 0 | 0 |
| POWER_EN_Q (Q) | 0 | 0 | 1 | 1 | 1 or 0 | ? |
| POWER_EN_Q-bar (Q̄) | 1 | 1 | 0 | 0 | 0 or 1 | ? |

M.2 KEY E



| | |
|-------------|---|
| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | M.2 Key-E |

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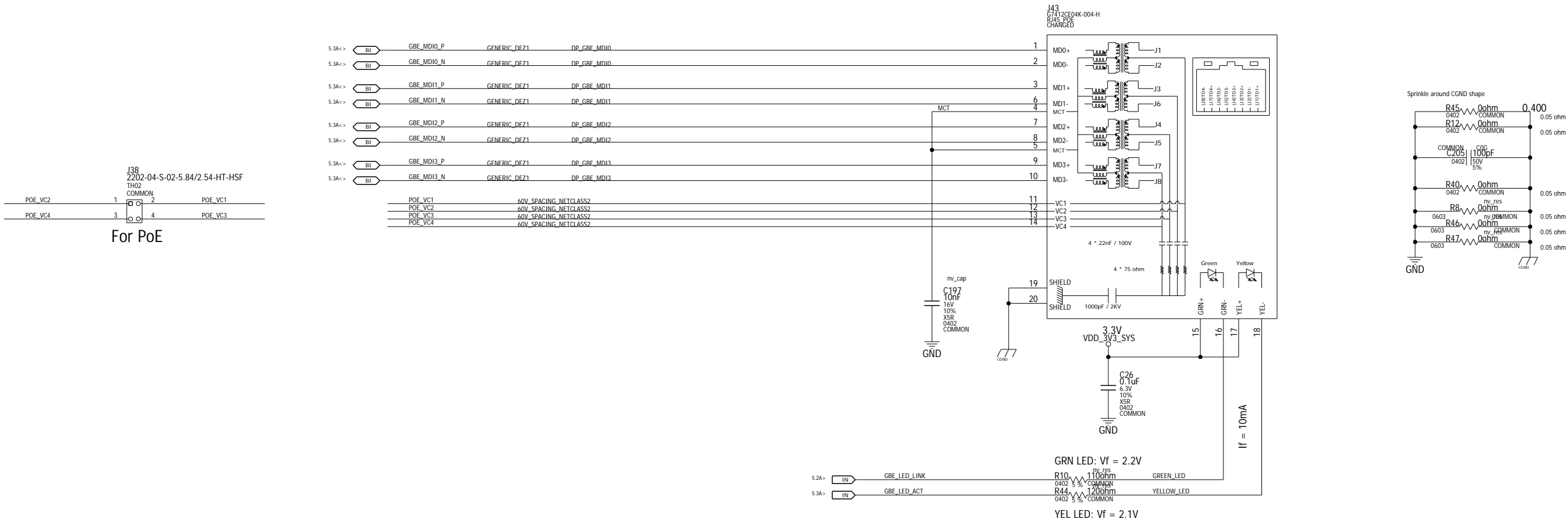
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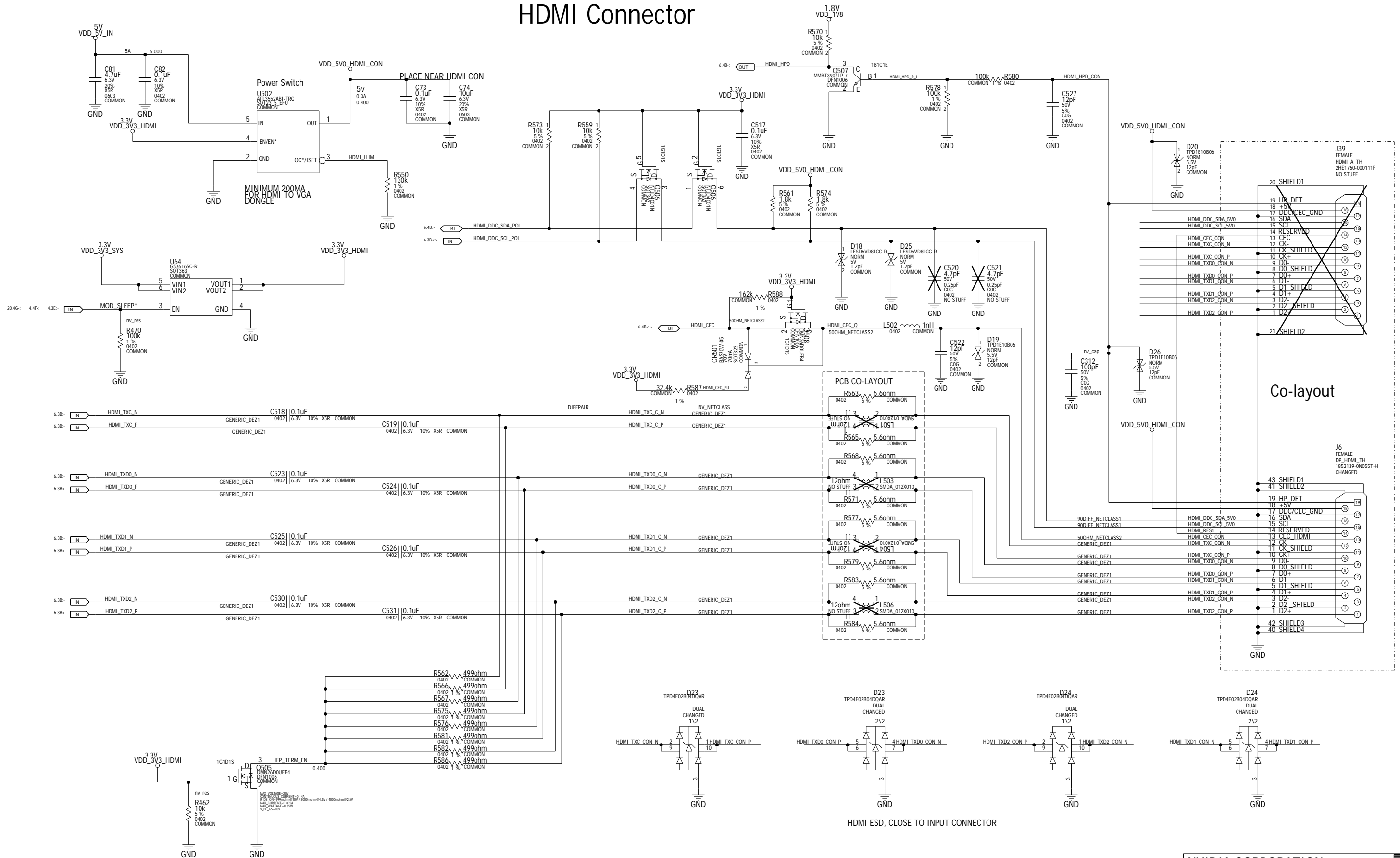
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Gigabit Ethernet with PoE



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HDMI Connector



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| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | HDMI |

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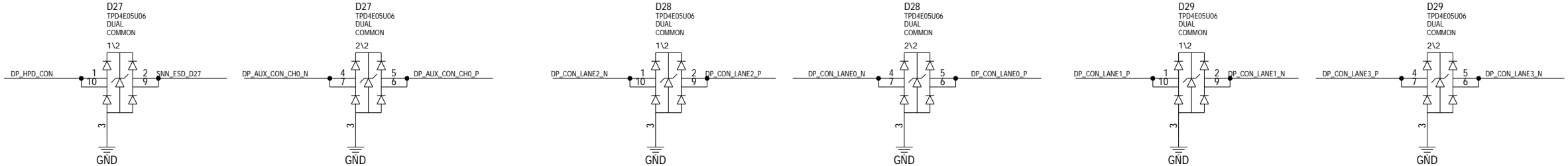
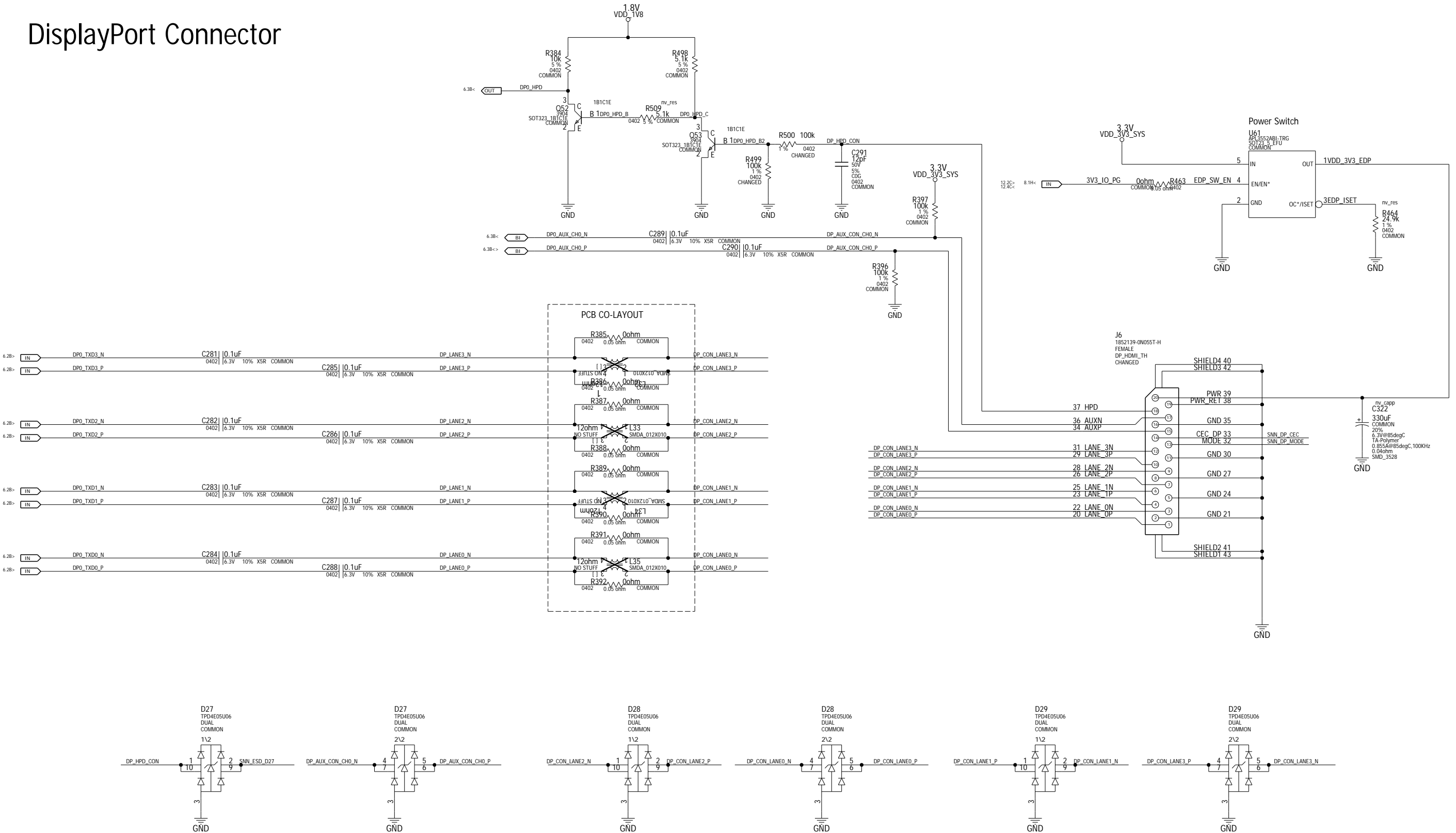
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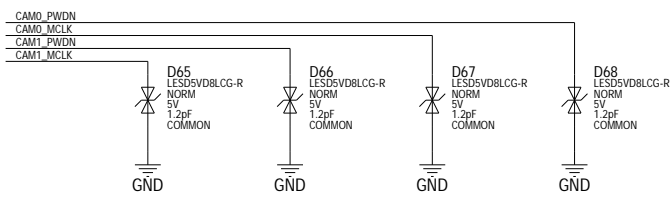
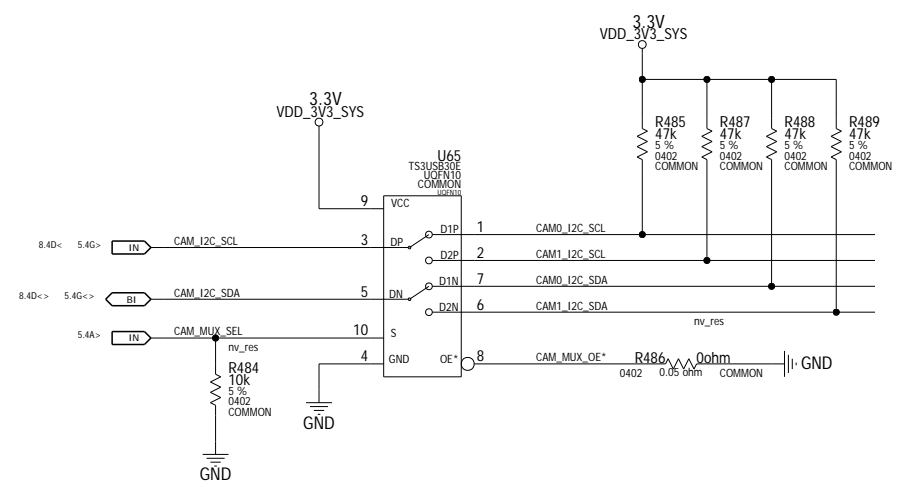
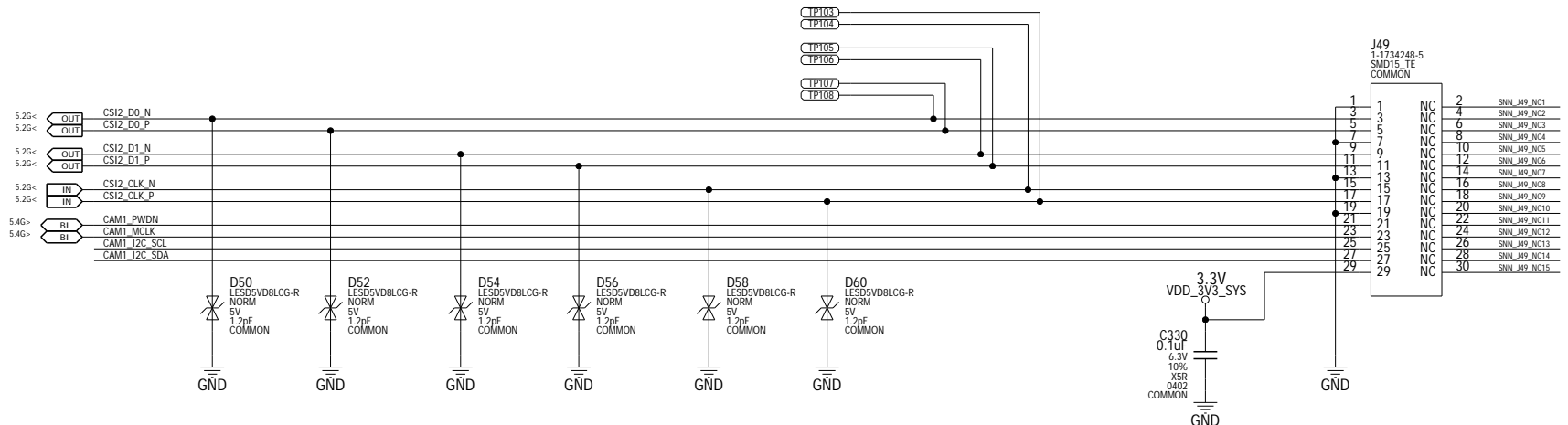
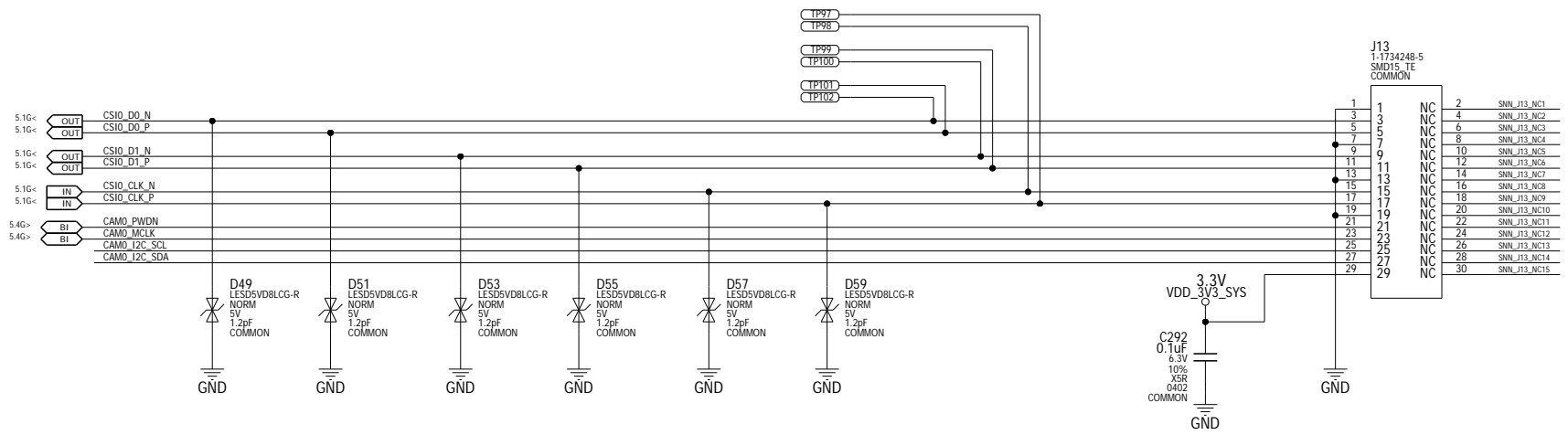
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DisplayPort Connector

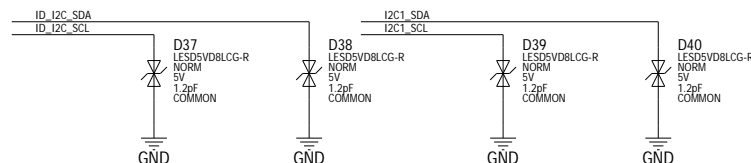
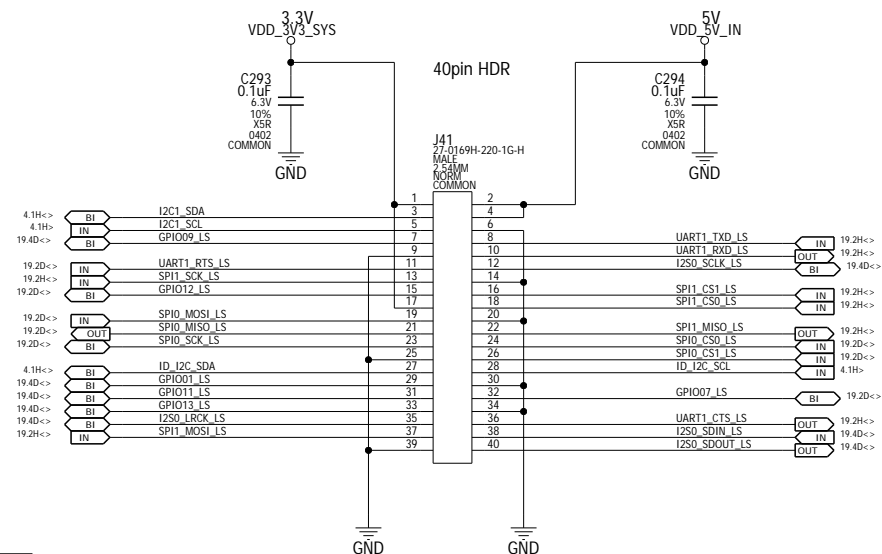
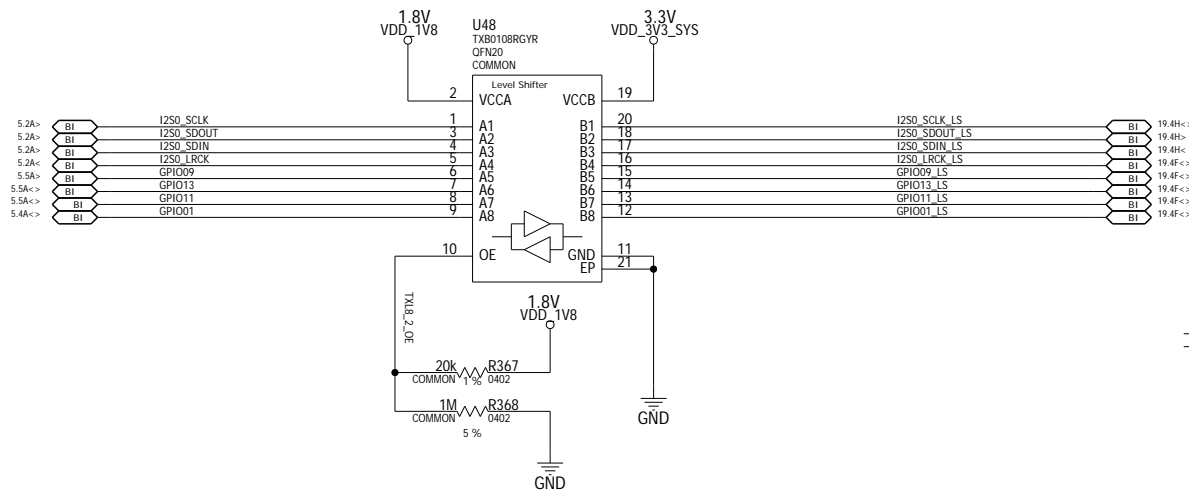
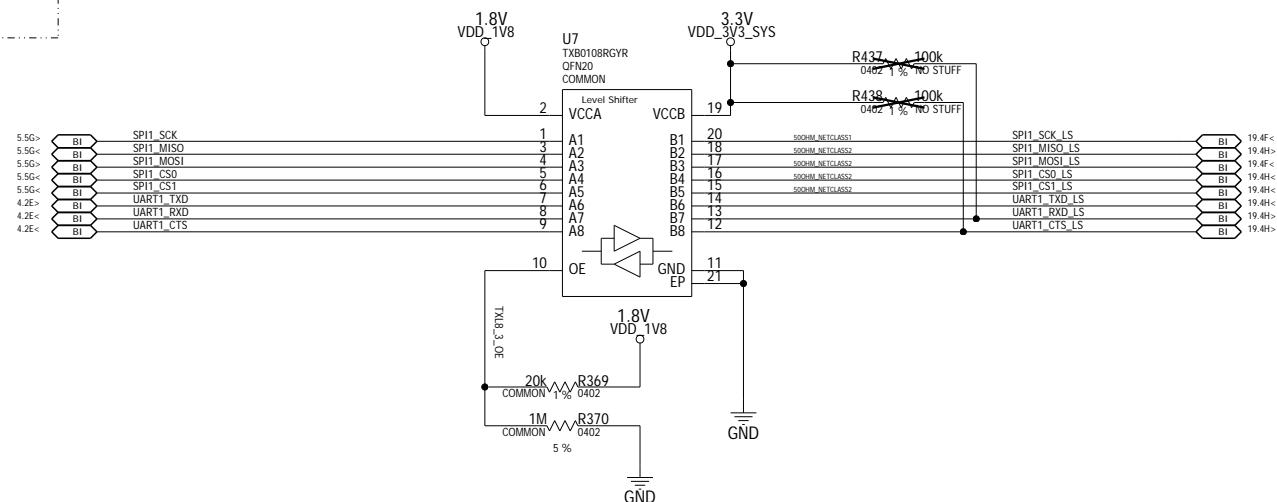
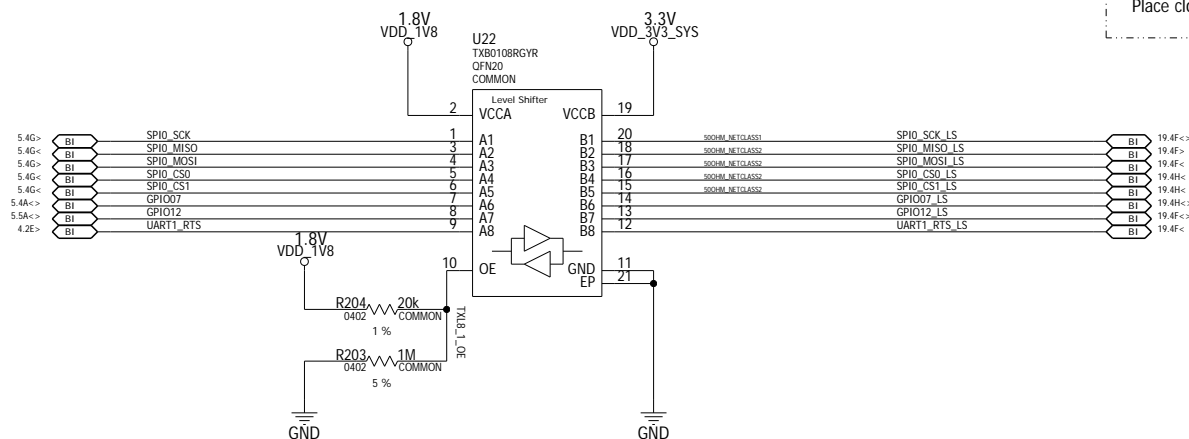
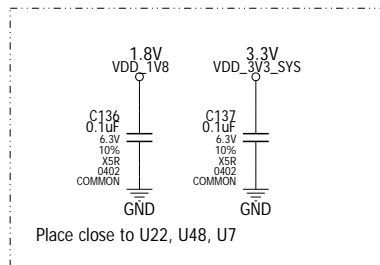


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CSI CAM CONNECTOR



40PIN GPIO EXPANSION HEADER & LEVEL SHIFTERS



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| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | 40Pin GPIO expansion header |

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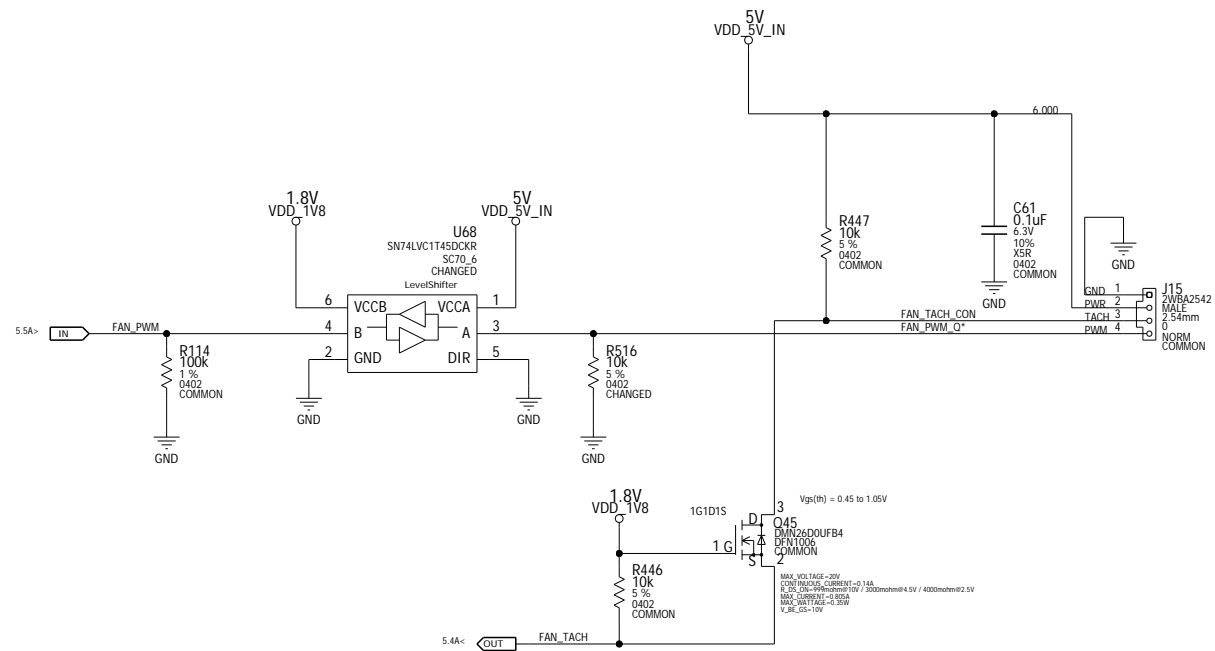
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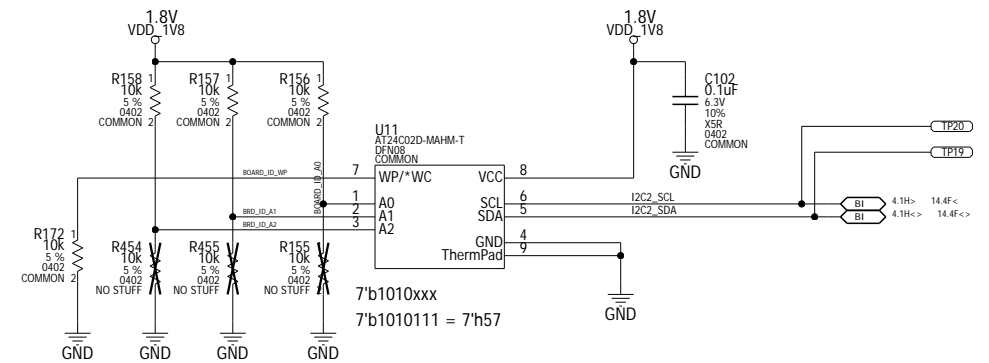
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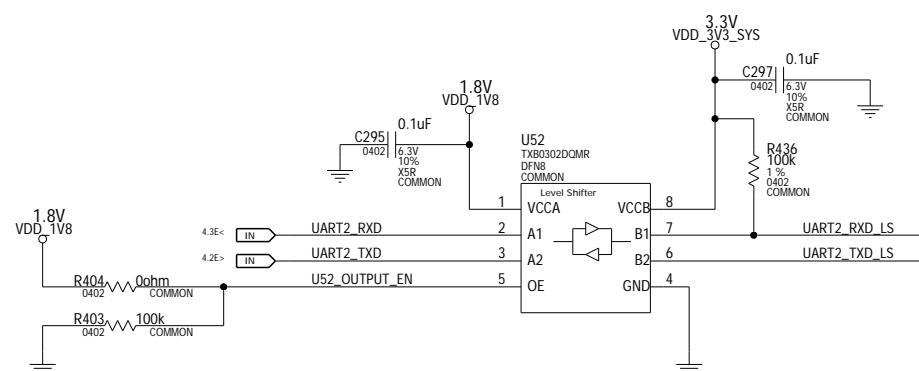
SoC Fan Header 4pin



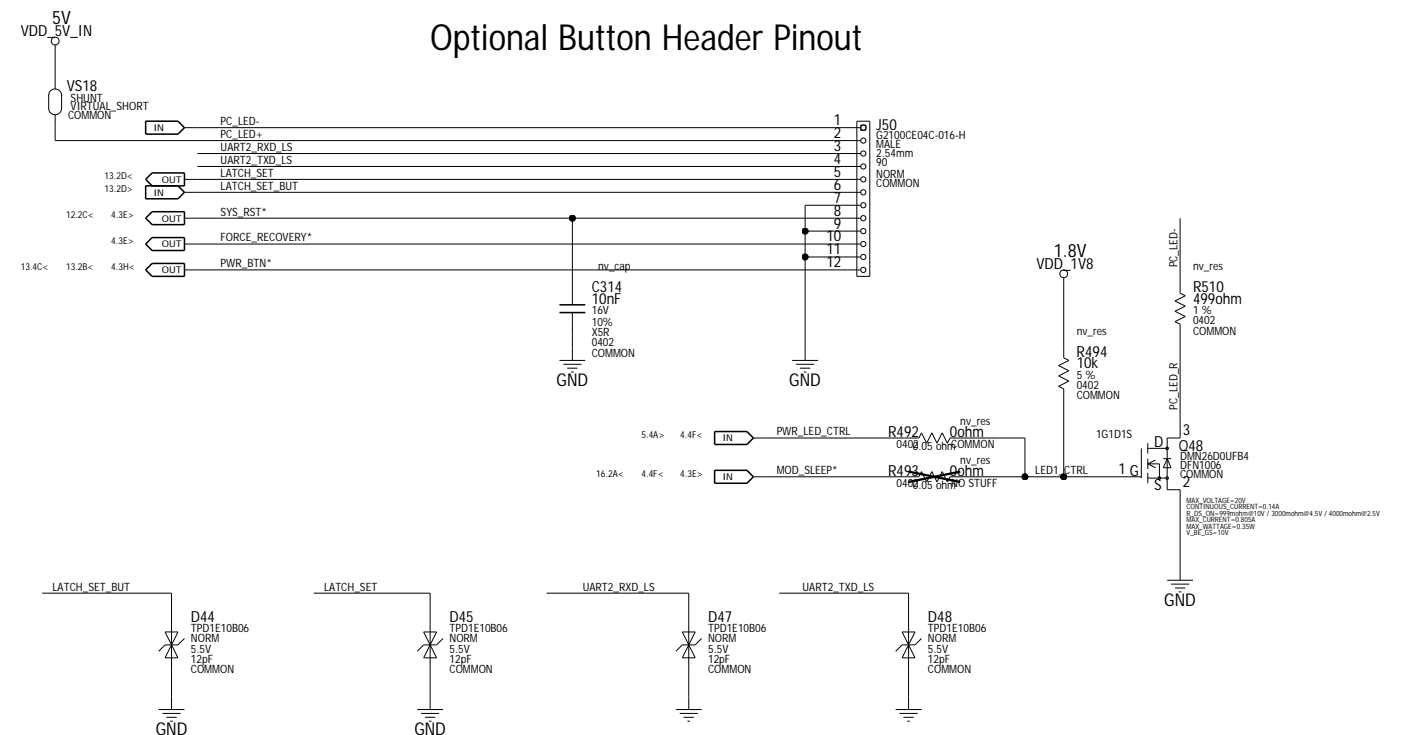
Carrier board config



Serial Port



Optional Button Header Pinout



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| ASSEMBLY | Jetson Nano Developer Kit Carrier Board |
| PAGE DETAIL | Fan, Debug and EEPROM |

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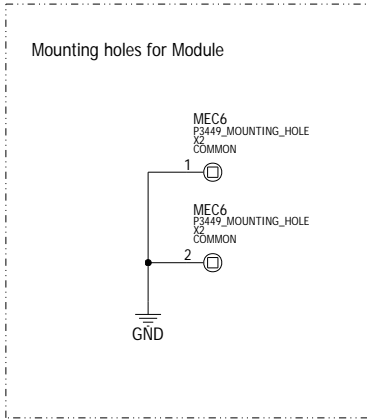
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Mechanical



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| | | | | ASSEMBLY | | Jetson Nano Developer Kit Carrier Board | | |
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