

Sidhant Sharma

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SKILLS

- **Languages:** C, C++, Python, Verilog
- **Tools/Platforms:** AutoCAD, Creo, Proteus, TinkerCAD, MATLAB, Cadence, DSCH software, Arduino IDE, Altium designer
- **Soft Skills:** Solution Oriented, Collaborative, Adaptable, Team Management

INTERNSHIP

- **THDCIL, Uttarakhand** Jul 2025
Hydro Intern
 - Gained hands-on exposure to the working principles and electricity generation process of hydro power plants
 - Assisted in the maintenance and operational support of machinery and equipment within the hydro power plant
 - Observed safety protocols and plant operations to understand large-scale power production systems
- **AutomateX – PLC based training for Industrial Automation** Jul 2025
Student Intern
 - Learned about the development of ladder logic structure for the working of industrial automation setups
 - Worked with PLC simulation and programming tools such as **LogixPro**, **TwidoSuite**, and **Ladder Logic** to design automation workflows
 - Gained practical exposure to industrial control processes and automation system configurations

PROJECTS

- **Solar tracking system** Sep 2025
 - Built a dual axis solar tracking system using solar panel, LDR modules and motor driver module
 - Improved solar energy capture by automatically adjusting the panel's orientation based on light intensity
 - Enhanced overall efficiency by maximizing sunlight exposure throughout the day
 - Tech used: **LDR module**
- **Obstacle avoidance robot** Jan 2025
 - Designed an autonomous obstacle-avoidance robot car using an ultrasonic sensor and Arduino microcontroller programmed in C++
 - Achieved accurate real-time obstacle detection and automatic path correction for smooth navigation
 - **Tech used:** C++, Arduino IDE
- **Home security system** Nov 2024
 - Developed a smart home security system integrating GSM, PIR and ultrasonic sensors embedded with Arduino IDE microcontroller
 - Enabled automated intrusion detection with instant mobile alerts, significantly improving home safety
 - **Tech used:** C++, Arduino IDE, Soldering

CERTIFICATES

- Verilog HDL: VLSI Hardware Design Comprehensive Masterclass Mar 2025
- Complete A.I. & M.L., Data Science Bootcamp Feb 2025
- Computer Science 101: Master the Theory Behind Programming Mar 2024

ACHIEVEMENTS

- Secured **1st position** in the batch-level Analytical Skills Competition conducted by LPU
- Achieved recognition as a **national-level badminton player** in competitive tournaments

EDUCATION

- **Lovely Professional University** Phagwara, Punjab
Bachelor of Technology – Electrical and Electronics Engineering; CGPA: 7.07 Since Aug 2023
- **Ishu Model Sen. Sec. School** Rehan, Himachal Pradesh
Intermediate- Percentage: 79 May 2022 – May '23
- **Noorpur Public School** Nurpur, Himachal Pradesh
Matriculation- Percentage: 86 Mar 2020 – Apr '21