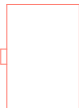




W2

+12V

PCI

**+12V**

PCI



PCI

GND

CPU

J1

CPU

J3

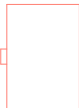
+12V**+12V**

Lr



W4

PCI

**+12V**

PCI



PCI

+12V

W13

W5

W6

GND

W11

W12

W20



W3



W1