Gesture Controlled Alarm Clock

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Purpose

- Alarm Clocks are susceptible to power failure
- Also very unintuitive
- Could be infinitely better if you didn't have to touch it as much

Solution: VL53L5

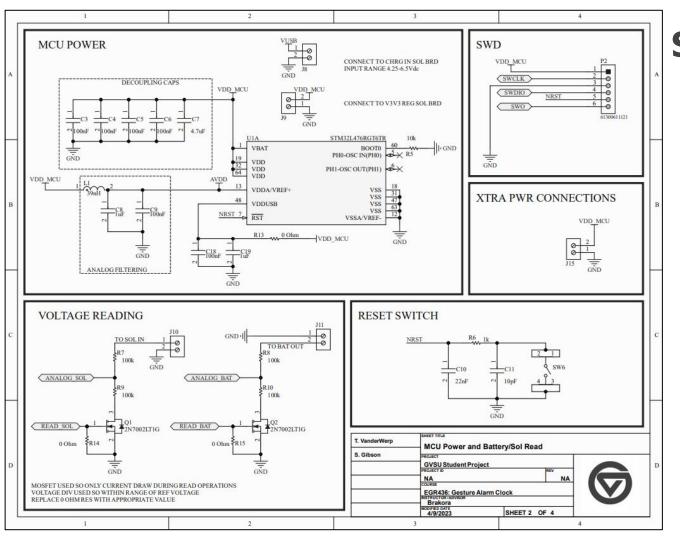


Features to note

- Set alarm via on board buttons, and bluetooth
- Read alarm via bluetooth, terminal, and LCD display
- Turn off alarm via gesture or pushbutton
- Alarms are stored on flash and exist after a total power out event.
 - Main clock still has to be reset during a total power out event.
- Speaker triggers upon alarm
 - Turned off via button or gesture
- LCD Display only powered during USB connection, increasing battery life of the system.
- Voltage on the battery and solar panel can be viewed on the LCD.

STM32L4RG76 I/O TEST POINTS PC1 SPII MISO SPIL CLK PC2 ANALOG BAT SPI1 CS PC3 READ BAT PC4 SPI1 CLK SPI1 MISO PC5 PC6 SPII MOSI SPII MOSI USARTI TX PC7 PC8 PC9 PAII PCII USARTI RX PA12 PC12 PC14-OSC32 IN(PC14) PC15-OSC32 OUT(PC15) (SWDIO) PA13(JTMS-SWDIO) SWCLK PA13(JTMS-SWDIO) PA14(JTCK-SWCLK) PC15-OSC32 OUT(PC15) I2C SDA PD2 SWO SPB3(JTDO-TRACESWO) VDD MCU PB4(JTRST) PB5 VL53 INT SPI FLASH I2C SCI CS 1 * SPII CS SPIL MOSI 5 SPII MISO DI (100) DO (IO1) 6 SPII CLK MCU BTN1 33 PB11 MCU BTN2 34 PB12 MCU BTN3 35 PB13 MCU BTN3 35 PB14 VDD_MCU + VCC GND W25X40CLSNIG STM32L476RGT6TR Cl 0.luF BUTTONS VDD MCU BTN2 BTN3 19 MCU BTNI IN2 IN3 BTN4 BTN3 18 MCU BTN2 OUT2 HZ 17 MCU BTN3 OUT3 16 MCU BTN4 IN5 OUT4 15 MCU BTN5 OUT5 SWI SW3 SW4 SW5 OUT6 OUT7 SW DEBOUNCE OUT8 I GND GND MAX6818EAP+T *REQUIRES INTERNAL MCU PULLUP T. VanderWerp MCU and Peripherals S. Gibson GVSU Student Project FD1 FD2 O H4 Mounting Hole #4 Mounting Hole EGR436: Gesture Alarm Clock O O O O O H4 Mounting Hole #4 Mounting Hole Brakora 4/9/2023 SHEET 1 OF 4

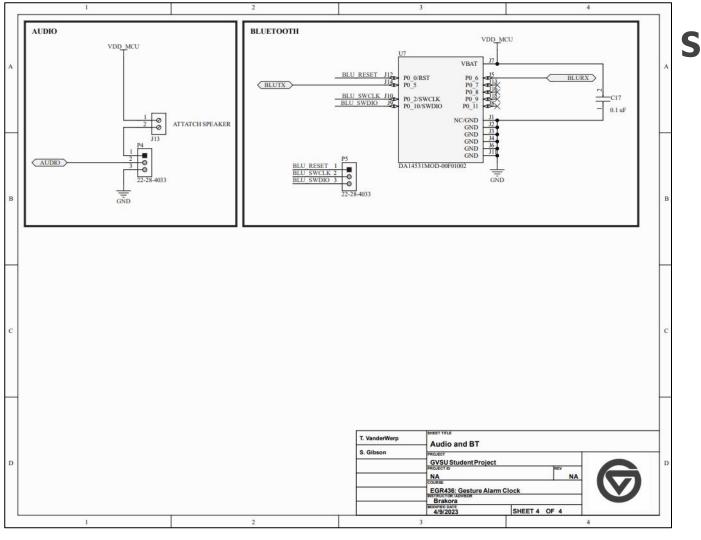
Schematics (1)



Schematics (2)

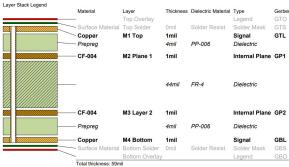
VL53L5CX LCD VDD_MCU VDD MCU THERMAL PAD GND GND MISO SCL/MCLK SDA/MOSI SPI I2C N C4 ' GPIO1 SDA VDD MCU→ LPn *PULLUP GPIO2 GNB AVDD AVDD GND GND AVDD VL53L5CXV0GC/1 PORTEXP SDA *PULLUP - REQUIRES MCU INTERNAL PULLUP CP2102 RTC DTR DSR DCD RI CTS RTS TXD RXD VDD MCU VDD_MCU R12 32kHz 4.7k INT/SQW RTC INT SUSPEND SUSPEND REGIN GND *N/A 6 VDD VCC GND GND DS3231MZ+TRL CP2102-GMR NC NC NC NC *PULLUP - REQUIRES MCU INTERNAL PULLUP **N/A - VBAT BACKUP NOT USED AS ENTIRE PROJECT RUNNING ON BATTERY USB USB ESD PROTECTION *N/A - CP2102 VOLTAGE REGULATOR NOT USED ZX62R-B-5P VBUS T. VanderWerp ToF, LCD, UART Bridge, RTC and USB ID S. Gibson GND GVSU Student Project NA GND EGR436: Gesture Alarm Clock SP0503BAHTG Brakora 4/9/2023 SHEET 3 OF 4

Schematics (3)

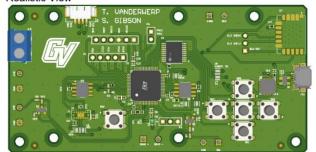


Schematics (4)

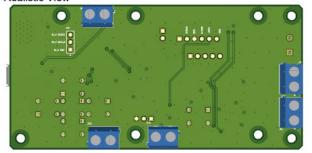


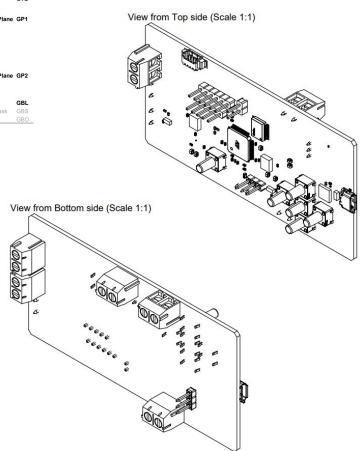


Realistic View



Realistic View

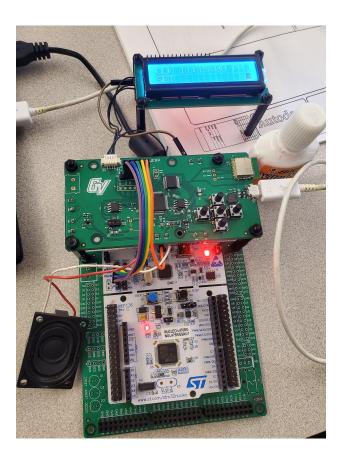




Prototype/Demo

It'll look pretty,

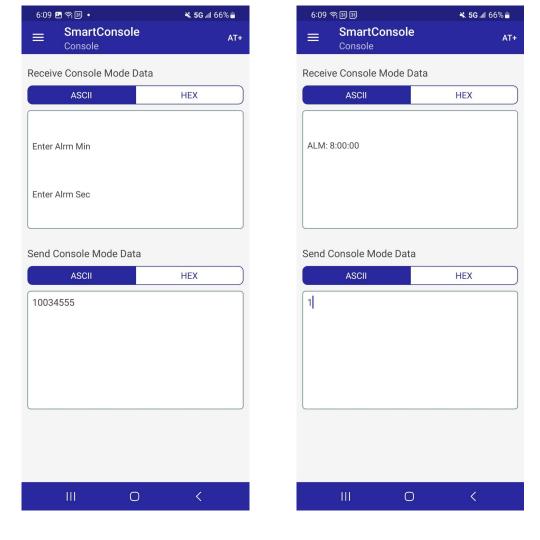
we promise.



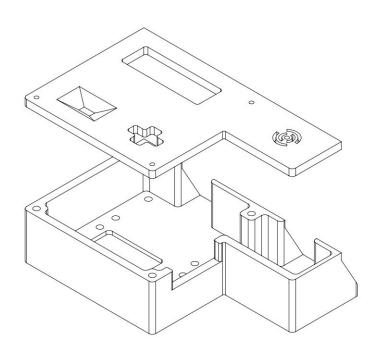
Results

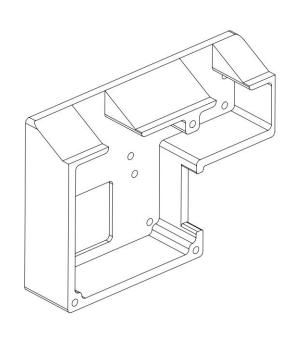


Bluetooth



Enclosure





Final Progress (Completed)

- "Legacy" System
 - Solar/Battery/USB Power Capabilities
 - Time and Alarm visible on LCD
 - Alarm visible over UART (Serial & BT)
 - Menu Navigation w/ Buttons
 - Time and Alarm can be set by buttons & hand wave
 - Time and Alarm can be set by UART (Serial & BT)
 - Alarm Time stored to FLASH
 - Voltage ADC Readouts available
 - Alarm Triggers with horrible noise
 - Alarm can be turned off by button & hand wave

Future Improvements (To Do)

- Final System
 - Integrating TOF Time Selection
 - General Debugging
 - Final Assembly
 - GOAL Friday, 4/21
- Cut Features
 - Selecting time values via hand swiping
 - Snooze button functionality

Questions