HUMBER

Course Outline

Course Name: Programmable Logic Controllers: Introduction (ELEC 209)

Academic Period: 2022 - 2023

Faculty:

Faculty Availability:

Associate Dean:
Shaun Ghafari
shaun.ghafari@humber.ca

Schedule Type Code:

Land Acknowledgement

Humber College is located within the traditional and treaty lands of the Mississaugas of the Credit. Known as Adoobiigok [A-doe-bee-goke], the "Place of the Alders" in Michi Saagiig [Mi-Chee Saw-Geeg] language, the region is uniquely situated along Humber River Watershed, which historically provided an integral connection for Anishinaabe [Ah-nish-nah-bay], Haudenosaunee [Hoeden-no-shownee], and Wendat [Wine-Dot] peoples between the Ontario Lakeshore and the Lake Simcoe/Georgian Bay regions. Now home to people of numerous nations, Adoobiigok continues to provide a vital source of interconnection for all.

Equity, Diversity and Inclusion Statement

Humber College and the University of Guelph-Humber (Humber) are leaders in providing a learning, working and living environment that recognizes and values equity, diversity and inclusion in all its programs and services. Humber commits to reflect the diversity of the communities the College serves. Students, faculty, support and administrative staff feel a sense of belonging and have opportunities to be their authentic selves.

Faculty or Department	Faculty of Applied Sciences & Technology
Course Name:	Programmable Logic Controllers: Introduction (ELEC 209)
Pre-Requisites	none
Co-Requisites	none
Equates	none
Restrictions	none
Credit Value	3
Total Course Hours	42

| Developed By: | Prepared By: | Approved by: |

Shaun Ghafari

Humber Learning Outcomes (HLOs) in this course.

The HLOs are a cross-institutional learning outcomes strategy aimed at equipping Humber graduates with the employability skills, mindsets, and values they need to succeed in the future of work. To explore all the HLOs, please consult the Humber Learning Outcomes framework.

Course Description

N/A

Course Rationale

PLC's are designed for multiple input and output controls and are used in many industries and machines. Because of their leading role in industrial applications, it becomes increasingly more important to study them and gain practical skills related to these devices.

Course Learning Method(s)

- Case Based Learning
- Lecture
- Online

Learning Outcomes

- Describe the principle of operation and architecture of a PLC as well as programming Standard IEC 61131-3 as it is applied to PLCs
- Create wiring diagrams for PLC system and Input/Output modules
- Use programming software for communication with PLCs
- Use Data File image tables and addressing for programming purpose
- Apply basic bit level instructions, such as XIC, XIO, OTE, OTL, OTU, OSR, OSF in application programs
- Design basic programs oriented to real industrial applications
- Employ troubleshooting techniques using the "Force/over-ride" instructions
- Use Timers and Counters in PLC programs
- Apply Bit Shift instructions in PLC programs
- Program the word level instructions, such as Mathematical functions, Data manipulation and Comparison instructions in PLC applications

Assessment Weighting

Assessment	Weight
Final Exam	
Test#2	35%
Midterm Exam	
Test#1	35%

Assessment	Weight
In-class Exercise	
Lab assignment	30%
Total	**100%**

Modules of Study

Module	Course Learning Outcomes	Resources	Assessments
Module 1: Introduction to PLCs	• Describe the principle of operation and architecture of a PLC as well as programming Standard IEC 61131-3 as it is applied to PLCs • Create wiring diagrams for PLC system and Input/Output modules • Use programming software for communication with PLCs	Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education	• Lab assignment • Test#1
Module 2: Ladder Logic	• Create wiring diagrams for PLC system and Input/Output modules • Use programming software for communication with PLCs • Use Data File image tables and addressing for programming purpose • Apply basic bit level instructions, such as XIC, XIO, OTE, OTL, OTU, OSR, OSF in application programs • Design basic programs oriented to real industrial applications • Employ troubleshooting techniques using the "Force/over-ride" instructions • Use Timers and Counters in PLC programs	Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education	• Lab assignment • Test#1
Module 3: Timers	• Create wiring diagrams for PLC system and Input/Output modules • Design basic programs oriented to real industrial applications • Use Timers and Counters in PLC programs	Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education	• Lab assignment • Test#2
Module 4: Counters	• Create wiring diagrams for PLC system and Input/Output modules • Design basic programs oriented to real industrial applications • Use Timers and Counters in PLC programs	Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education	• Lab assignment • Test#2

Module	Course Learning Outcomes	Resources	Assessments
Module 5: Bit Shift Left and Right	• Create wiring diagrams for PLC system and Input/Output modules • Design basic programs oriented to real industrial applications • Apply Bit Shift instructions in PLC programs	Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education	• Lab assignment • Test#2
Module 6: Data Manipulations	• Create wiring diagrams for PLC system and Input/Output modules • Design basic programs oriented to real industrial applications • Program the word level instructions, such as Mathematical functions, Data manipulation and Comparison instructions in PLC applications	Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education	• Lab assignment • Test#2

Required Resources

Petruzella, F. D. (2017). *Programmable Logic Controllers (5th Ed.)*. 2 Penn Plaza, New York: McGraw-Hill Education

Supplemental Resources

Rehg, J.A., Sartori, G.J. (2006). *Industrial Electronics*. Upper Saddle River, New Jersey: Pearson Prentice Hall.

Manufacturer's documentation, manuals and user guides.

Additional Tools and Equipment

• Not required

Essential Skills

Section	Skills	Measurement	Details
Communication	• Reading • Writing • Speaking • Presenting • Visual Literacy	Teach and measure	• Through lecturing and practical activities in the laboratory • Through lab assignments and tests
Numeracy	• Understanding and applying mathematical concepts and reasoning • Analyzing and using numerical data • Conceptualizing	Teach and measure	• Through lecturing, lab activities, discussions, case studies • Through lab assignments and tests

Section	Skills	Measurement	Details
Critical Thinking and Problem-Solving	• Analysing • Evaluating • Decision-Making	Teach and measure	• Through lecturing, lab activities, discussions • Through lab assignments and tests
Information Management	• Gathering and managing information • Selecting and using appropriate tools and technology for a task or project • Computer literacy • Internet skills	Teach and measure	• Through lecturing, lab activities, discussions • Through lab assignments and tests
Interpersonal Skills	• Teamwork • Conflict resolution	Reinforce and measure	• Through lab activities • Through lab assignments
Personal Skills	• Managing self • Managing change and being flexible and adaptable • Engaging in reflective practice • Demonstrating personal responsibility	Reinforce and measure	• Through lab activities • Through lab assignments

Prior Learning Assessment & Recognition (PLAR)

Prior Learning Assessment and Recognition (PLAR) is the formal evaluation and credit-granting process whereby candidates may obtain credits for prior learning. Prior learning includes the knowledge competencies and skills acquired, in both formal and informal ways, outside of post-secondary education. Candidates may have their knowledge, skills and competencies evaluated against the learning outcomes as defined in the course outline. Please review the Assessment Methods Glossary for more information on the Learning Portfolio assessment methods identified below.

The method(s) that are used to assess prior learning for this course may include:

- Challenge Exam (results recorded as a % grade and added to student's CGPA)
- Learning Portfolio (results reflected as SAT and not added to student's CGPA)
- Skills Test
- Interview

Please contact the Program Coordinator for more details.

Academic Regulations

It is the student's responsibility to be aware of the College Academic Regulations. The Academic Regulations apply to all applicants to Humber and all current students enrolled in any program or course offered by Humber, in any location. Information about academic appeals is found in the **Academic Regulations**.

Anti-Discrimination Statement

At Humber College, all forms of discrimination and harassment are prohibited. Students and employees have the right to study, live and work in an environment that is free from discrimination and harassment. If you need assistance on concerns related to discrimination and harassment, please contact the Centre for Human Rights, Equity and Inclusion or the Office of Student Conduct.

Accessible Learning Services

Humber strives to create a welcoming environment for all students where equity, diversity and inclusion are paramount. Accessible Learning Services facilitates equal access for students with disabilities by coordinating academic accommodations and services. Staff in Accessible Learning Services are available by appointment to assess specific needs, provide referrals and arrange appropriate accommodations. If you require academic accommodations, contact:

Accessible Learning Services

North Campus: (416) 675-6622 X5090

Lakeshore Campus: (416) 675-6622 X3331

Academic Integrity

Academic integrity is essentially honesty in all academic endeavors. Academic integrity requires that students avoid all forms of academic misconduct or dishonesty, including plagiarism, cheating on tests or exams or any misrepresentation of academic accomplishment.

Disclaimer

While every effort is made by the professor/faculty to cover all material listed in the outline, the order, content, and/or evaluation may change in the event of special circumstances (e.g. time constraints due to inclement weather, sickness, college closure, technology/equipment problems or changes, etc.). In any such case, students will be given appropriate notification in writing, with approval from the Dean (or designate) of the School.

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