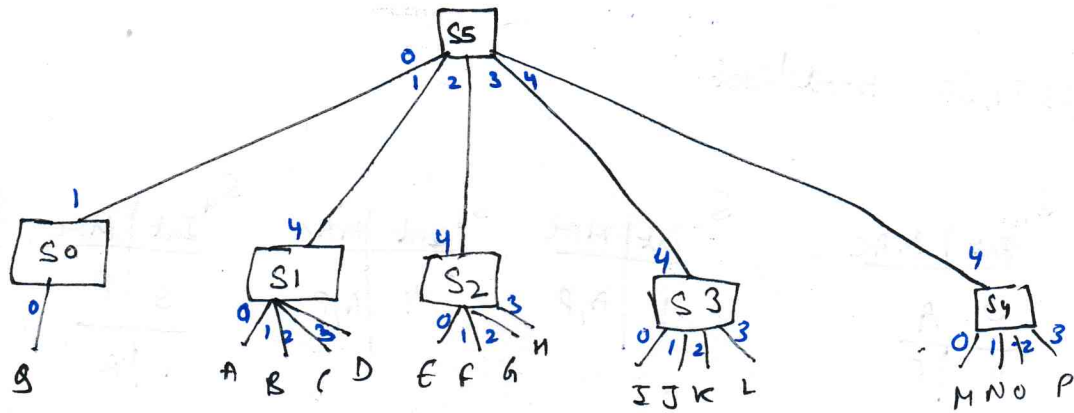


0-3)



Only showing filled entries

a) A → L

S1 broadcasts & stores MAC of A at interface 0

S5	broadcasts	"	"	"	"	"	"	1
S0	"	"	"	"	"	"	"	1
S2	"	"	"	"	"	"	"	4
S3	"	"	"	"	"	"	"	4
S4	"	"	"	"	"	"	"	4

S₀:

Interface	MAC
1	A

S₁:

Interface	MAC
0	A

S₂:

Interface	MAC
4	A

S₃:

Interface	MAC
4	A

S₄:

Interface	MAC
4	A

S₅:

Interface	MAC
1	A

b) $P \rightarrow Q$

$S_0, S_1, S_2, S_3, S_4, S_5$ broadcast.

$$S_0$$

Int	MAC
1	A, P

$$S_1$$

Int	MAC
0	A
4	P

$$S_2$$

Int	MAC
4	A, P

$$S_3$$

Int	MAC
4	A, P

$$S_4$$

Int	MAC
3	P
4	A

$$S_5$$

Int	MAC
1	A
4	P

c) $Q \rightarrow P$

S_0 sends via port 1

S_5 sends via port 4

S_4 sends via port 3

$$S_0$$

Int	MAC
0	Q
1	A, P

$$S_1$$

Int	MAC
0	A
4	P

$$S_2$$

Int	MAC
4	A, P

$$S_3$$

Int	MAC
4	A, P

$$S_4$$

Int	MAC
3	P
4	A, Q

$$S_5$$

Int	MAC
0	Q
1	A
4	P

