

FPGA Device Type

DE0-Nano version B0 and version C0 FPGA has a Cyclone-V SoC
5CSEMA4U23C6N Device

Pin Assignment

FPGA_CLK1_50 (50 MHz)	PIN_V11
Switch 1	PIN_AG28
Switch 2	PIN_Y15
Switch 3	PIN_AF11
Switch 4	PIN_AE12
Switch 5	PIN_AA15
Switch 6	PIN_AG26
Switch 7	PIN_AF23
Switch 8	PIN_AF21
Switch 9	PIN_AF9
Switch 10	PIN_AD10
Switch 11	PIN_AD11
Switch 12	PIN_AD12
Switch 13	PIN_AH27
Switch 14	PIN_AH24
Switch 15	PIN_AE22
Switch 16	PIN_AG20
Switch 17	PIN_AE8
Switch 18	PIN_AE9
Switch 19	PIN_AF10
Switch 20	PIN_AE11
LED 1	PIN_T8
LED 2	PIN_T12
LED 3	PIN_Y5
LED 4	PIN_U11
LED 5	PIN_AH18
LED 6	PIN_AG21
LED 7	PIN_AH21
LED 8	PIN_AH19

LED 9	PIN_W8
LED 10	PIN_Y4
LED 11	PIN_Y8
LED 12	PIN_AB4
LED 13	PIN_AC22
LED 14	PIN_AA18
LED 15	PIN_AE23
LED 16	PIN_AD23
LED 17	PIN_W12
LED 18	PIN_AF8
LED 19	PIN_V12
LED 20	PIN_AF7
Button 1	PIN_AG5
Button 2	PIN_AE4
Button 3	PIN_AH4
Button 4	PIN_AF5
Seven Segmented display #1 A	PIN_AE24
Seven Segmented display #1 B	PIN_AD20
Seven Segmented display #1 C	PIN_AG19
Seven Segmented display #1 D	PIN_AG18
Seven Segmented display #1 E	PIN_AC23
Seven Segmented display #1 F	PIN_AE20
Seven Segmented display #1 G	PIN_AD19
Seven Segmented display #1 DP	PIN_AF20
Seven Segmented display #2 A	PIN_AG23
Seven Segmented display #2 B	PIN_AH23
Seven Segmented display #2 C	PIN_AG24
Seven Segmented display #2 D	PIN_AA19
Seven Segmented display #2 E	PIN_AH26
Seven Segmented display #2 F	PIN_AE19
Seven Segmented display #2 G	PIN_AF18
Seven Segmented display #2 DP	PIN_AF25

Datasheet for seven segmented display: <https://www.farnell.com/datasheets/2863910.pdf>

How to assign pins in Quartus: <https://www.youtube.com/watch?v=XH4OajcYYjA>