



Gold-Mediated Exfoliation of Ultralarge Optoelectronically-Perfect Monolayers

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Transition-metal dichalcogenides (TMDCs) are a class of layered materials analogous to graphene, which have aroused immense interest in the last few years as a potential platform for future electronic and optoelectronic applications. [1,2] The layers in these materials are held together by weak van der Waals (vdW) forces, making it easy to cleave them to the limit of a monolayer (a single unit of three atomic layers, comprised of one layer of transition metal atoms sandwiched between two layers of chalcogen atoms). Monolayers of many TMDCs, such as MoS2, WS2, WSe2, and MoSe2, are especially exciting since they are direct band gap semiconductors, making them ideal for applications in devices such as light-emitting diodes (LEDs), lasers, photodiodes, etc. Their naturally passivated surfaces, sizeable bandgaps (\approx 1–2 eV), atomic scale thickness (\approx 0.7 nm), and low dielectric constant (≈4) also make them suitable candidates for replacing silicon at the metal-oxide-semiconductor field-effect transistor (MOSFET) scaling limit due to mitigated short channel effects.[3]

Monolayer-TMDC flakes typically produced using the tape exfoliation method (similar to Scotch® tape method used for graphene [4]) are small in size (\approx 5 µm) and the yield of the procedure is poor. Simple modifications like exfoliating the flakes on a hot plate result in a slight increase in the size of

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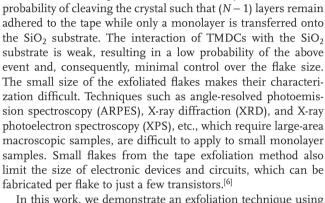


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DOI: 10.1002/adma.201506171

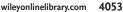


flakes achieved.^[5] Tape exfoliation of monolayers relies on the

In this work, we demonstrate an exfoliation technique using evaporated gold films $^{[7,8]}$ to exfoliate large area TMDC monolayers onto various substrates such as SiO₂/Si and quartz. Gold is known to have a strong affinity for chalcogens, especially to sulfur atoms with which it forms a semicovalent bond with bond strength of $\approx\!\!45$ kcal mol $^{-1}$. The interaction between gold and sulfur has been used in the formation of self-assembled monolayers of thiolated organic molecules on gold surfaces $^{[10]}$ and gold—thiolate complexes. The same idea is used in this work by selectively increasing the adhesion of the topmost layer of a bulk TMDC crystal to gold by evaporating a thin film on it and subsequently peeling it off from the bulk crystal.

The detailed process flow is illustrated in Figure 1a. Gold (≈100–150 nm) is evaporated onto bulk MX_2 (M = Mo or W; X = S or Se) crystals. The gold atoms bond with the chalcogen atoms of the topmost layer of MX2. The interaction of the topmost layer with the evaporated gold is stronger than the vdW interactions of that same layer with the bottom layers of MX₂. This enables selective peel-off of the topmost layer using a thermal release tape which is later stuck onto the desired target substrate (SiO₂/Si or quartz). The thermal tape is then released on a hot plate (≈ 130 °C) and the substrate is treated with a mild O2 plasma to remove the tape residue from the surface. The O₂ plasma power and etching time are kept low to ensure that the gold is not etched away, thereby exposing the underlying monolayer MX₂. The gold film is then etched using potassium iodide and iodine (KI/I2) wet etch (Transene Gold Etch-type TFA for 4 min), which does not etch the TMDC flakes. This is followed by a 10 min acetone clean and isopropyl alcohol (IPA) rinse to remove any residues and obtain the large-area monolayers.

The relatively strong binding of the gold to the topmost layer of MX_2 enhances the probability of the crystal to cleave right at the topmost layer, increasing both the yield and the



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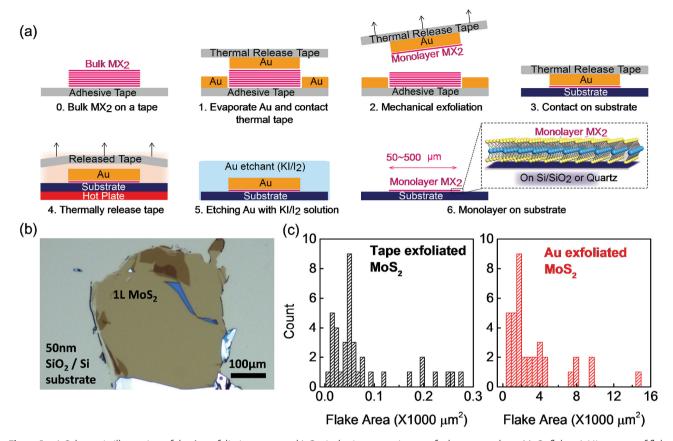


Figure 1. a) Schematic illustration of the Au exfoliation process. b) Optical microscope image of a large monolayer MoS_2 flake. c) Histogram of flake areas for tape-exfoliated versus Au-exfoliated MoS_2 .

size of the flakes obtained. Using this technique, large area MoS_2 monolayers with lateral dimensions up to $\approx 500 \mu m$ are obtained. Figure 1b shows the optical image of one such large monolayer on a 50 nm SiO₂/Si substrate. The process was also used to exfoliate other TMDCs such as WS2 and WSe₂ (Figure S1b,c, Supporting Information). For the scope of this paper, we focus on the exfoliation and characterization of MoS₂ as a model system for all TMDCs. The histograms in Figure 1c show that the flake area for MoS₂ flakes obtained by the gold (Au) exfoliation method is ≈10,000 times larger than that of flakes obtained by the tape exfoliation method. The size of the monolayer flakes exfoliated by gold is primarily limited by the size of the source MoS2 crystal domains and may be increased in the future by the use of large sized flat crystals. Figure S2 (Supporting Information) shows the optical microscope images of several Au-exfoliated MoS2 samples on 50 nm SiO₂/Si substrates. The images indicate that exfoliated monolayer area is larger than that of multilayer MoS2, thereby demonstrating that Au adheres preferentially to the topmost layer of MoS2. The exfoliated bulk regions may be attributed to cleaving at potential defect sites in the source crystal. Also, we empirically observed that the probability of obtaining Auexfoliated monolayers is several times more compared to the tape exfoliation method. The exfoliation method described here does not involve the double transfer of exfoliated monolayers like the technique in ref., [7] which first requires exfoliation onto an Au substrate followed by transfer to the target substrate.

Furthermore, the flakes are uniform over a large area which is important for electrical device applications.

The success of the Au mediated exfoliation of large-area MoS₂ can be attributed to two main factors. First, as noted above, the Au binds to the S atoms quite strongly which enables its adhesion to the topmost layer of MoS₂. A second factor also contributes to the single layer selectivity of the process: as shown by previous calculations, large mismatch in lattice constants causes strain in the topmost layer of MoS₂ and gold.^[11] The first layer of MoS₂ is compliant and can slip relative to the underlying substrate, which slightly weakens its bond with the other layers of the bulk MoS₂ crystal. Similarly, strain due to evaporated metal films was also used to exfoliate single layer graphene as shown in ref.^[11]

Prior to this work, because of the small size of tape-exfoliated flakes, surface characterization of MoS_2 monolayers has only been possible with photoemission electron microscopy (PEEM).^[12] Gold-exfoliated monolayers are sufficiently large to be characterized by standard XPS measurements. XPS measurements were performed using a macroscale monochromatic Al $K\alpha$ excitation source and allowed direct comparison of the monolayers to the bulk crystal used for our source material (**Figure 2**). Following background subtraction and fitting (details given in the Experimental Section), the XPS data for monolayer MoS_2 is found to be qualitatively similar to the XPS spectrum of MoS_2 bulk crystal with some notable differences in the peak binding energies. Of particular note is the





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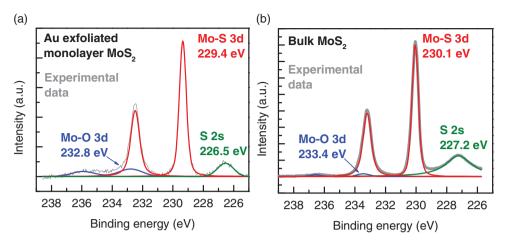


Figure 2. a) XPS experimental data and fitted Voigt curves for a Au-exfoliated monolayer MoS2. b) XPS experimental data and fitted curves for the bulk MoS₂ crystal source.

redshift (≈0.7 eV) in the binding energy for monolayer MoS₂ compared to bulk MoS₂. This is consistent with charge transfer from the underlying silicon substrate to the monolayer MoS₂ flake. This XPS measurement demonstrates the importance of the Au exfoliation technique toward accelerating monolayer TMDC research since measurements such as PEEM, require bright light sources which are typically limited to synchrotron facilities.

The Au-exfoliated MoS₂ flakes undergo a multistep process, including Au evaporation, KI/I2 etching, and acetone cleaning. Apart from XPS, careful characterization by optical and electrical measurements is necessary to compare their quality to flakes from the tape exfoliation method. Figure 3a,b shows that the Raman and photoluminescence (PL) spectra for Au-exfoliated MoS₂ flakes are identical to those of tape-exfoliated MoS₂ flakes. No new features or peaks are observed in both Raman and PL. Identical Raman peak positions also indicate that the flakes are not strained. Figure 3c,d shows that the Raman map of the position of the E^1_{2g} and the A_{1g} peaks of MoS_2 for the large flake are shown in Figure S1a (Supporting Information). The Raman peak position value can be used to identify the monolayer and multilayer MoS2 regions of the flake.^[13] The peak positions are quite uniform over the entire area of the monolayer flake away from any markers on the chip. Near the markers (5/35 nm thick Cr/Au) we see a slight variation in the peak position indicating that the MoS2 locally is strained

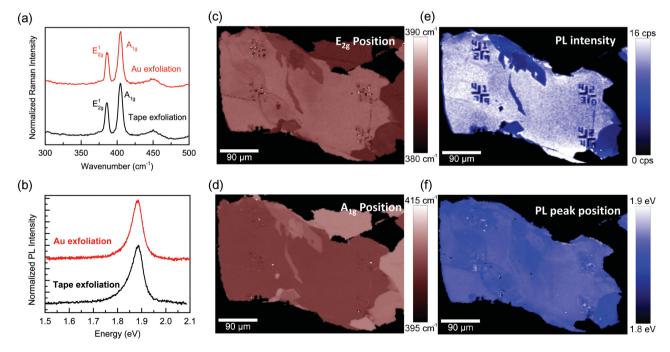


Figure 3. a) Raman spectra comparison for Au-exfoliated flake versus tape-exfoliated flake. b) Photoluminescence spectra comparison for Au-exfoliated flake versus tape-exfoliated flake. c,d) Raman peak E_{2g}^1 and A_{1g} maps of monolayer MoS_2 flake in Figure S1a (Supporting Information). e,f) PL peak intensity and position maps of monolayer MoS2 flake in Figure S1a (Supporting Information).



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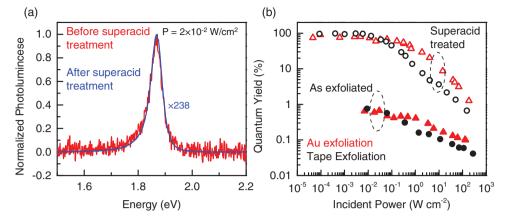


Figure 4. a) PL spectra before and after chemical treatment on Au-exfoliated monolayer MoS_2 . b) Quantum yield versus incident power intensity for tape-exfoliated [15] and Au-exfoliated monolayer MoS_2 before (as exfoliated) and after chemical treatment (TFSI).

due to substrate topography.^[14] Figure 3e,f shows the PL peak position and intensity map for the same flake. This map is also quite uniform over the entire monolayer region except for the strain fields near the markers which is visible in the PL peak position map. The absence of strain and the uniformity of Raman and PL over the large area of MoS₂ is of utmost significance for device applications. Strain induced variations of the bandgap, mobility and effective mass can all affect device performance, resulting in statistical variations of intrinsic parameters like threshold voltage, ON/OFF current, etc., similar to problems caused by line-edge roughness in modern MOSFETs.

Photoluminescence quantum yield (QY) measurements, which provide a quantitative estimate of the fraction of radiative recombination in a material over total recombination, are an accurate way to gauge optoelectronic quality. For an ideal material with zero defects (i.e., trap states) and low carrier concentration the QY should be 100% at low injection levels where carrier-carrier interactions, such as Auger or biexcitonic recombination, are not dominant. However, it has been experimentally found that the tape-exfoliated MoS2 flakes have poor QY of the order of ≈1% at most^[2] due to defects such as vacancies preexisting in the bulk crystal. Recently, we demonstrated that it is possible to repair/passivate defects in tape-exfoliated MoS₂ flakes using bis(trifluoromethane)sulfonimide (TFSI, Sigma-Aldrich), an organic superacid, leading to a drastic enhancement in QY to almost 100%.[15] The same treatment procedure is applied here to the Au-exfoliated large-area MoS2 flakes. Figure 4a shows the PL spectrum for the Au-exfoliated monolayer MoS2 before and after the chemical treatment. The PL intensity increased by a factor of ≈238× after treatment for this sample. Figure 4b shows the QY measured over a pump-power dynamic range of six orders of magnitude for both tape-exfoliated (from ref.^[15]) and Au-exfoliated MoS₂ monolayers, before and after the chemical treatment. The details of the measurement are provided in ref.^[15]. The chemical treatment results in almost 100% internal QY, yielding ultralarge optoelectronically perfect monolayer flakes at low pump-power as was observed previously in tape-exfoliated samples. Furthermore, at higher injection levels we observe a reduction in the QY consistent with intrinsic biexcitonic recombination which was observed in tape-exfoliated flakes; both samples show the onset of biexcitonic recombination at similar pump-power. These observations suggest that the optical quality, defect type, and defect passivation mechanism are similar for MoS_2 flakes obtained from both exfoliation methods.

Electrical characterization of Au-exfoliated monolayer MoS₂ was performed to comprehensively compare its quality against tape-exfoliated flakes and extract important device parameters such as mobility. Figure 5a shows the cross-section schematic of a back-gated monolayer MoS2 MOSFET fabricated on 50 nm SiO_2/n^{++} Si substrate with Ni source and drain contacts. Figure 5b is a representative image of fabricated monolayer MoS₂ devices with varying channel lengths. The arbitrarily shaped monolayer MoS2 flakes are pattern etched into a regular shape channel to ensure deterministic current flow from source to drain thereby permitting accurate extraction of device parameters. Details of the device fabrication are described in the Experimental Section. Figure 5c,d shows the $I_{\rm D}\!\!-\!V_{\rm GS}$ and I_D - V_{DS} characteristics for a long channel ($L = 20 \mu m$) device measured in vacuum. The Si substrate is used as the global back gate with the 50 nm SiO2 layer serving as the gate dielectric. The electrical characteristics show typical long channel MOSFET behavior with saturation observed in the I_D - V_{DS} characteristics.

After accounting for the large contact resistance ($R_{\rm C}$) which leads to a huge underestimation of mobility of carriers as described in ref., [16] the mobility for electrons is calculated to be ~26 cm² V⁻¹ s⁻¹ for the L=20 µm monolayer MoS₂ device in Figure 5c,d ($R_{\rm C}\approx 3.85$ M Ω µm, ($V_{\rm G}-V_{\rm T}$) ≈ 6 V). The value of extracted mobility is consistent with the range of values reported in literature for monolayer MoS₂. [17] The electrical characteristics of Au-exfoliated flakes are thus found to be similar to those of monolayer MoS₂ flakes from tape exfoliation. The long channel length MOSFETs (L=20 and 30 µm) further demonstrate the uniformity of the monolayer MoS₂ flakes over a large area which is essential for building complex circuits.

In conclusion, we have demonstrated an exfoliation technique utilizing the interaction strength of Au with chalcogens to preferentially obtain large areas of monolayer TMDCs on various substrates with high yield compared to the standard tape exfoliation method. Through extensive electrical and





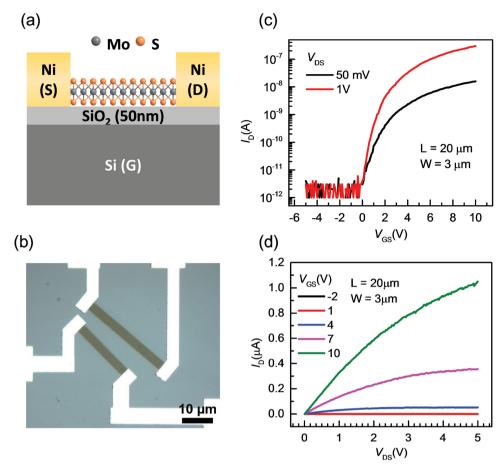


Figure 5. a) Schematic illustration of a monolayer MoS₂ back-gated MOSFET. b) Representative completed device post-MoS₂ patterned etching and contact formation. c,d) I_D – V_{CS} and I_D – V_{DS} data for L = 20 μ m, W = 3 μ m device, respectively.

optical characterization we conclude that the quality of flakes obtained by the tape exfoliation and by the Au exfoliation techniques is the same. Ultralarge optoelectronically perfect MoS_2 monolayers with a PL QY of $\approx 100\%$ were obtained after treatment with an organic superacid. Standard XPS performed on ultralarge area monolayer MoS_2 illustrates the importance of this technique for expanding research capabilities. Automating and mechanizing the transfer process described in this work may be explored in the future for more controlled exfoliation and transfer of TMDC monolayers onto desired substrates. Ultimately, this technique can advance the possibility of large scale fabrication of monolayer TMDC devices.

Experimental Section

XPS Measurements: XPS measurements were performed on a Kratos AXIS Ultra-DLD (delay-line detector) spectrometer, using a monochromatic Al K-Alpha excitation source (spot size is 1 mm \times 2 mm). Instrument work function was calibrated immediately prior to the measurement using a sputtered Au reference, utilizing the valence band Fermi edge and Au 4f position for the energy reference. Measurement of the bulk material was performed on a freshly exfoliated surface, using a slot aperture defining an area $\approx\!700~\mu m \times 300~\mu m$. Measurement of a large ($\approx\!50~\mu m \times 60~\mu m$) monolayer, exfoliated by Au exfoliation method and transferred using a dry pick-and-place

method^[12] to the silicon analysis substrate, was performed using the same excitation source and pass energy resolution, and the field of view was limited to $\approx\!400~\mu m \times 400~\mu m$ by tuning of the electrostatic lens and use of a small area aperture. Charge correction for the MoS $_2$ samples was performed using the adventitious carbon C 1s peak position. [18]

Molybdenum and sulfur core level curve fits were performed using Voigt line shapes, with doublet spin orbit splitting area ratios defined to the normal 3:2 ratio for the 3d 5/2 and 3/2 components. Background subtraction was performed in combination with curve fits using the well-known Shirley background for the Au reference and MoS_2 bulk sample, and an alternative linear background for the monolayer. This alternate was necessary due to a more prominent linear background lineshape present in the Mo 3d and S 2s analysis area, likely due to substrate contributions to the spectra.

Raman and PL Mapping: High-resolution PL mapping was performed using a WITec Alpha 300RA equipped with a scanning stage. The sample was excited using the 532 nm line of a frequency-doubled Nd:YAG (neodymium-doped yttrium aluminium garnet) laser as the excitation source and focused on the sample using a 100× objective. The laser power is 2 μW with a diffraction-limited spot size. QY measurement conditions are described in ref. [15]

Sample Preparation for Photoluminescence Quantum Yield Measurements: For quantum yield measurements MoS_2 (SPI Supplies) was exfoliated on quartz (using Au exfoliation method). Monolayers were identified by optical contrast. Samples were treated using TFSI (Sigma-Aldrich) using the following preparation procedure: TFSI (20 mg) was dissolved in 1,2-dichloroethane (10 mL) (DCE, Sigma-Aldrich) to make a 2 mg mL $^{-1}$ solution. The solution is further diluted with 1,2-dichlorobenzene (Sigma-Aldrich) or DCE to make a 0.2 mg mL $^{-1}$

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TFSI solution. The sample was then immersed in the 0.2 mg mL⁻¹ solution in a tightly closed vial for 10 min on a hot plate (100 °C). The sample was removed and blow-dried with nitrogen without rinsing and subsequently annealed at 100 °C for 5 min.

Electrical Device Fabrication: Electron-beam lithography is used to pattern an etch mask using poly(methyl) methacrylate (PMMA) C4 resist (MicroChem Corp.). XeF₂ etching^[19] is used to then pattern the monolayer into long strips which form the channel region of the MOSFET. The resist is removed in acetone following which the sourcedrain contacts are patterned using electron-beam lithography. 40 nm Ni is evaporated using thermal evaporation followed by subsequent liftoff in acetone to form the contacts.

Supporting Information

Supporting Information is available from the Wiley Online Library or from the author.

Acknowledgements

S.B.D., S.R.M., and M.A. contributed equally to this work. This work was supported by the Electronics Materials program funded by the Director, Office of Science, Office of Basic Energy Sciences, Material Sciences and Engineering Division of the U. S. Department of Energy under Contract No. DE-AC02-05CH11231. XPS measurements were performed at the Joint Center for Artificial Photosynthesis (JCAP), a Department of Energy (DOE) Energy Innovation Hub, supported through the Office of Science of the U.S. Department of Energy under Award No. DE-SC0004993.

> Received: December 11, 2015 Revised: January 25, 2016 Published online: March 23, 2016

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