

Title: **Isolated Power**

File: PowerIsolation.SchDoc

Sheet: 2 of 5

Revision: C

Time: 5:35:44 PM

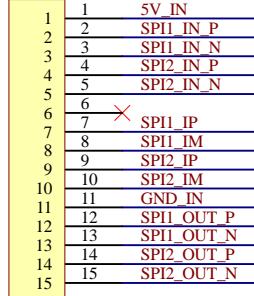
Date: 4/15/2020

Engineer: Ashad Farhan



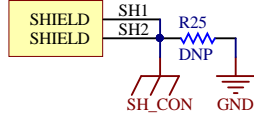
CON1A

ICD15S13E6GX00LF



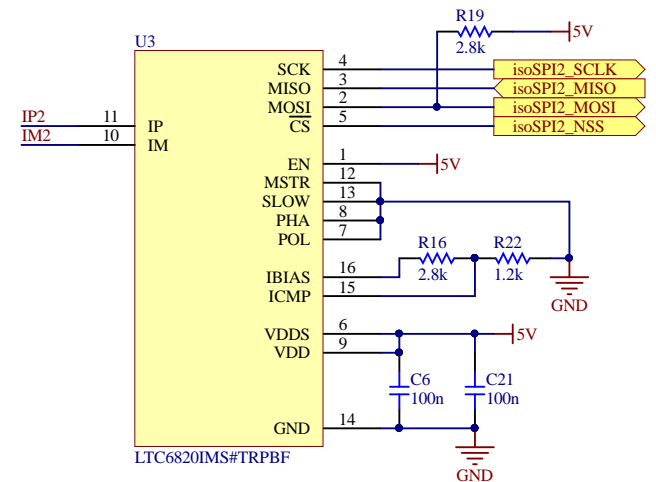
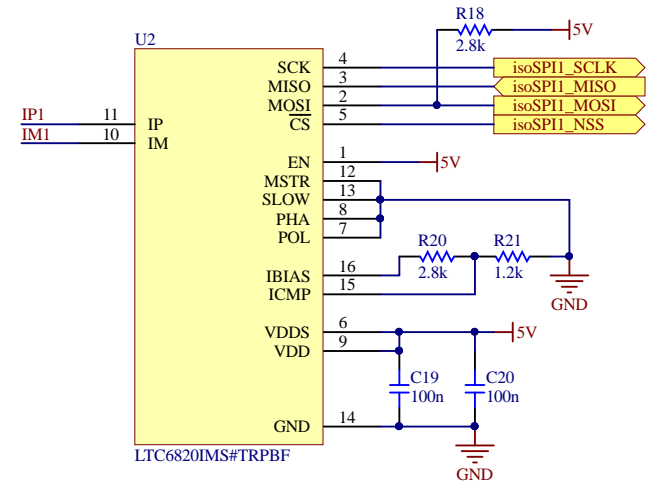
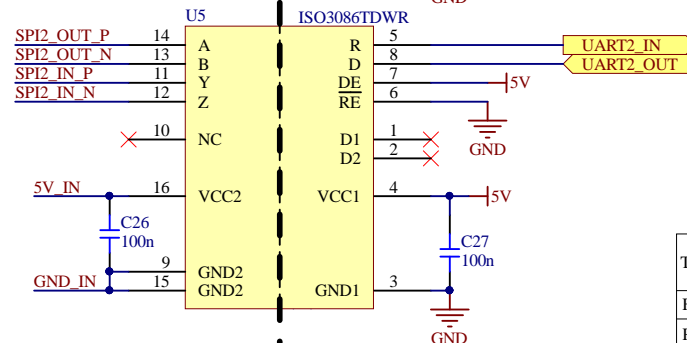
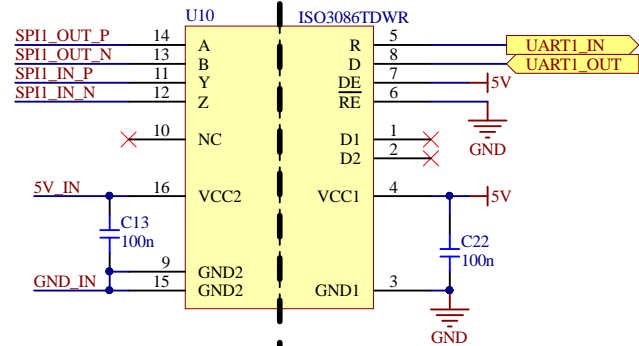
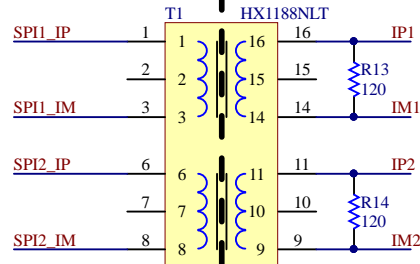
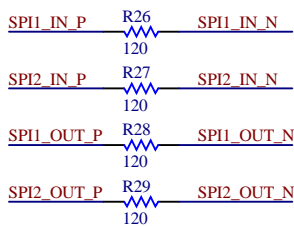
CON1B

ICD15S13E6GX00LF



NOTE: HD15 CON labels match AMDC, so IN / OUT are flipped for this board.

IN is our output
OUT is our input

Title: **Connector + Isolation**

File: ConnectorIsolation.SchDoc

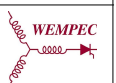
Sheet: 3 of 5

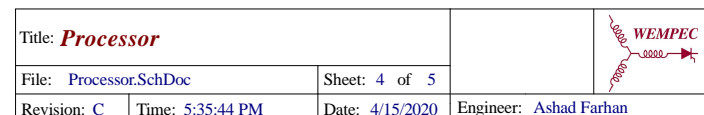
Revision: C

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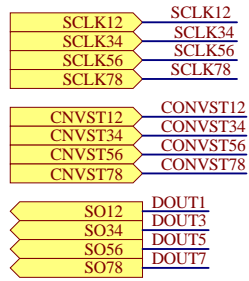
Date: 4/15/2020

Engineer: Nathan Petersen

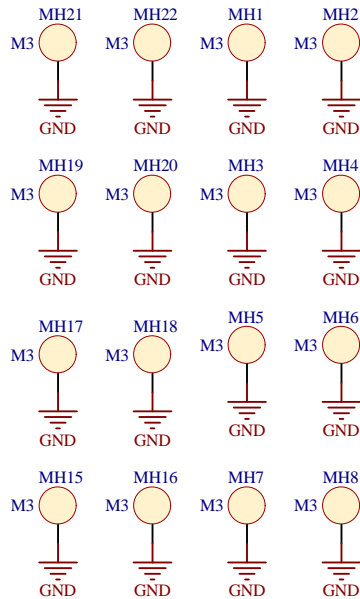




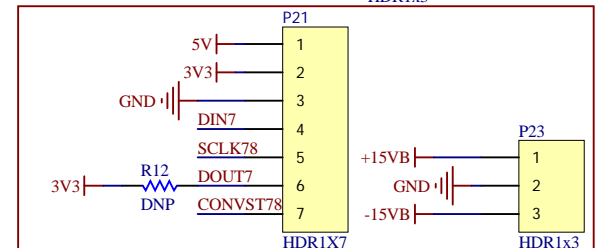
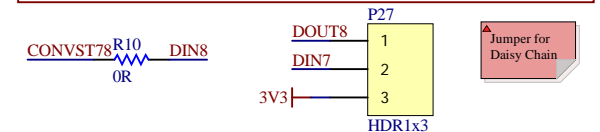
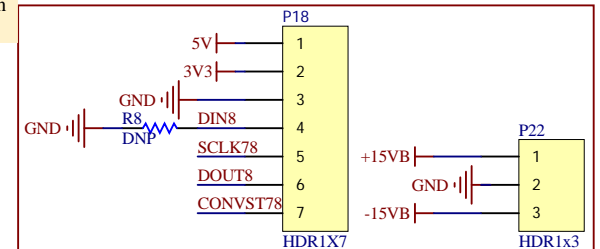
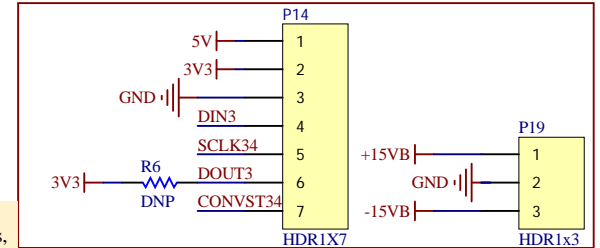
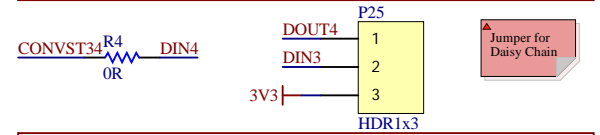
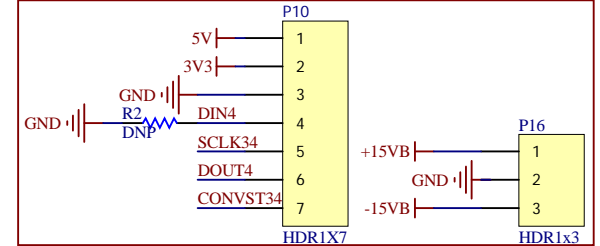
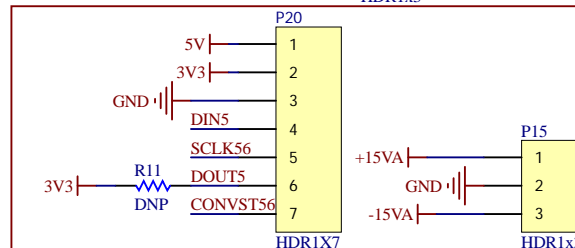
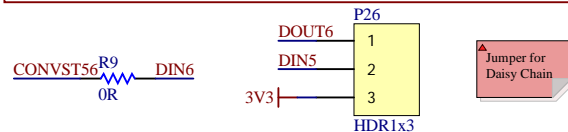
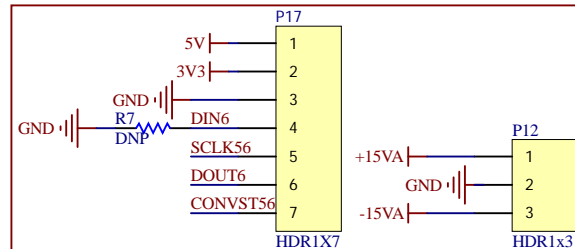
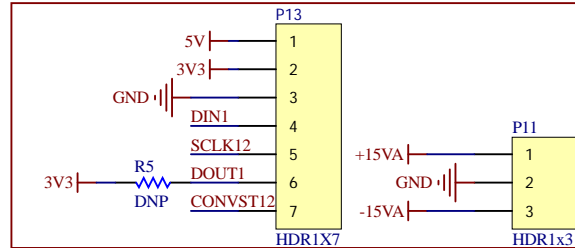
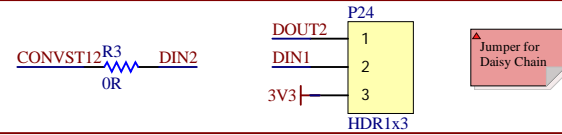
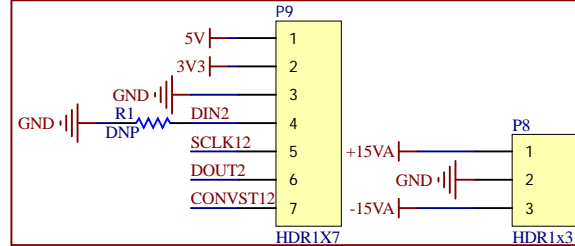
Processor Interface



Daughterboard Mounting



Daughterboard Connections



Eight daughterboards, divided into four pairs, each pair has two boards connected in daisy chain connection