

A

B

C

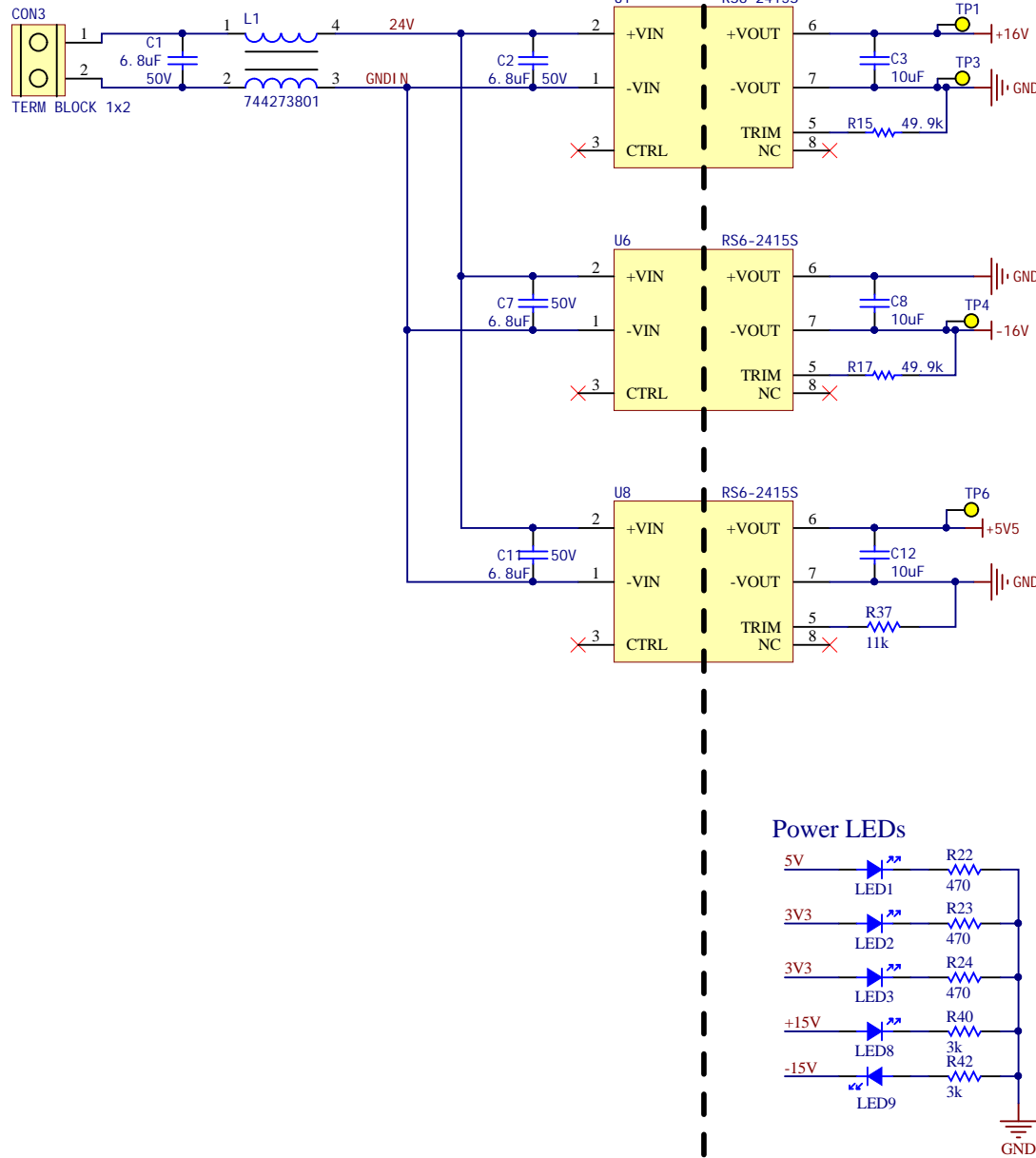
D

A

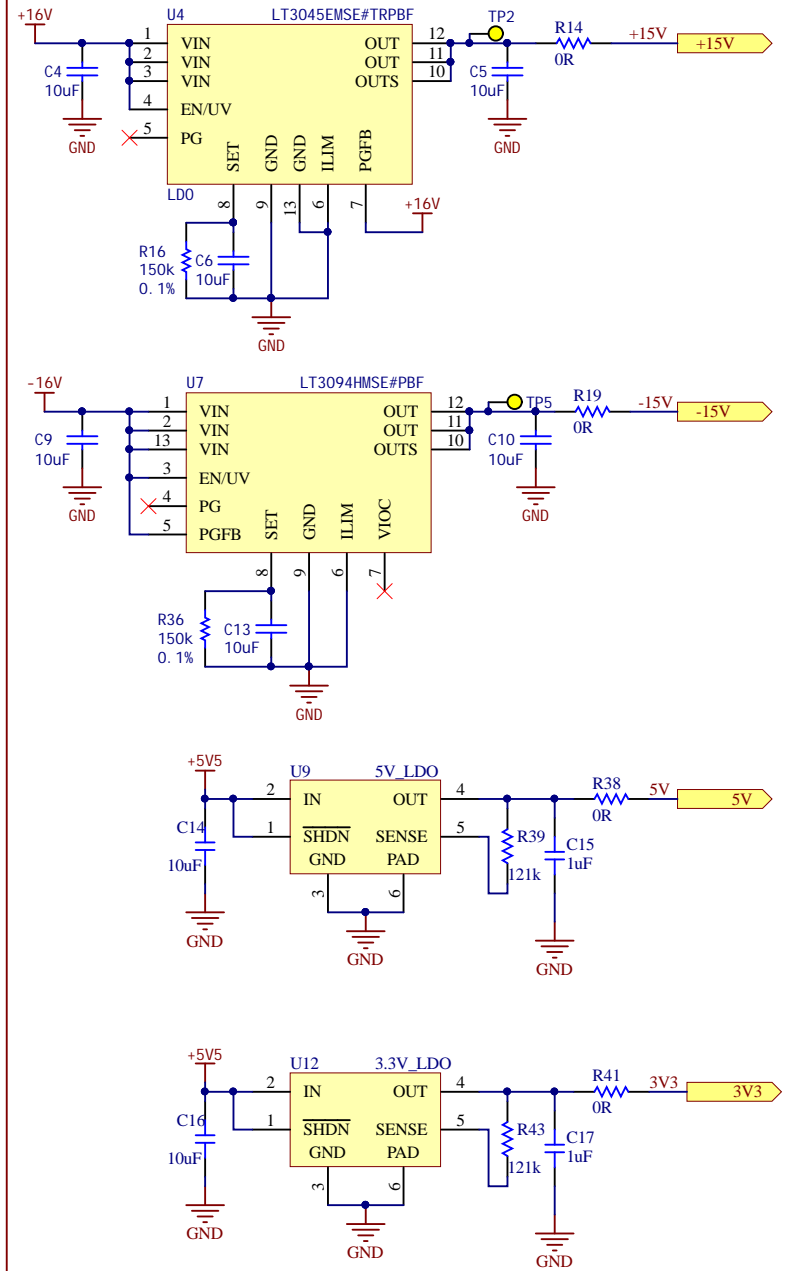
B

C

D



## LDOs



Title: **Isolated Power**

File: PowerIsolation.SchDoc

Sheet: 2 of 5

Revision:

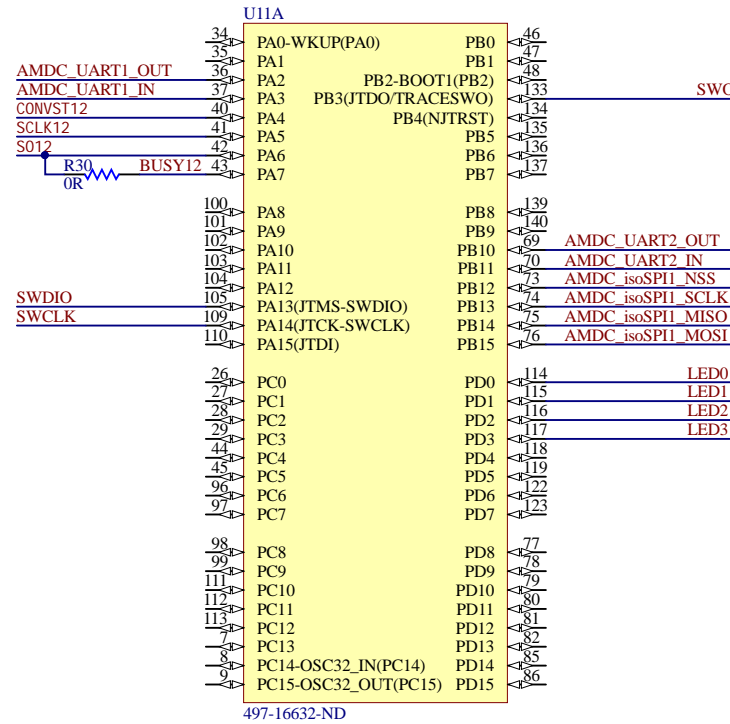
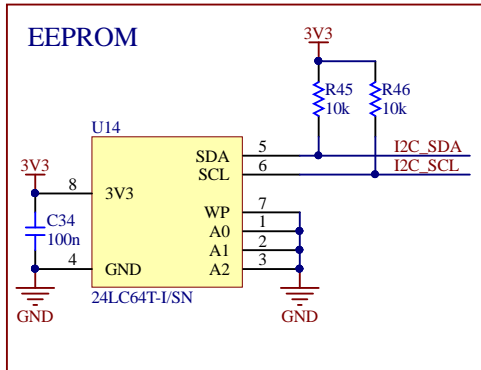
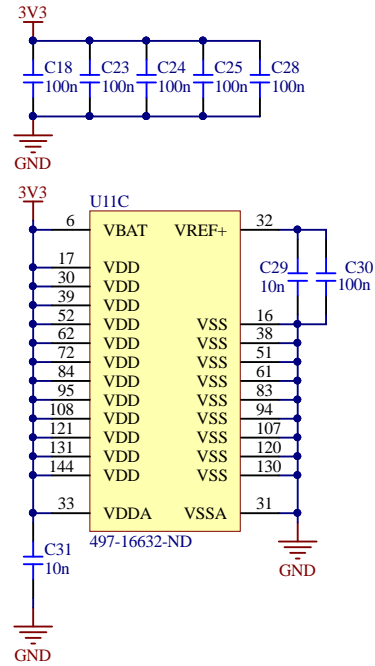
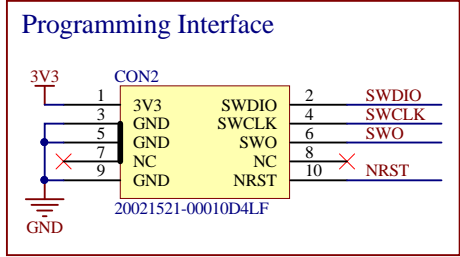
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Date: 4/9/2020

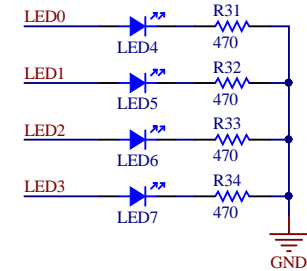
Engineer: Ashad Farhan



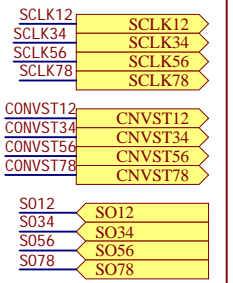




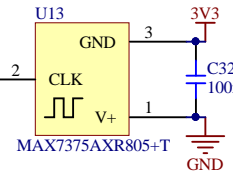
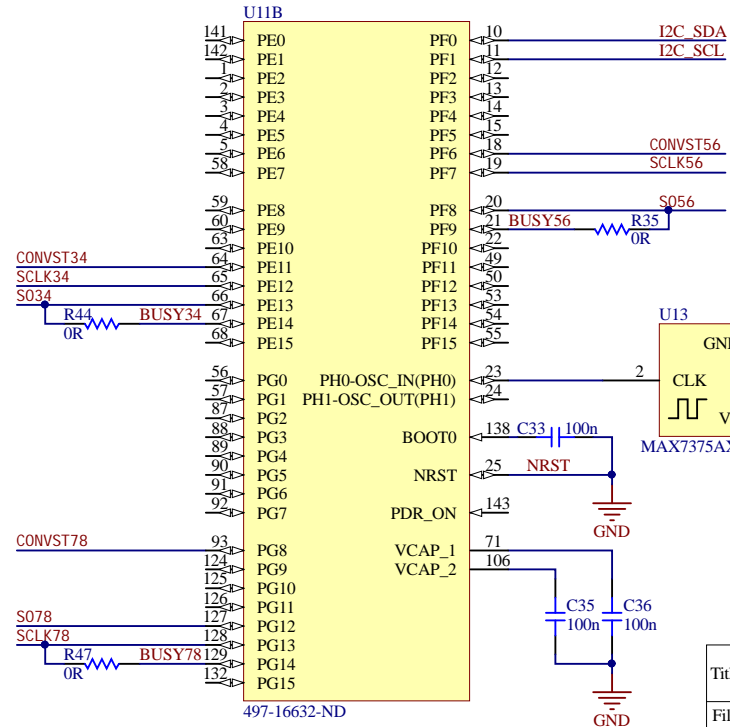
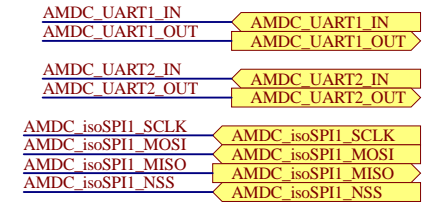
### Status LEDs



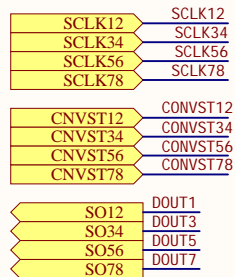
### Sensor Card Interface



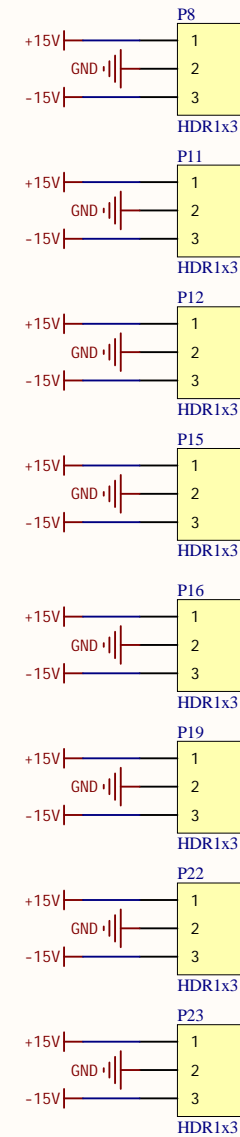
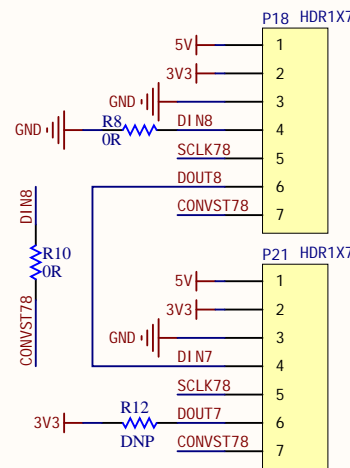
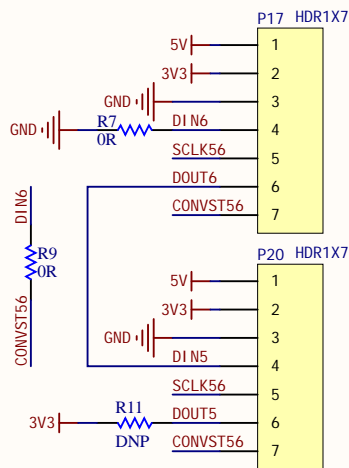
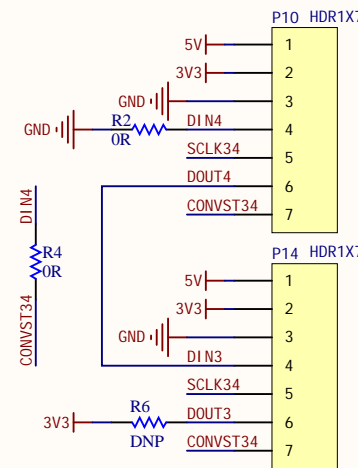
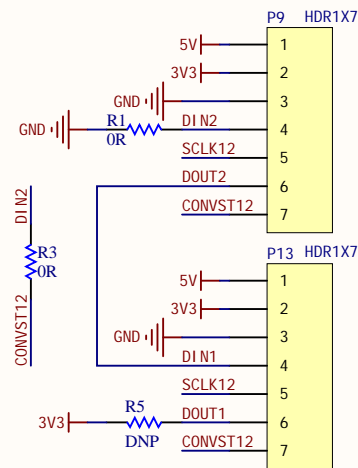
### AMDC Interface



## Processor Interface



## Daughterboard Connections



Eight daughterboards, divided into four pairs, each pair has two boards connected in daisy chain connection

