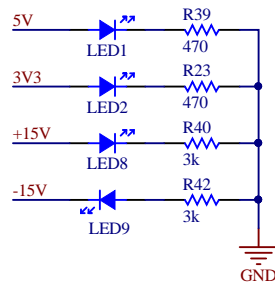
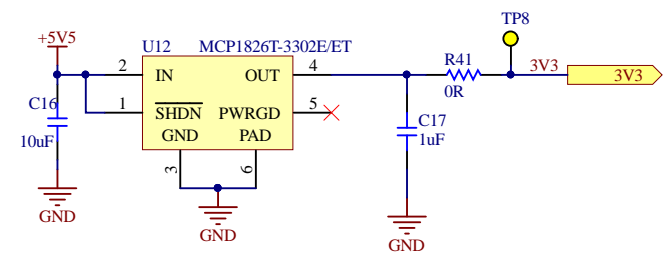
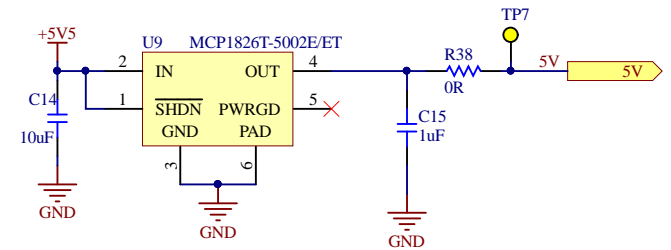
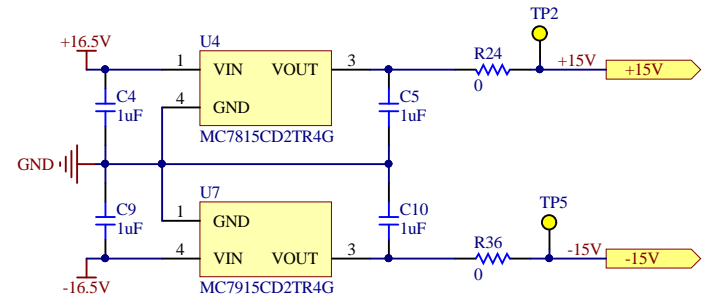


### Power LEDs



### LDOs



Title: **Isolated Power**

File: PowerIsolation.SchDoc

Sheet: 2 of 5

Revision: C

Time: 2:45:37 PM

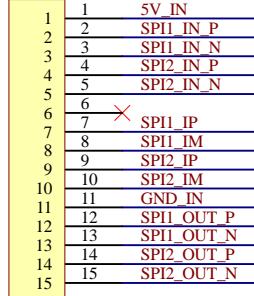
Date: 4/13/2020

Engineer: Ashad Farhan



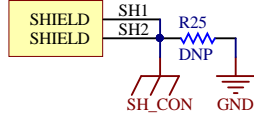
CON1A

ICD15S13E6GX00LF



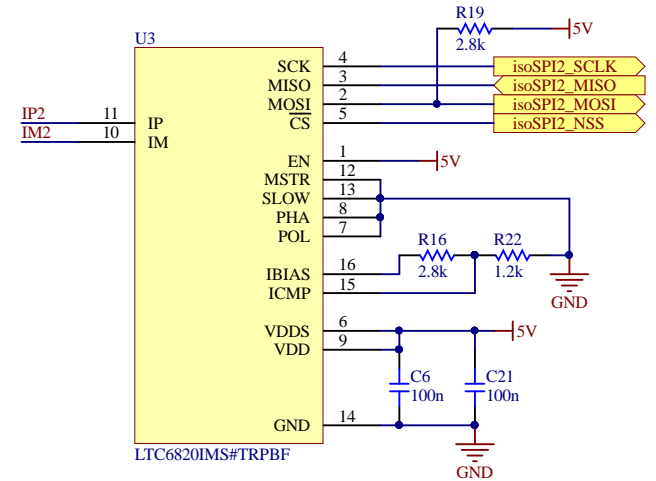
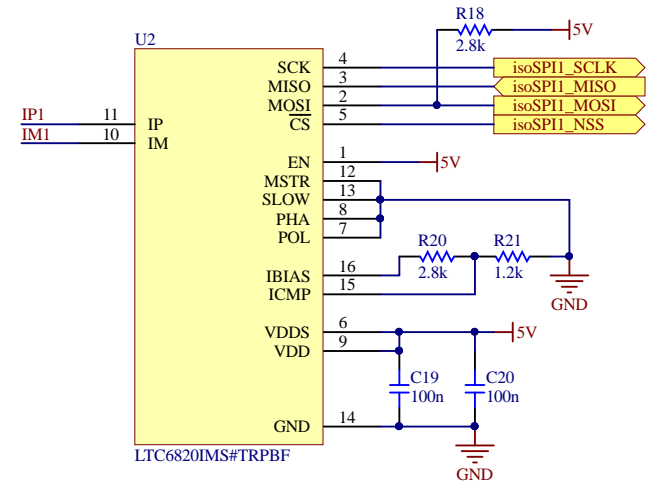
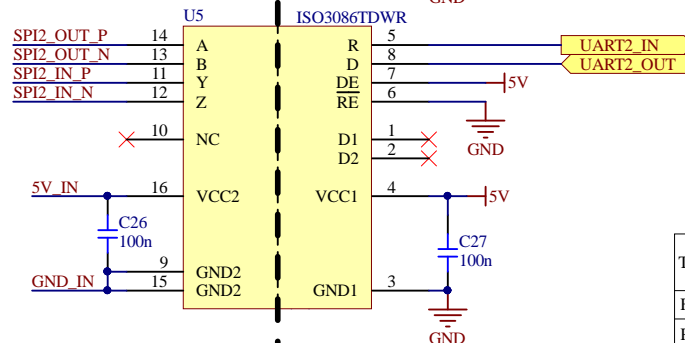
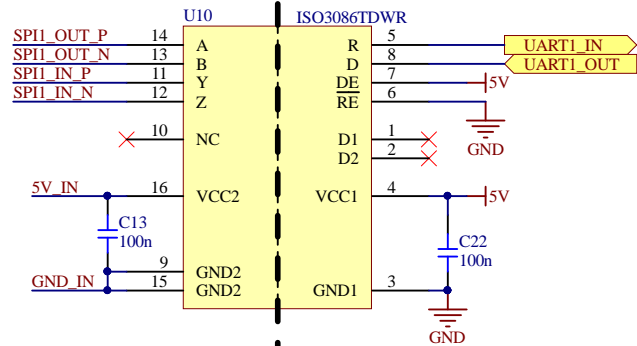
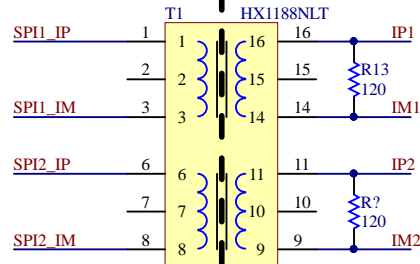
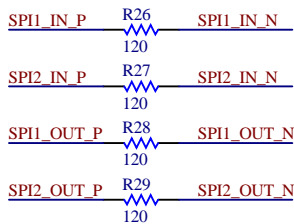
CON1B

ICD15S13E6GX00LF



NOTE: HD15 CON labels match AMDC, so IN / OUT are flipped for this board.

IN is our output  
OUT is our input

Title: **Connector + Isolation**

File: ConnectorIsolation.SchDoc

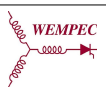
Sheet: 3 of 5

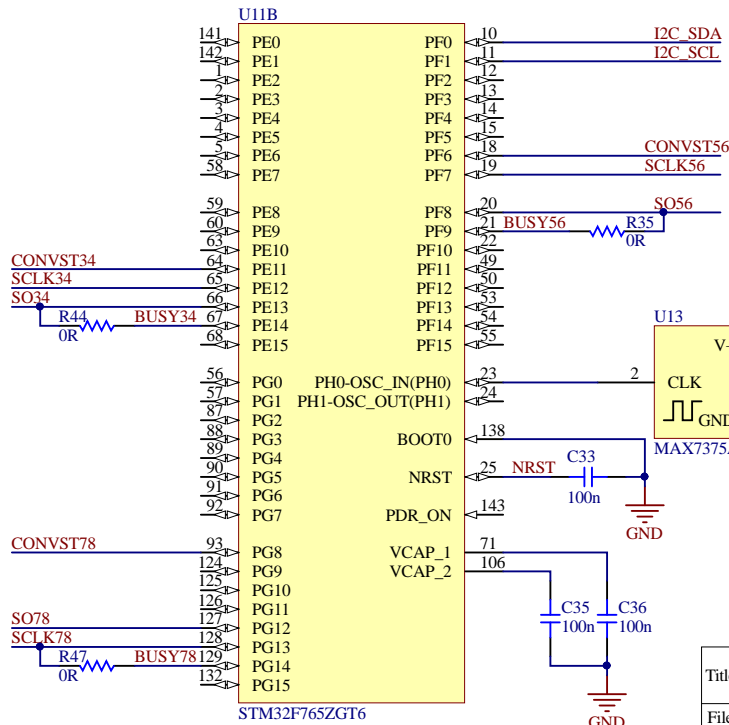
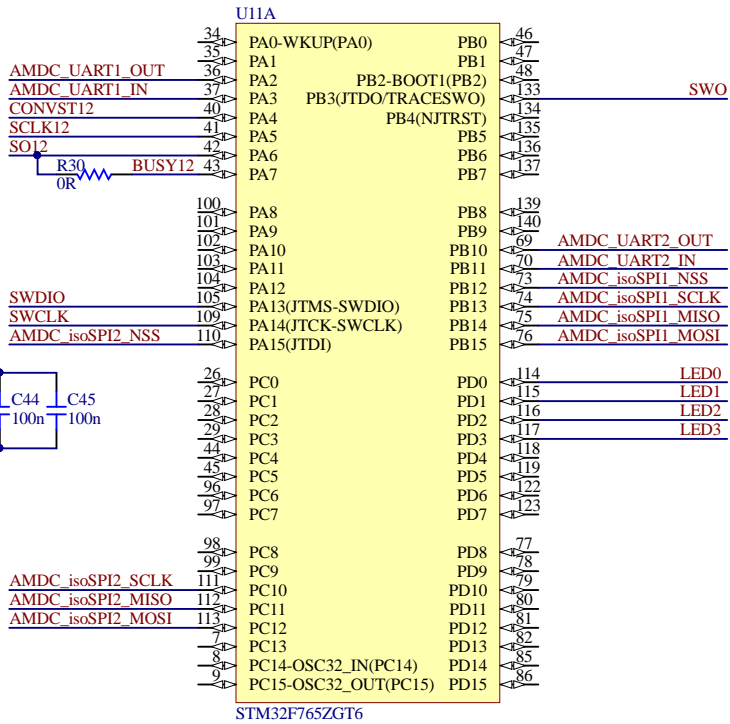
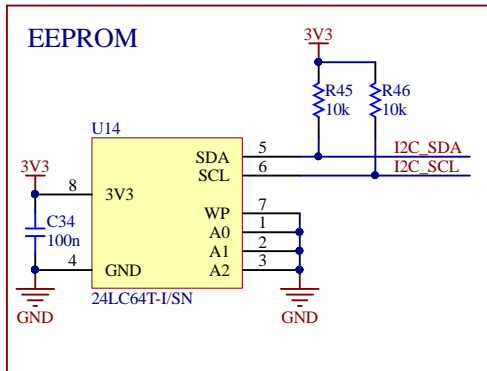
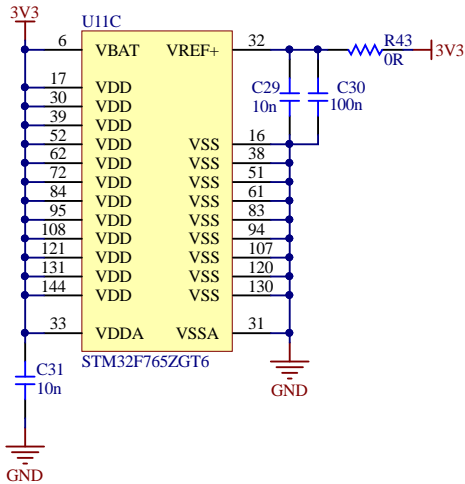
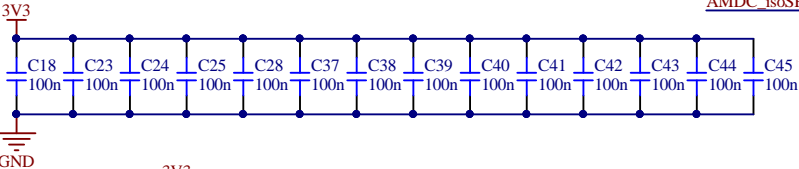
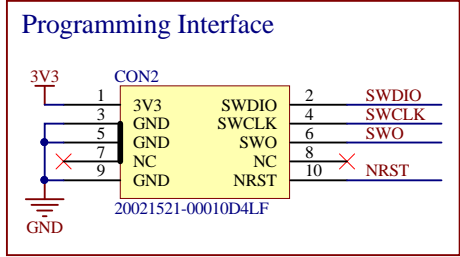
Revision: C

Time: 2:45:37 PM

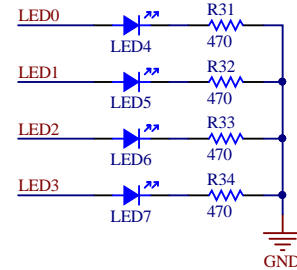
Date: 4/13/2020

Engineer: Nathan Petersen

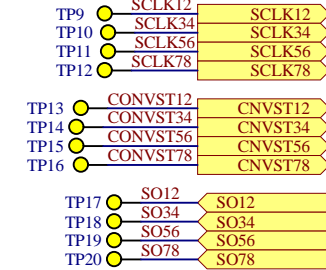




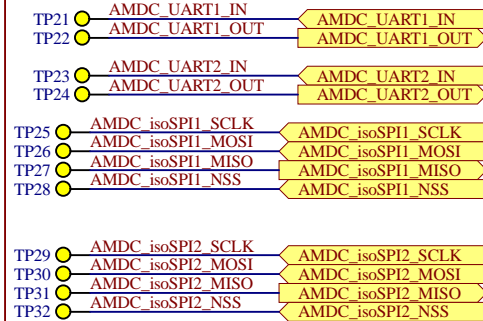
### Status LEDs



### Sensor Card Interface



### AMDC Interface



Title: **Processor**

File: Processor.SchDoc

Sheet: 4 of 5

Revision: C

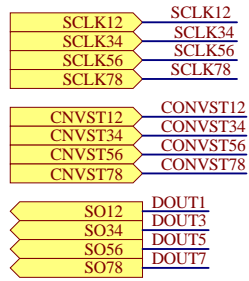
Time: 2:45:37 PM

Date: 4/13/2020

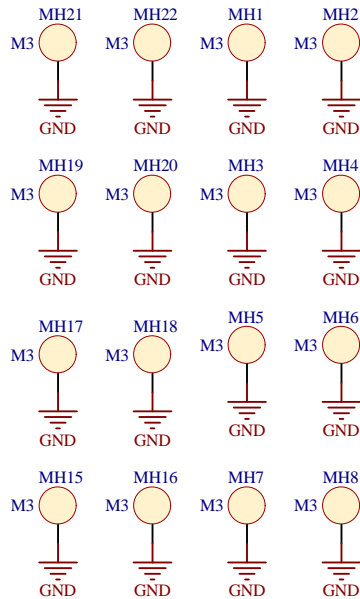
Engineer: Ashad Farhan



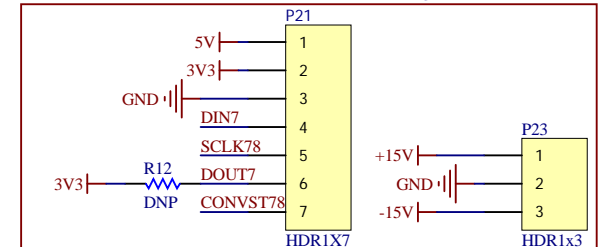
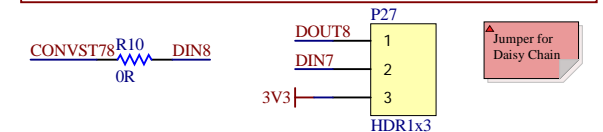
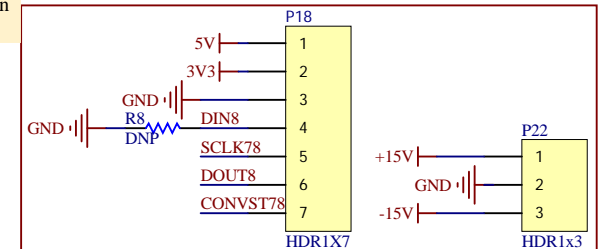
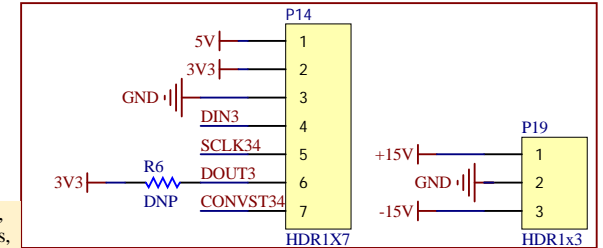
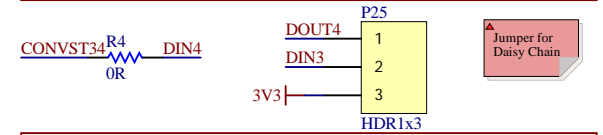
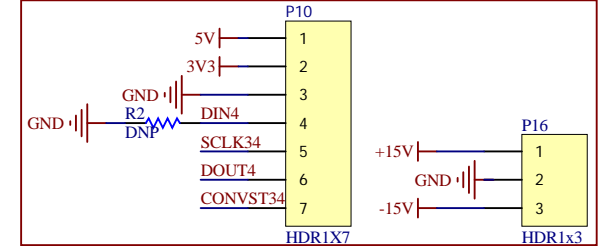
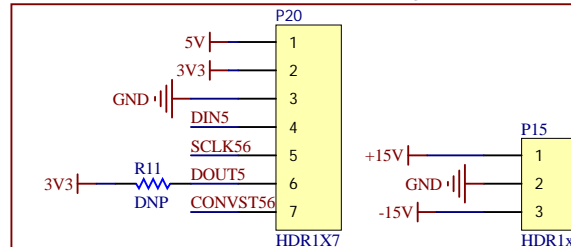
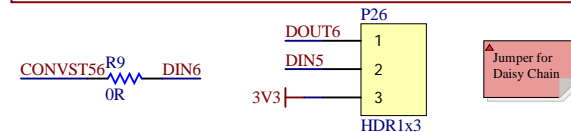
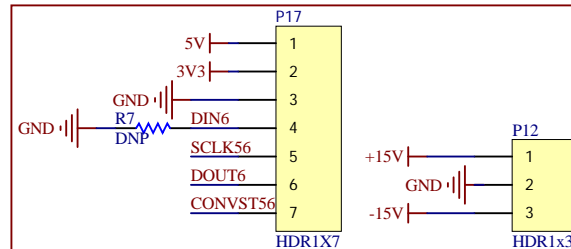
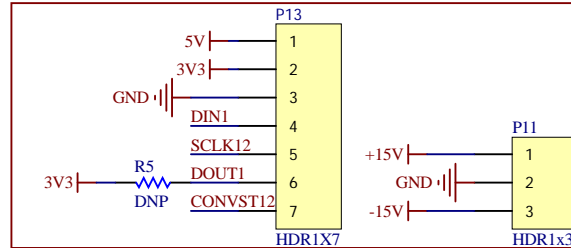
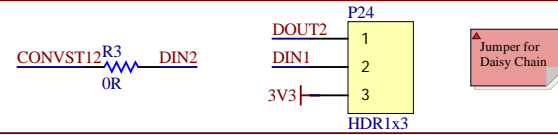
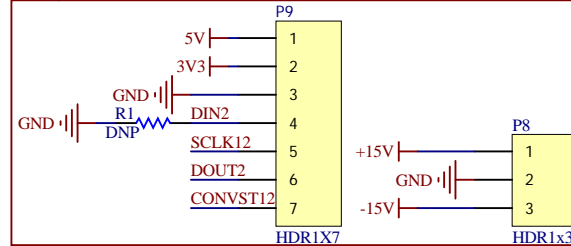
## Processor Interface



## Daughterboard Mounting



## Daughterboard Connections



Eight daughterboards, divided into four pairs, each pair has two boards connected in daisy chain connection