Problem 1:

Area of one transistor =
$$7 nm * 7 nm * 10 = 490 nm^2$$

Diameter of wafer =
$$3.048 * 10^8 nm$$

Area of wafer
$$= \left(\frac{3.048*10^8}{2}\right)^2 * \pi = 7.297*10^{16} \ nm$$

Number of dies =
$$\frac{7.298*10^{16} nm^2}{2000*490 nm^2} = \frac{7.447*10^{10} \frac{dies}{wafer}}{}$$

Problem 2:

The
$$\frac{cost}{die} = \frac{\$3500}{7.447*10^{10}} = \frac{\$4.700*10^{-8}}{die}$$

Problem 3

Assuming that a circular ink drop diameter is 100 um:

$$Area = \left(\frac{100*10^{-6}}{2}\right)^2 * \pi = 7.854 * 10^9 nm^2$$

Number of transistors =
$$\frac{7.854*10^9 \text{ } nm^2}{490 \text{ } nm^2} = \frac{1.603*10^7}{1.603*10^7}$$

Problem 4:

Some can be turned off when not needed, reducing heat/power consumption. Also, lower frequency means less power consumed by parasitics.

Problem 5:

Feature size of 7 nm process = 7 nm

Diameter of a silicon atom = 210 pm = 0.210 nm

$$\frac{7 nm}{.210 nm} = 33.33 \text{ times larger}.$$

Diameter of SiO₂ about 310 pm = .310 nm

$$\frac{7 nm}{.310 nm} = \frac{22.58}{10 nm}$$
 times larger.

Diameter of a human hair = 100 um = 100,000 nm

Problem 6

Samsung: \$62 Billion
Saudi Aramco: \$365 Billion
Nestle \$92 Billion

Problem 7:

<mark>10 nm</mark>

Problem 8:

a) For Core Intel i7 3930k P = 123.69W Current at 1.2V = $I = \frac{P}{V} = \frac{123.69W}{1.2V} = \frac{103A}{1.2V}$

b) For gold wire
$$\rho=1.16\Omega/{\rm inch}$$

$$R=\rho*L=1.16\Omega*\frac{1}{2}=0.58\Omega$$

$$V=I*R={\color{red}59.78V}$$

c) Power Dissipated =
$$P = I^2 * R = 103^2 * 0.58 = 6153 W$$

d) Fusing Current =
$$0.6 \sim 0.7$$
 A Actual Current = $0.06 \sim 0.07$ A Number of wires = $\frac{103}{.06} \sim \frac{103}{.07} = \frac{1471 \sim 1717}{0.000}$ gold wired

Problem 9:

| Туре | Storage Density (Bit/cm²) | Cost of Storage (\$/bit) | |
|-----------|---------------------------|-----------------------------|---------|
| CD | 107 | 10 ⁻¹¹ | |
| DVD | 108 | 10^{-12} | Lowest |
| Blue Ray | 109 | 10^{-12} | Lowest |
| Hard Disk | 10 ¹⁰ | 10^{-12} | Lowest |
| SRAM | 10^{7} | 10^{-6} | Highest |
| DRAM | 109 | 10^{-9} | |
| FLASH | 10^{10} | 10^{-10} | |

Ratio =
$$\frac{10^{-6}}{10^{-12}}$$
 = 10^{6}

Problem 11:

Techcrunch and HIS Markit report approximately 6.1 billion smartphones will be in use by 2020.

Problem 12:

From Gartner.com

| Android | 81.7% |
|------------|-------|
| iOS | 17.9% |
| Windows | 0.3% |
| BlackBerry | 0.0% |
| Other | 0.1% |

Problem 13:

From Gartner.com

Worldwide Smartphone sales in 2016 – 1,495,358,000 Worldwide Smartphone users in 2016 – 2,100,000,000

About 70% of smartphone users bought a new phone in 2016. This creates a large market potential each year and implies the useful life of a smartphone is about 1 - 2 years.

Problem 14:

Number of full time engineers =
$$\frac{\$500*.1*1495358000}{\$60000} = \frac{1.25*10^6}{\$60000}$$
 engineers

Problem 15:

Area of Skylane Chip = 82 mm²

a) Number of Skylane Chips/wafer =
$$\frac{\left(\frac{450 \text{ } mm}{2}\right)^2 * \pi}{82 \text{ } mm^2} = \frac{1939}{1939}$$

b)
$$Cost = \frac{\$2500}{1940*0.9} = \frac{\$1.43}{chip}$$

Problem 16:

```
h /home/jaaymond/ee330/verilog/EE330Homi
 Ln#
 1
        `timescale 1ns/1ps
 2
       module HW1_2NOR(iA, iB, out);
 \overline{4}
          input iA, iB;
 5
          output out;
 6
7
          wire out;
 8
          assign out = \sim(iA | iB)|;
 9
        endmodule
10
11
12
```

h]/home/jaaymond/ee330/verilog/EE330Homework/h Ln# 1 `timescale 1ns/1ps 2 3 module HW1_3AND(iA, iB, iC, out); 4 input iA, iB, iC; 5 6 7 output out; wire out; 8 assign out = iA*iB*iG; 9 endmodule 10 11

```
h /home/jaaymond/ee330/verilog/EE330Homework/HW1_tb.v (/HW1_tb) - Defau
 Ln#
 1
        timescale 1ns/1ps
       module HW1_tb();
 2
 3
         reg a, b, c;
 4
5
6
7
         wire oAnd, oNor;
         HW1_3AND myAnd( .iA(a), .iB(b), .iC(c), .out(oAnd) );
         HW1_2NOR myNor( .iA(a), .iB(b), .out(oNor) );
 8
         initial
 9
         begin
10
           a = 1'b0; b = 1'b0; c = 1'b0;
11
           #20;
12
           a = 1'b0; b = 1'b0; c = 1'b1;
13
           #20;
14
15
           a = 1'b0; b = 1'b1; c = 1'b0;
           #20;
16
           a = 1'b0; b = 1'b1; c = 1'b1;
17
           #20;
18
           a = 1'b1; b = 1'b0; c = 1'b0;
19
           #20;
20
           a = 1'b1; b = 1'b0; c = 1'b1;
21
22
23
24
25
26
           #20;
           a = 1'b1; b = 1'b1; c = 1'b0;
           a = 1'b1; b = 1'b1; c = 1'b1;
         end
27
28
29
       endmodule
```

