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EE330

Lab 8

Discrete Semiconductor Amplifiers

Introduction

In this lab we will be experiment with the small-signal amplifiers of MOS and Bipolar transistors. We will also view how to use BJTs and MOSFETs in both digital and analog applications. We will be measuring waveforms, operating points, and gains and all of these measurements should be made with the oscilloscope.

Part 1

we will be creating a voltage controlled amplifier using MOSFET transistor. Which means we can control the gain of the output voltage to input by changing the V_{CONT} voltage.

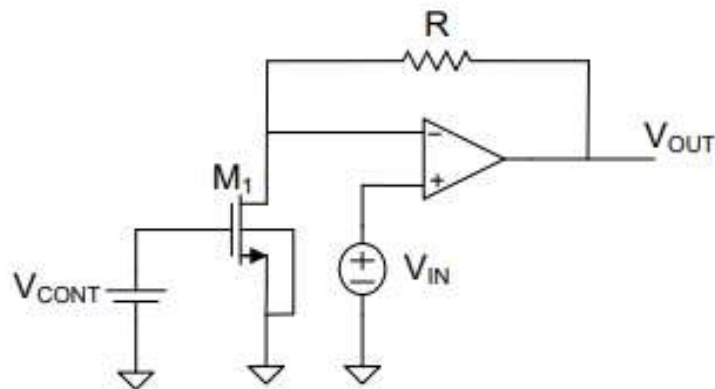


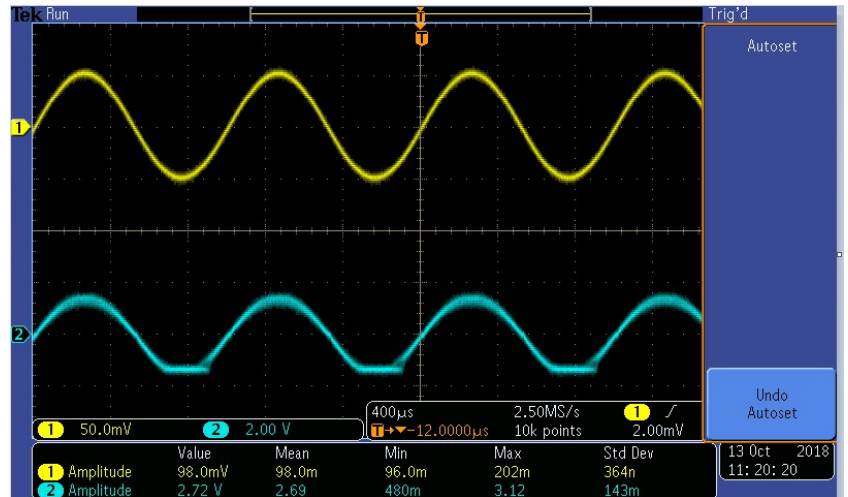
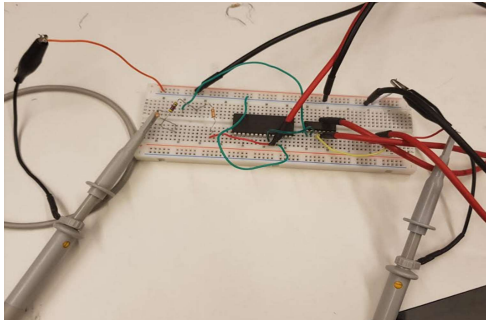
Fig. 1. Voltage Controlled Amplifier.

Calculations

determine R so that the voltage gain is 30 with $V_{CONT} = 2.5V$ then find V_{GS} with gain 10

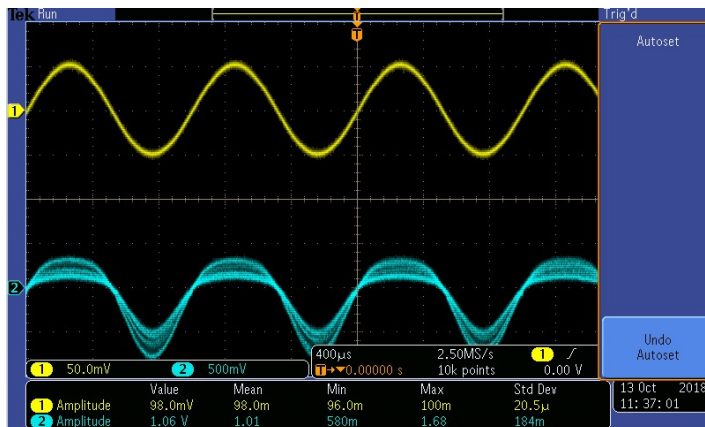


measurements



$$G = V_{out}/v_{in} = 2.72V/(0.098v) = 27.755$$

VCONT need to be around 1.25v in orther for the gain to 10 which is close to my calculation



In this case my output was a little bit instable but I was able to calculate the gain to be:

$$G = 1.06/0.98 = 10.81$$

Part 2 A Nonlinear Application

In this part of the lab, we will be applying two circuits below to determine the relationship between V_{OUT} and V_{IN} when V_{IN} is sinusoidal waveform within -2V and 2V (total of 4V) with 1KHz frequency. The circuit bellow

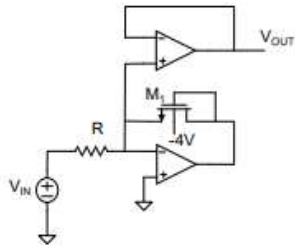


Figure 1

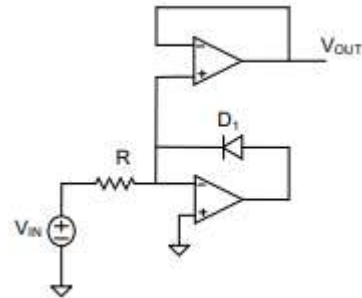
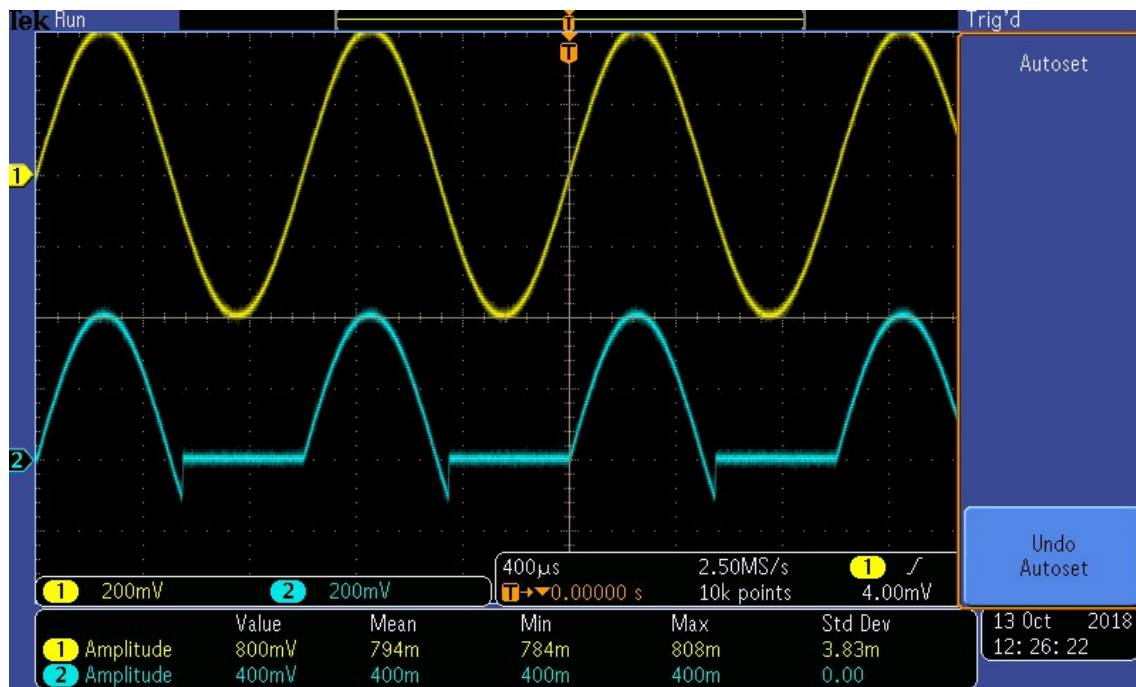


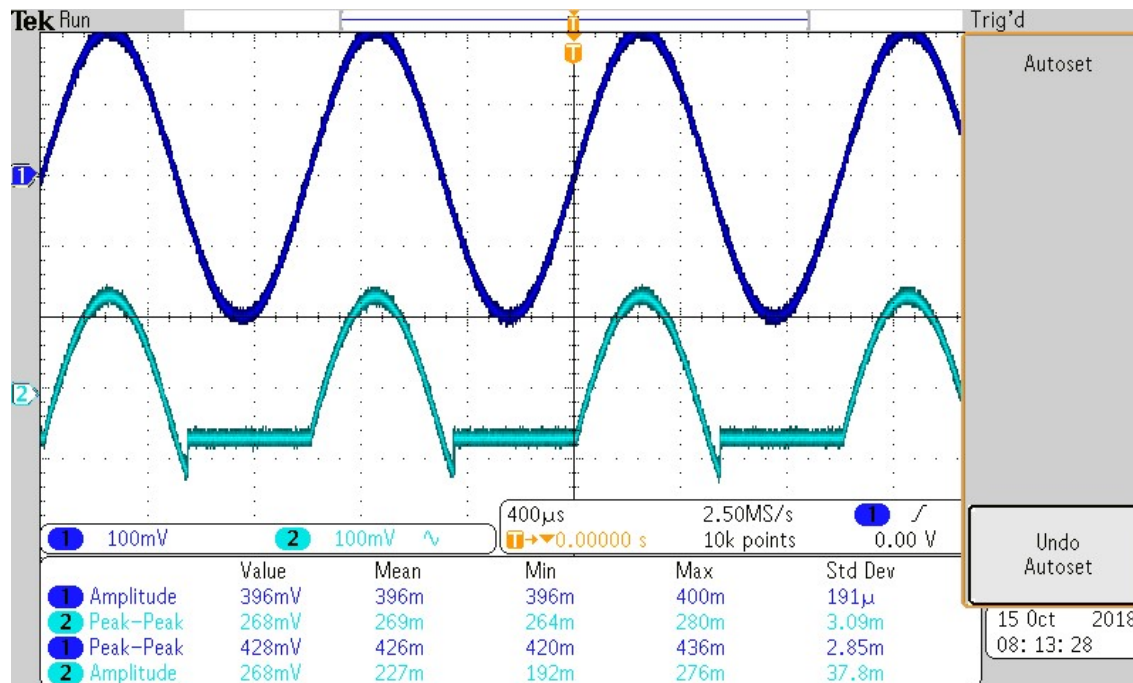
Figure 2

For figure 1



From the graph above we can see the gain is about $794/400=1.98$ which means there's not much amplification and also from the waveform we can easily identify that the MOSFET is functioning as a half-wave rectifier to convert from AC current to DC.

For figure 2



From this second graph we can see that the gain is $396/192 = 2$ which is almost the same as the one from figure 1 so we can conclude that the MOSFET function exactly like a diode with such kind of connection to perform half-wave rectifier.

Part 3 Common-Emitter Amplifier

we are given a Common-Emitter amplifier and we will need to determine the value of β for the BJT (PN2222) when it varies within the same model of device. I will be using:

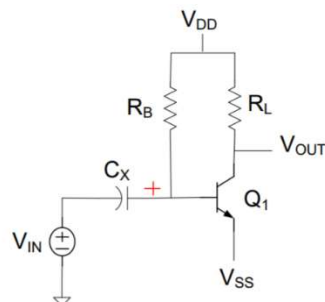
capacitor= 1uF

$R_B = 440 \text{ k}\Omega$

$V_{DD} = 12V$

$V_{SS} = 0V$

$R_L = 5 \text{ k}\Omega$.



a) Measure I_C and I_B

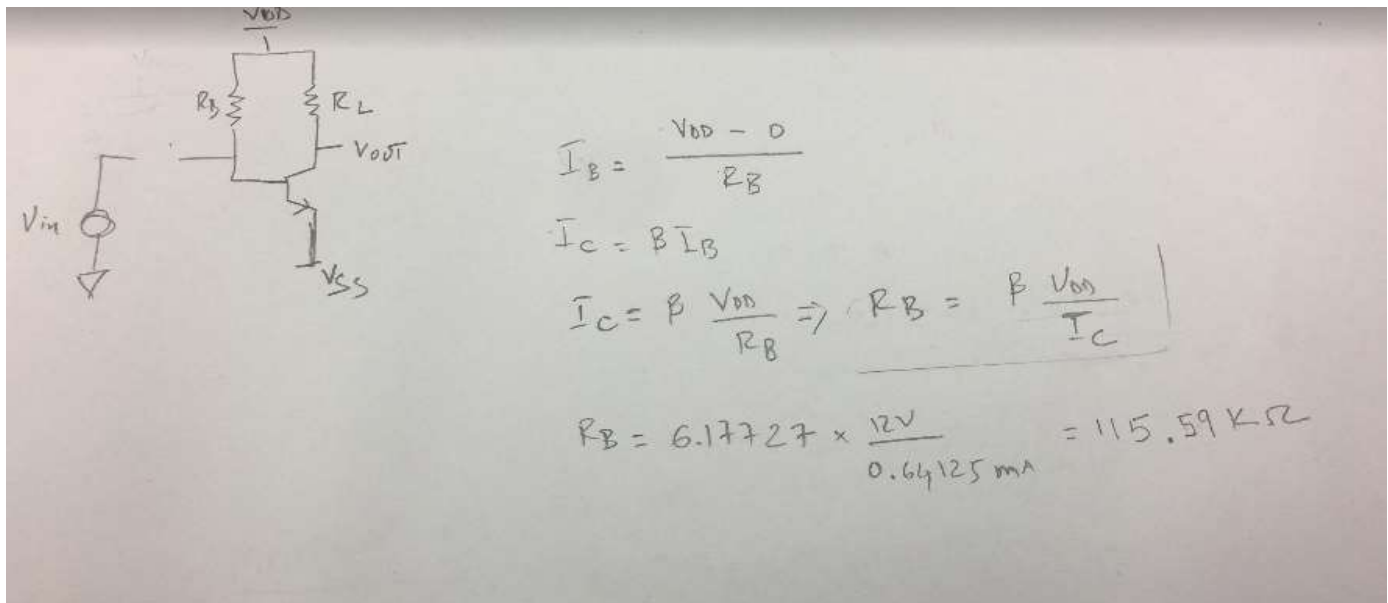
$I_C = 0.64125 \text{ mA}$

$I_B = 0.103808 \text{ mA}$

Calculate Beta using $I_C = \beta I_B$

$$\beta = I_C / I_B = 6.17727$$

b) Calculate R_B necessary to establish a quiescent collector current of 1mA.



The handwritten work for part b includes a circuit diagram of a common-emitter BJT amplifier. The input signal V_{in} is applied to the base through a resistor R_B . The collector is connected to a load resistor R_L and a DC supply V_{DD} . The output voltage V_{out} is taken from the collector. The emitter is connected to a DC supply V_{SS} .

The calculations shown are:

$$I_B = \frac{V_{DD} - 0}{R_B}$$

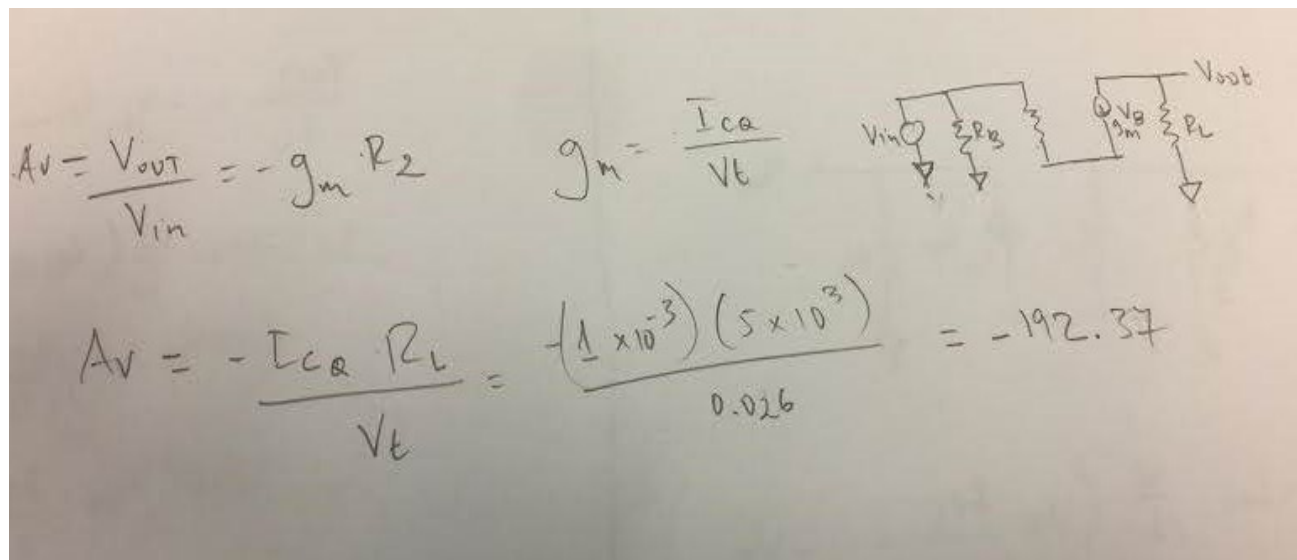
$$I_C = \beta I_B$$

$$I_C = \beta \frac{V_{DD}}{R_B} \Rightarrow R_B = \beta \frac{V_{DD}}{I_C}$$

$$R_B = 6.17727 \times \frac{12V}{0.64125 \text{ mA}} = 115.59 \text{ K}\Omega$$

c) Compare the theoretical small-signal voltage gain with what is measured for this circuit.

we will have to redraw the small signal analysis circuit diagram and find out the small signal gain.



The handwritten work for part c includes a small-signal equivalent circuit diagram. The input signal V_{in} is applied to the base through a resistor R_B . The collector is connected to a load resistor R_L and a DC supply V_{DD} . The output voltage V_{out} is taken from the collector. The emitter is connected to a DC supply V_{SS} .

The calculations shown are:

$$A_v = \frac{V_{out}}{V_{in}} = -g_m R_2$$

$$g_m = \frac{I_{CQ}}{V_t}$$

$$A_v = -\frac{I_{CQ} R_L}{V_t} = \frac{-(1 \times 10^{-3})(5 \times 10^3)}{0.026} = -192.37$$

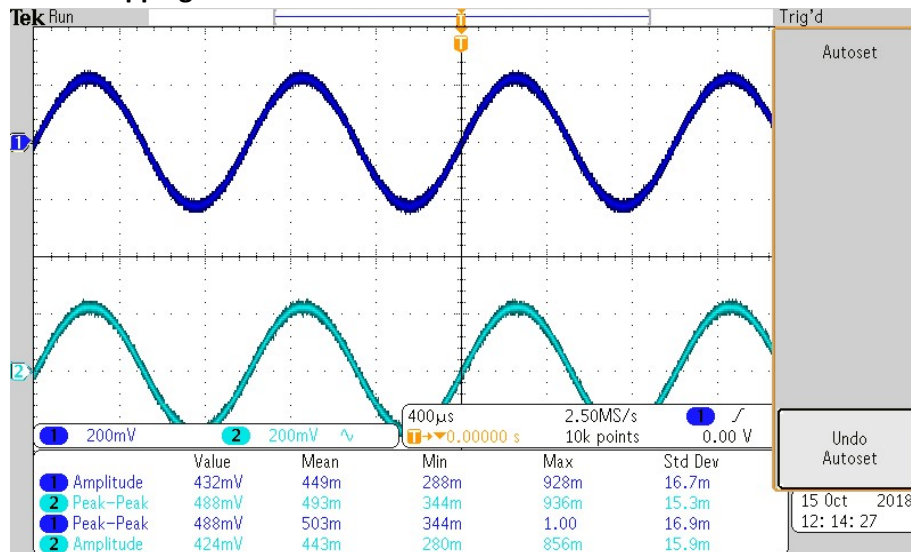
With the following information:

$I_{CQ} = 1\text{mA}$, $R_L = 5\text{K}\Omega$, $V_t = 0.026$ (By default in room temperature)

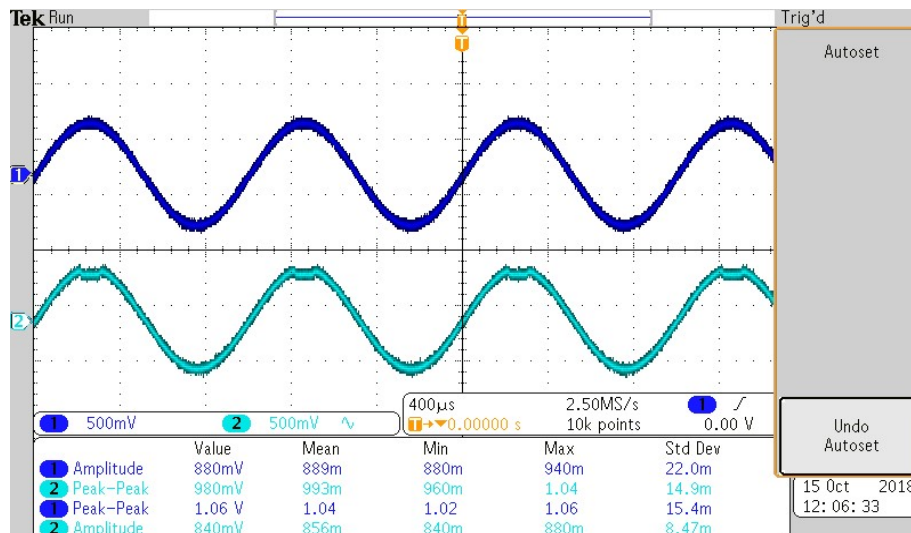
We find that $A_v = -192.37$.

- d) Gradually increase the amplitude of the input until clipping distortion is observed on the output.

Before clipping



After clipping



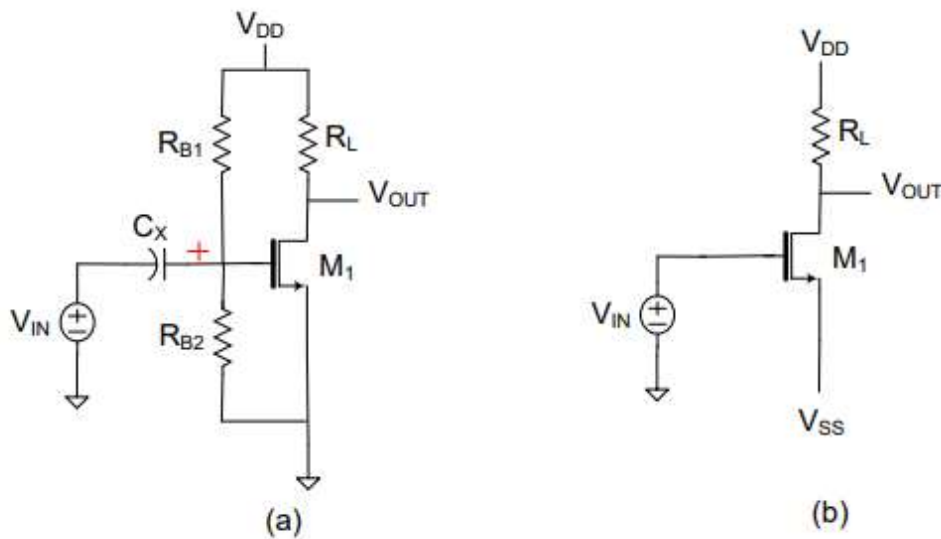
The maximum of voltage my voltage can reach before it starts clipping is 449 mv amplitude voltage. The clipping started early and was not very stable.

e) Change the frequency to 440 Hz and listen to the output signal

When I listen to the sound of the signal using speakers given. I was able to hear a significant difference as the voltage of V_{IN} increases. The sound becomes distorted and I think it might be due to the clipping at the output V_{OUT} as input increases.

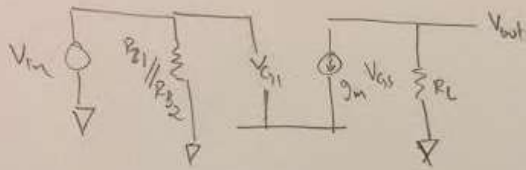
Part 4 Common Source Amplifier

In this part we will be Deriving an expression for and comparing the voltage gains of these two amplifiers if the transistor is operating in the saturation region.



Comparison of the gain of "a" and "b". the calculations of the gain are showing below. The gain of the "b" is 7.5 if the voltage divider part in the equation of "a" is 0. We can observe that the equations are the same except for the resistance value. "a" has two resistances to the input we have to account for but "b" does not.

(a)



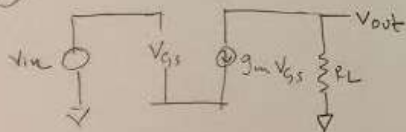
$$I_{DQ} = \mu_n C_{ox} \frac{W}{2L} \left(V_{DD} \frac{R_{B2}}{R_{B1} + R_{B2}} - V_{Tn} \right)^2$$

$$= (150 \times 10^{-6}) \left(\frac{60}{2(3)} \right) \left(V_{DD} \frac{R_{B2}}{R_{B1} + R_{B2}} - 0.5 \right)^2$$

$$= 1.5 \times 10^{-3} \left(V_{DD} \frac{R_{B2}}{R_{B1} + R_{B2}} - 0.5 \right)^2 \quad ; \quad V_{GS} \geq V_{in}$$

$$V_{DS} \geq V_{GS}$$

(b)



$$A_v = \frac{V_{out}}{V_{in}} = -g_m R_L \quad g_m = 3 \times 10^{-3} (0 - 0.5)$$

$$= -(-1.5 \times 10^{-3}) \times 5 \times 10^3 = 7.5$$

$$A_v = 7.5$$

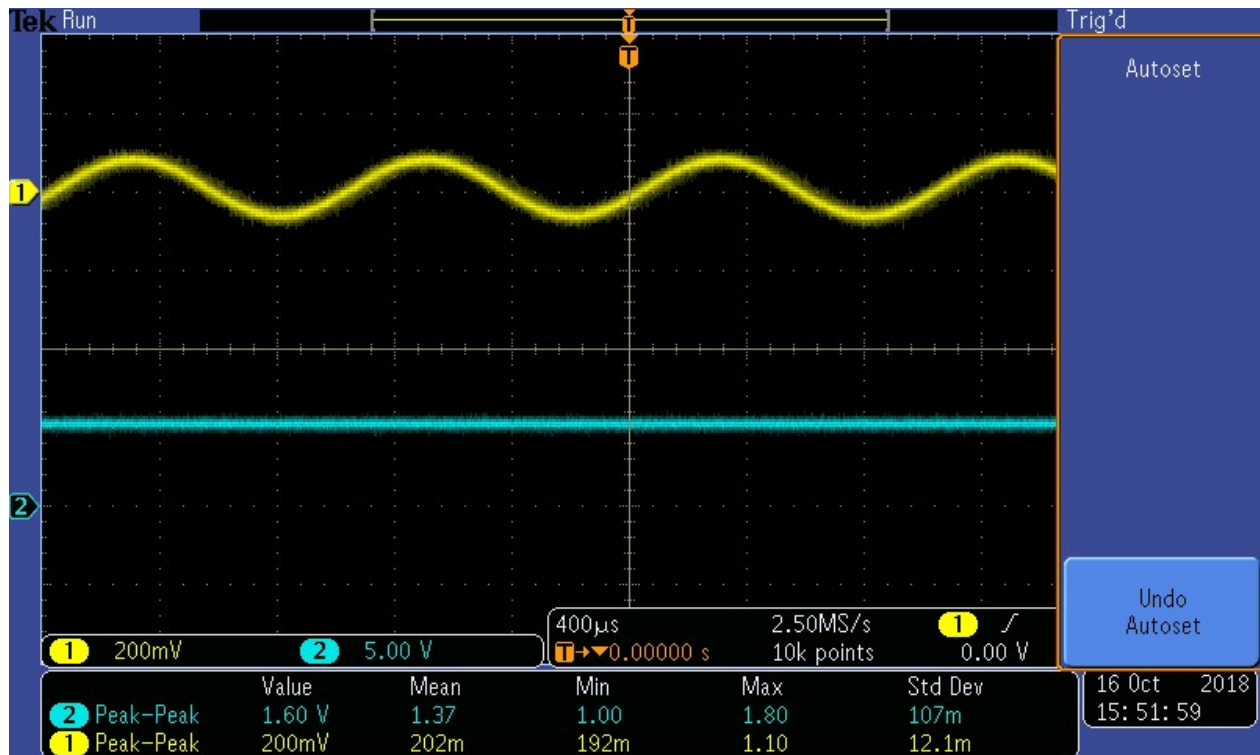
$$\frac{V_{out}}{R_L} = -g_m V_{in}$$

$$g_m = \mu_n C_{ox} \frac{W}{L} (V_{GS} - V_{Tn})$$

$$= (150 \mu A/V^2) \left(\frac{60}{5} \right) \left(V_{DD} \frac{R_{B2}}{R_{B1} + R_{B2}} - 0.5 \right)$$

$$\left\{ \begin{aligned} g_m &= 3 \times 10^{-3} \left(V_{DD} \frac{R_{B2}}{R_{B1} + R_{B2}} - 0.5 \right) \\ A_v &= \frac{V_{out}}{V_{in}} = -g_m R_L \end{aligned} \right.$$

I built circuit the b and test it



$$\text{Gain} = 1.37 / (202 \times 10^{-3}) = 6.88$$

The gain is almost 7 which is not exactly what I calculated but it is close. I think the lost might be due to the non-ideality of the transistor. My output appears like a DC signal but when I increase the amplitude of the input signal the output become a clipped wave form.

Part 5 Amplifier Design

In this part of the lab I will be designing an amplifier circuit using BJT which gives a small-signal gain as desired, I used a circuit similar to the one in part 3 to construct my design. I also used similar parameter as the part 3 circuit.

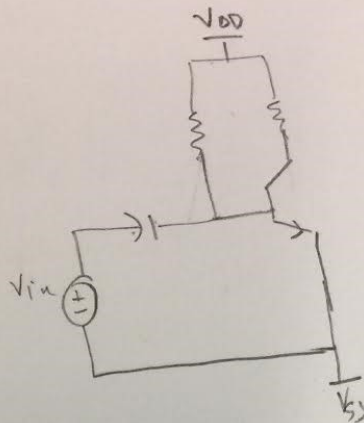
The calculation for my design are showing bellow and the parameter in the left upper corner.

$$G = AV = -10$$

$$R_L = 5 \text{ k}$$

$$V_t = 0.026 \text{ V}$$

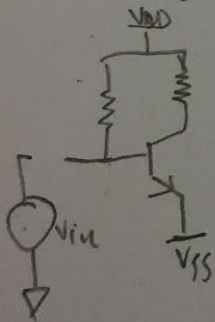
$$C = 10 \cdot \mu\text{F}$$



$$AV = \frac{-I_{CQ} R_L}{V_t}$$

$$I_{CQ} = \frac{(AV)(V_t)}{R_L} = \frac{-10 (0.026)}{5000} = -52 \cdot \mu\text{A}$$

$$I_C = \beta I_B \Rightarrow I_B = \frac{-52 \times 10^{-6}}{-10} = -5.2 \cdot \mu\text{A}$$



$$-I_{B1} = \frac{V_{DD} - V_{in}}{R_{B1}} = \frac{12 - 0}{-5.2 \times 10^{-6}} = 2 \text{ M}\Omega$$

The picture below was my actual circuit I used 5k resistor for my load (since I don't have an actual 5k resistor I put to resistor in parallel to form my 5 k resistance).

