EE 330 F12 HW 6 solution Due 9/28/2012

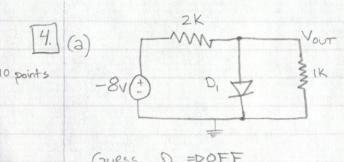
$$\frac{W}{L} = \frac{1}{R \times (M_{Ob}) \times (V_{OS} - V_{E})} = \frac{1}{1/K \times (S7.8_{E}6.9) \times (3.5 - .79)} = \frac{1}{3.19}$$

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$$\frac{W}{L} = \frac{1}{1/K \times (308_{E} - 6.3) \cdot (1.5 - .41)} = \frac{1}{1/49}$$

$$\frac{W}{L} \times (M_{Ob}) \times (1/49 - V_{E})$$

$$\frac{W}{L} \times$$



$$V_{\text{OUT}} = \left(-8_{\text{V}}\right) \left(\frac{1\text{K}}{1\text{K}+2\text{K}}\right)$$

$$V_{\text{OUT}} = -2.667\text{V}$$

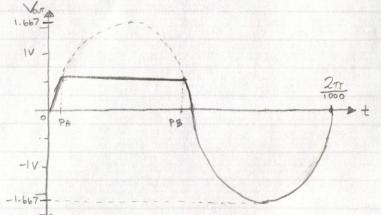
Check: VD=Vout = -2.667 < 0.6 V D, =>OFF & correct Guess

$$\frac{5 - V_{out}}{2k} = I_D + \frac{V_{out}}{1k} \quad let \quad I_S = (50 \mu m^2) \left(10^{-15} \frac{1}{1} \frac{1}{1$$

$$-V_{OUT}\left(\frac{1}{2k} + \frac{1}{1k}\right) + \frac{5}{2k} = (50)(15^{15})\left(e^{\frac{V_{OUT}}{26mV}} - 1\right)$$

$$V_{OUT} = 0.628 \text{ V}$$
 $I_{R_2} = \frac{V_{OUT}}{1K}$ 
 $I_{R_2} = 0.628 \text{ mA}$ 

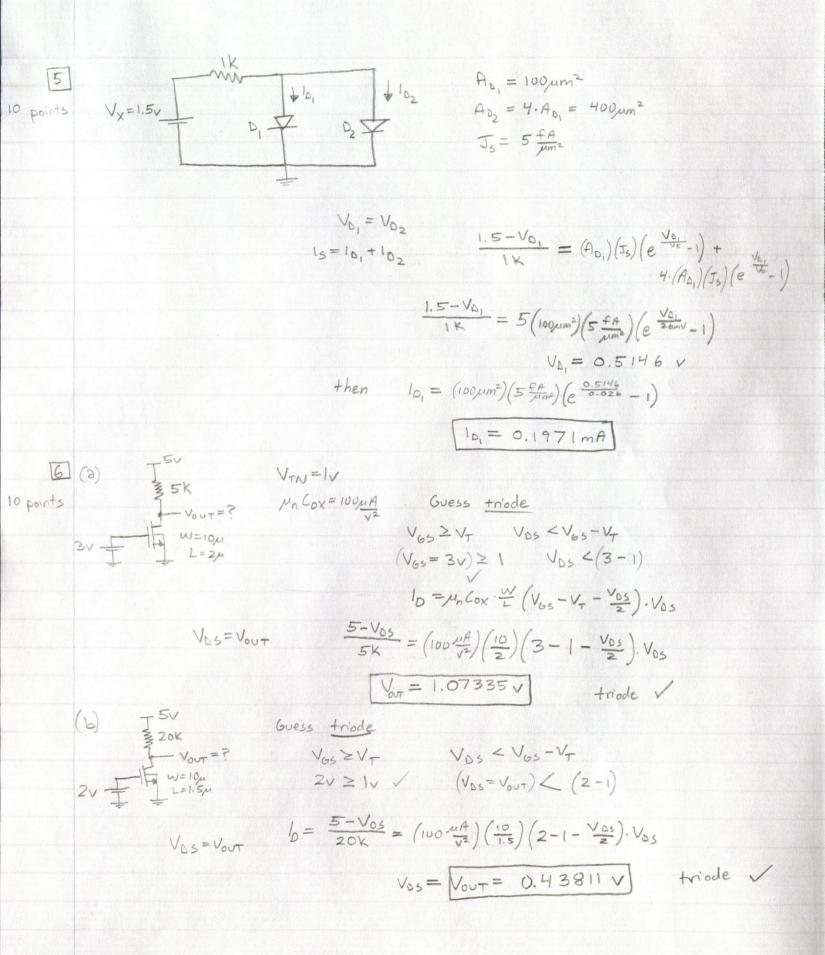
for 
$$V_{OUT} = \begin{cases} 0.6 \text{V} & \text{Vin} \ge 1.8 \text{V} \\ \frac{1}{3} \text{Vin} & \text{Vin} < 1.8 \text{V} \end{cases}$$

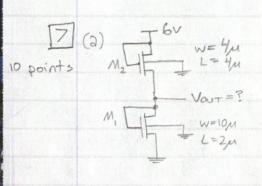


$$t(PA) = \frac{1}{1000} \sin^{-1}(0.6 \cdot \frac{3}{5})$$
  
= 0.368 ms

$$t(PB) = \frac{\binom{2\pi}{1000}}{2} - t(PA)$$

$$t(PB) = 2.77 \text{ ms}$$





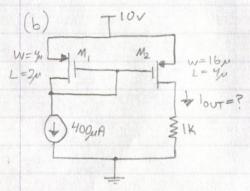
V<sub>TN</sub>=1 
$$\mu_n C_{ox}=100 \frac{\mu A}{v^2}$$
  
SATURATION Since  $V_{DS}=V_{GS} \rightarrow V_{TN}=1$   
40 for Both M1 + M2

$$|D_{1} = |D_{2}|$$

$$|M_{1}| \log \frac{W_{2}}{2 \cdot L_{2}} \cdot (V_{65_{2}} - V_{T})^{2} = |M_{1}| \log \frac{W_{1}}{2 \cdot L_{1}} \cdot (V_{65_{1}} - V_{T})^{2}$$

$$\frac{4}{2 \cdot 4} \left( (6 - V_{00T}) - 1 \right)^{2} = \frac{10}{2 \cdot 2} \cdot \left( V_{00T} - 1 \right)^{2}$$

$$|V_{00T} = 2.236 [V]$$



$$|_{D_1} = 400\mu A = -\mu_p C_{0x} \frac{w_1}{2L_1} \cdot (V_{65_1} - V_{6})^2$$

$$409\mu A = \left(33 \frac{\mu A}{V^2}\right) \left(\frac{4}{2 \cdot 2}\right) \left(V_{65_1} + 1\right)^2$$

$$V_{65_1} = -4.48155$$

$$V_{65_1} = V_{65_2}$$

$$I_{D_2} = 33 \frac{\mu A}{v^2} \left(\frac{16}{2 \cdot 4}\right) \left(-4.48155 + 1\right)^2$$

$$I_{D_2} = 800 \mu A$$

check

$$V_{05} = (800\mu A \cdot 1K) - 10 = -9.2$$
  
 $V_{65} = -4.48155 V$   
 $(-9.2) < (-4.48 + 1)$  saturation

15 points (a) 
$$W_2 = 10\mu$$
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 $W_3 = 10\mu$ 
 $W_4 = 10\mu$ 

$$V_{TN} = IV \qquad \mu_{N} C_{OX} = I00 \frac{\mu_{N}}{v^{2}}$$

$$saturation \qquad \text{for} \quad M_{1} + M_{2}$$

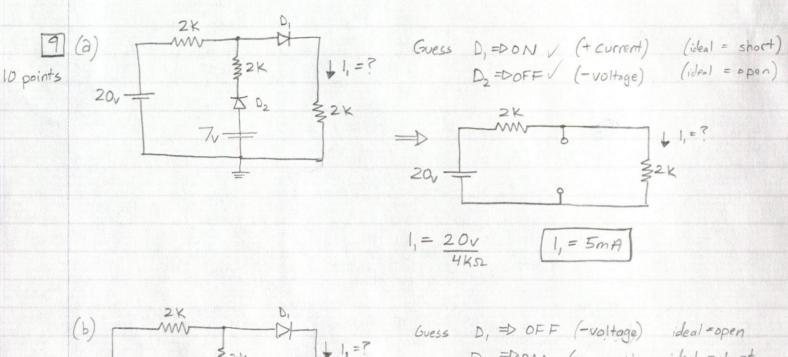
$$I_{D_{1}} = I_{D_{2}}$$

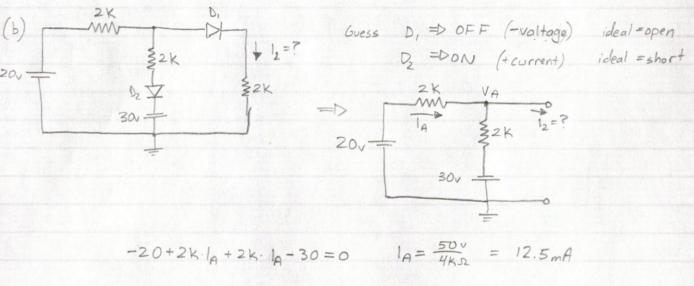
$$\mu_{N} C_{OX} = \frac{W_{2}}{2 \cdot L_{2}} \left( V_{GS_{2}} - V_{T} \right)^{2} = \mu_{N} C_{OX} = \frac{W_{1}}{2 \cdot L_{1}} \left( V_{GS_{1}} - V_{T} \right)^{2}$$

$$\frac{10}{2 \cdot 1} \left( (5-2) - 1 \right)^{2} = \frac{W_{1}}{L_{1}} \cdot \left( \frac{1}{2} \right) \cdot \left( 3 - 1 \right)^{2}$$

$$\frac{W_{1}}{L_{1}} = I0 \qquad \text{Pick} \qquad \frac{W_{1} = I04}{L_{1} = IM}$$

(b) for 
$$V_{out} = 0.5 V$$
 $M_2 \Rightarrow Saturation$ 
 $M_1 \Rightarrow Triode$ 
 $0.5 < 3-1$ 
 $I_{0_1} = I_{0_2}$ 
 $I_{0_1} = I_{0_1}$ 





$$V_A = 20 - 2k \cdot I_A = -5v$$
  $V_A = -5v$ 

since the current through  $D_2$  is positive (forward bias) and the voltage accross  $D_1$  is negative (reverse bias) the current  $I_2 = OA$ 

10pts 10. I = 1/2 MCox . \* (Viest - Ven) = 1/2 MpCox . \* (Ves - Vep) 2
assuming saturation

I = 1/2.100E-6. + (Vout - 1) = 15.33E-6. + (-2++1)2

Vout = 1.57 V

Saturation confirmed

## Extra Credit

+10pls 11. I, =? Assume Forward Bias

$$I_1 = 5.6/2k = 2.8mA$$
 $I_2 = 5.6/2k = 2.8mA$ 
 $I_3 = 5.6V$ 
 $I_4 = 5.6V$ 
 $I_5 = 5.6V$ 
 $I_5 = 5.6V$ 
 $I_6 = 5.6V$ 
 $I_7 = 5.6V$ 
 $I_8 = 5.6V$ 
 $I_8 = 5.6V$ 
 $I_8 = 5.6V$ 
 $I_9 = 5.$ 

Since Is is positive, it confirms the diade is in forward bios

$$I_{a} = 15/4\kappa = 3.75mA$$
 $V_{a} = 15/4\kappa = 3.75mA$ 
 $V_{b} = 15/2 = 7.5V$ 
 $V_{b} = 5-7.5 = -2.5 < 0$  ... confirms reverse bias

$$V_1 = 15/2 = 7.5V$$
  
 $V_0 = 5 - 7.5 = -2.5 < 0$  : confirms reverse big

HOpis 12. 
$$I = [J_{5x} \cdot A (T^m e^{-V_{60}/Vt})] \cdot e^{V_{0}/Vt}$$
  
 $J_{5x} = .5 \quad V_{90} = 1.17 \quad m = 2.3^2 \quad A = 100 \quad V_{0} = .575V$   
 $V_{t} = K \cdot T/g = 1.38 \text{ E-}33 \cdot T/1.602 \text{ E-}19$ 

a) 
$$T = -20^{\circ} C = 253.15 \, K$$
  $V = .0218 \, V$ 

## Extra Credit – Problem 13 (20 Points)

```
module Counter_0_9(INPUT, count, seg);
 2
           input INPUT;
            output reg [6:0]seg;//7 segment display register [A B C D E F G]
 4
            output reg [3:0]count;
 5
 6
7
           always @(posedge(INPUT))//always on rising edge of the clock (INPUT)
              case (count)//increase the counter
  4'b0000: count<=4'b0001;//0 -> 1
 8
                4 b0000: count<=4'b0001;//0 -> 14
4'b0001: count<=4'b0010;//1 -> 2
 9
10
11
                 4'b0010: count<=4'b0011;//2 -> 3
12
                 4'b0011: count<=4'b0100;//3 -> 4
13
                 4'b0100: count < = 4'b0101; //4 \rightarrow 5
                 4'b0101: count<=4'b0110;//5 -> 6
14
15
                 4'b0110: count<=4'b0111;//6 -> 7
16
                 4'b0111: count<=4'b1000;//7 -> 8
17
                 4'b1000: count<=4'b1001;//8 -> 9
18
                 4'b1001: count<=4'b0000;//9 -> 0
19
                 default: count<=4'b0000; //default value of 0
20
              endcase
              case(count)//create the 7_seg output
  4'b0000: seg <= 7'b1111110;//display 0
  4'b0001: seg <= 7'b0110000;//display 1</pre>
21
22
23
24
                 4'b0010: seg <= 7'b1101101;//display 2
                4'b0011: seg <= 7'b1111001;//display 3
4'b0100: seg <= 7'b0110011;//display 4
4'b0101: seg <= 7'b0110111;//display 5
25
26
27
28
                 4'b0110: seg <= 7'b1011111;//display 6
29
                 4'b0111: seg <= 7'b1110000;//display 7
                4'b1000: seg <= 7'b11111111;//display 8
4'b1001: seg <= 7'b1110011;//display 9
default: seg <= 7'b1111110;//defaults to a display of 0
30
31
32
33
              endcase
34
           end
35
         endmodule
```

Figure 1: Problem 13: 7 segement counter from 0 -> 9: code

```
\frac{1}{2}
        `timescale 1ns / 1ps
 3
        module Counter_0_9_tb();
 4
5
6
         req clk t;//INPUT
         wire [6:0]out_t;//7 segment display register
         wire [3:0]count_t;//counter register
 7
 8
          Counter 0 9 Counter 0 9 t(clk t, count t, out t);
 9
10
          initial
11
          begin
             repeat(21) begin
12
13
               clk_t<=0;
               #1 $display("Counter = %b", count_t);
#2 $display("7 seg = %b", out_t);
14
15
16
17
                clk_t<=1;
               #1 $display("Counter = %b", count_t);
#2 $display("7 seg = %b", out_t);
18
19
20
             end
21
          end
        endmodule
```

Figure 2: 7 segment counter from 0 to 9 testbench code

