

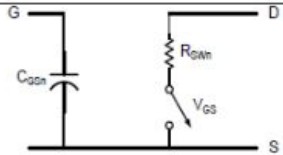
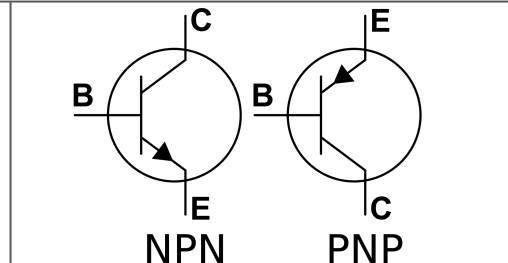
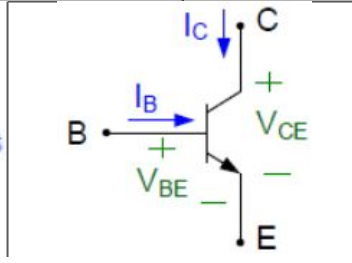
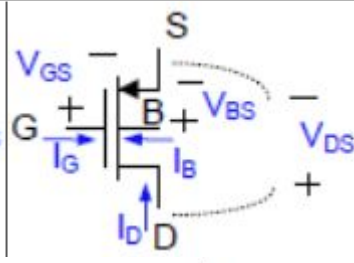
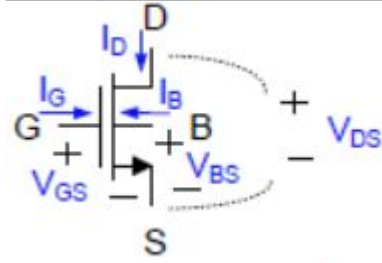
Sqr Law $V_{GS} \leq V_T$ Cutoff

$$I_D = \begin{cases} 0 & V_{GS} \leq V_T \\ \mu C_{ox} \frac{W}{L} \left(V_{GS} - V_T - \frac{V_{DS}}{2} \right) V_{DS} & V_{GS} \geq V_T, V_{DS} < V_{GS} - V_T \text{ Triode} \\ \mu C_{ox} \frac{W}{2L} (V_{GS} - V_T)^2 & V_{GS} \geq V_T, V_{DS} \geq V_{GS} - V_T \text{ Saturation} \end{cases}$$

Extended $V_{GS} \leq V_T$

$$I_D = \begin{cases} 0 & V_{GS} \leq V_T \\ \mu C_{ox} \frac{W}{L} \left(V_{GS} - V_T - \frac{V_{DS}}{2} \right) V_{DS} & V_{GS} \geq V_T, V_{DS} < V_{GS} - V_T \\ \mu C_{ox} \frac{W}{2L} (V_{GS} - V_T)^2 \cdot (1 + \lambda V_{DS}) & V_{GS} \geq V_T, V_{DS} \geq V_{GS} - V_T \end{cases}$$

$$V_T = V_{T0} + \gamma (\sqrt{\phi - V_{BS}} - \sqrt{\phi})$$



$$I_C = \beta I_B$$

$$V_{BE} > 0.4V$$

Forward Active

$$V_{BE} = 0.6V$$

$$V_{BC} < 0$$

Determine R_{SW} and C_{GS} for an n-channel MOSFET from square-law model in the 0.5u CMOS process if $L=1\mu$, $W=1\mu$

(Assume $\mu C_{ox}=100\mu A/V^2$, $C_{ox}=2.5fF/\mu^2$, $V_{T0}=1V$, $V_{DD}=3.5V$, $V_{SS}=0$)

When on operating in deep triode

$$I_D = \mu C_{ox} \frac{W}{L} \left(V_{GS} - V_T - \frac{V_{DS}}{2} \right) V_{DS} \approx \mu C_{ox} \frac{W}{L} (V_{GS} - V_T) V_{DS}$$

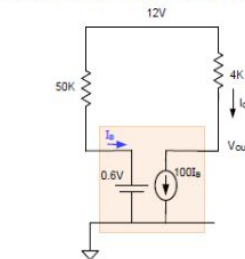
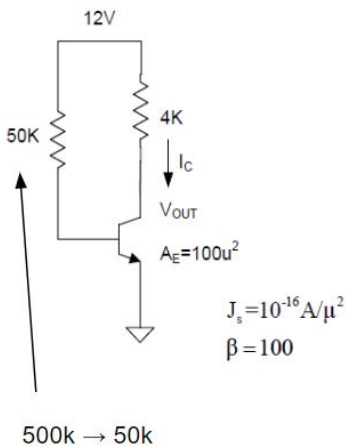
$$R_{SQ} = \frac{V_{DS}}{I_D} \bigg|_{V_{GS}=V_{DS}} = \frac{1}{\mu C_{ox} \frac{W}{L} (V_{GS} - V_T)} \bigg|_{V_{GS}=3.5V} = \frac{1}{(3.5-1) \left(\frac{1}{1} \right) (100)} = 4K\Omega$$

$$C_{GS} = C_{ox}WL = (2.5fF/\mu^2)(1\mu^2) = 2.5fF$$

Example: Determine I_C and V_{OUT} .

Solution:

1. Guess Forward Active Region
2. Solve Circuit with Guess



$$I_B = \frac{(12-0.6)}{50K}$$

$$I_C = \beta I_B = 100 \cdot \frac{(12-0.6)}{50K} = 22.8mA$$

$$V_{OUT} = 12 - I_C \cdot 4K = -79.2V$$

3. Verify FA Region $V_{BE} > 0.4V$ and $V_{BC} < 0$

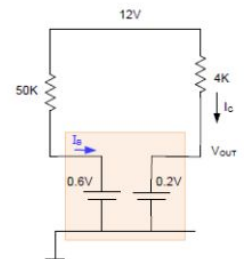
$$V_{BE} = 0.6V > 0.4V$$

$$V_{BC} = 0.6V - (-79.2V) = +79.8V > 0$$

Verify Fails so solution is not valid

Solution:

4. Guess Saturation
5. Solve Circuit with Guess



$$I_B = \frac{(12-0.6)}{50K} = 228\mu A$$

$$I_C = \frac{(12-0.2)}{4K} = 2.95mA$$

$$V_{OUT} = 0.2V$$

6. Verify SAT Region $I_C < \beta I_B$

$$\beta I_B = 100 \cdot 228\mu A = 22.8mA$$

$$I_C = 2.95mA$$

$$I_C = 2.95mA < \beta I_B = 22.8mA$$

Verify Passes so solution is valid

$$I_C = 2.95mA$$

$$V_{OUT} = 0.2V$$

$$I_G = 0$$

$$I_B = 0$$

$$I_D = \begin{cases} 0 & V_{GS} \leq V_T \\ \mu C_{ox} \frac{W}{L} \left(V_{GS} - V_T - \frac{V_{DS}}{2} \right) V_{DS} & V_{GS} \geq V_T, V_{DS} < V_{GS} - V_T \\ \mu C_{ox} \frac{W}{2L} (V_{GS} - V_T)^2 \cdot (1 + \lambda V_{DS}) & V_{GS} \geq V_T, V_{DS} \geq V_{GS} - V_T \end{cases}$$

$$\mu C_{ox} \frac{W}{2L} (V_{GS} - V_T)^2 \cdot (1 + \lambda V_{DS})$$

$$V_T = V_{T0} + \gamma (\sqrt{\phi - V_{BS}} - \sqrt{\phi})$$

$$V_{GS} \leq V_T$$

$$V_{GS} \geq V_T, V_{DS} < V_{GS} - V_T$$

$$V_{GS} \geq V_T, V_{DS} \geq V_{GS} - V_T$$

$$y_{11} = \frac{\partial I_D}{\partial V_{GS}} \bigg|_{V_D=V_Q} = 0$$

$$y_{12} = \frac{\partial I_D}{\partial V_{DS}} \bigg|_{V_G=V_Q} = 0$$

$$y_{13} = \frac{\partial I_D}{\partial V_{BS}} \bigg|_{V_G=V_Q} = 0$$

$$y_{21} = \frac{\partial I_D}{\partial V_{GS}} \bigg|_{V_D=V_Q} = g_m$$

$$y_{22} = \frac{\partial I_D}{\partial V_{DS}} \bigg|_{V_G=V_Q} = g_o$$

$$y_{23} = \frac{\partial I_D}{\partial V_{BS}} \bigg|_{V_G=V_Q} = g_{mb}$$

$$y_{31} = \frac{\partial I_B}{\partial V_{GS}} \bigg|_{V_D=V_Q} = 0$$

$$y_{32} = \frac{\partial I_B}{\partial V_{DS}} \bigg|_{V_G=V_Q} = 0$$

$$y_{33} = \frac{\partial I_B}{\partial V_{BS}} \bigg|_{V_G=V_Q} = 0$$

Small-Signal 4-terminal Model Extension

$$I_D = \mu C_{ox} \frac{W}{2L} (V_{GS} - V_T)^2 \cdot (1 + \lambda V_{DS})$$

Definition:

$$V_{EB} = V_{GS} - V_T$$

$$V_{EBQ} = V_{GSQ} - V_{TQ}$$

$$V_T = V_{T0} + \gamma (\sqrt{\phi - V_{BS}} - \sqrt{\phi})$$

$$g_m = \frac{\partial I_D}{\partial V_{GS}} \bigg|_{V_D=V_Q} = \mu C_{ox} \frac{W}{2L} 2(V_{GS} - V_T)^1 \cdot (1 + \lambda V_{DS}) \bigg|_{V_D=V_Q} \cong \mu C_{ox} \frac{W}{L} V_{EBQ}$$

Same as 3-term

$$g_o = \frac{\partial I_D}{\partial V_{DS}} \bigg|_{V_G=V_Q} = \mu C_{ox} \frac{W}{2L} 2(V_{GS} - V_T)^2 \cdot \lambda \bigg|_{V_G=V_Q} \cong \lambda I_{DQ}$$

Same as 3-term

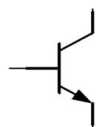
$$g_{mb} = \frac{\partial I_D}{\partial V_{BS}} \bigg|_{V_D=V_Q} = \mu C_{ox} \frac{W}{2L} 2(V_{GS} - V_T)^1 \cdot \left(-\frac{\partial V_T}{\partial V_{BS}} \right) \cdot (1 + \lambda V_{DS}) \bigg|_{V_D=V_Q}$$

$$g_{mb} = \frac{\partial I_D}{\partial V_{BS}} \bigg|_{V_D=V_Q} \cong \mu C_{ox} \frac{W}{L} V_{EBQ} \cdot \frac{\partial V_T}{\partial V_{BS}} \bigg|_{V_D=V_Q} = \left(\mu C_{ox} \frac{W}{L} V_{EBQ} \right) (-1) \gamma \frac{1}{2} (\phi - V_{BS})^{-\frac{1}{2}} \bigg|_{V_D=V_Q} (-1)$$

$$g_{mb} \cong g_m \frac{\gamma}{2\sqrt{\phi - V_{BSQ}}}$$

Large and Small Signal Model Summary

Large Signal Model



$$I_C = \beta I_B \left(1 + \frac{V_{CE}}{V_{AF}} \right) \quad V_{BE} > 0.4V$$

$$I_B = \frac{J_S A_E}{\beta} e^{\frac{V_{BE}}{V_T}} \quad V_{BC} < 0$$

Forward Active

$$V_{BE} = 0.7V$$

$$V_{CE} = 0.2V$$

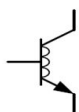
$$I_C < \beta I_B$$

$$V_{BE} < 0$$

$$V_{BC} < 0$$

$$I_C = I_B = 0$$

Small Signal Model



Forward Active

$$i_b = g_\pi v_{be}$$

$$i_c = g_m v_{be} + g_o v_{ce}$$

where

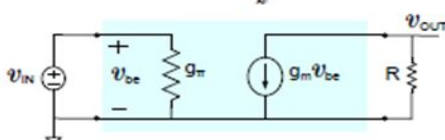
$$g_m = \frac{I_{CQ}}{V_T}$$

$$g_\pi = \frac{I_{CQ}}{\beta V_T}$$

$$g_o \cong \frac{I_{CQ}}{V_{AF}}$$

Neglect V_{AF} effects (i.e. $V_{AF} = \infty$) to be consistent with earlier analysis

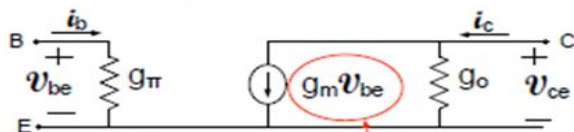
$$g_o = \frac{I_{CQ}}{V_{AF}} = 0$$



$$\left. \begin{aligned} v_{OUT} &= -g_m R v_{be} \\ v_{IN} &= v_{be} \end{aligned} \right\} \quad A_V = \frac{v_{OUT}}{v_{IN}} = -g_m R$$

$$g_m = \frac{I_{CQ}}{V_T}$$

$$A_V = -\frac{I_{CQ} R}{V_T}$$



$$g_m = \frac{I_{CQ}}{V_T}$$

$$g_\pi = \frac{I_{CQ}}{\beta V_T}$$

$$g_o \cong \frac{I_{CQ}}{V_{AF}}$$

Observe :

$$g_\pi \alpha_{be} = i_b$$

$$g_m \alpha_{be} = i_b \frac{g_m}{g_\pi}$$

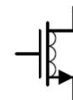
$$\frac{g_m}{g_\pi} = \frac{\left[\frac{I_{CQ}}{V_T} \right]}{\left[\frac{I_{CQ}}{\beta V_T} \right]} = \beta$$

$$g_m \alpha_{be} = \beta i_b$$

Can replace the voltage dependent current source with a current dependent current source

Small-Signal Model of MOSFET

Review from last lecture



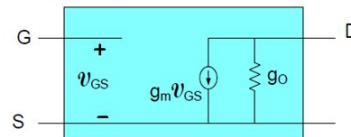
Alternate g_m

$$g_m = \sqrt{2\mu C_{ox} \frac{W}{L}} \cdot \sqrt{I_{DQ}}$$

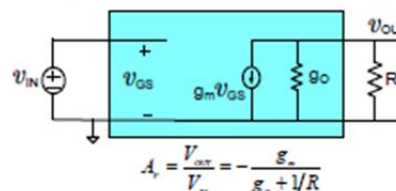
$$g_m = \frac{2I_{DQ}}{V_{GSQ} - V_T}$$

$$g_m = \mu C_{ox} \frac{W}{L} (V_{GSQ} - V_T)$$

$$g_o \cong \lambda I_{DQ}$$



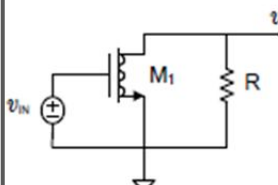
Small-signal analysis example



$$A_v = \frac{v_{OUT}}{v_{IN}} = -\frac{g_m}{g_o + 1/R}$$

This gain is expressed in terms of small-signal model parameters

For $\lambda=0$, $g_o = \lambda I_{DQ} = 0$

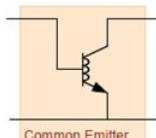
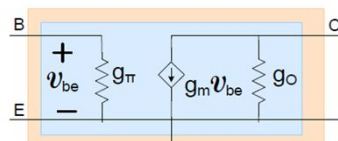


$$A_v = \frac{v_{OUT}}{v_{IN}} = -g_m R$$

$$\text{but } g_m = \frac{2I_{DQ}}{V_{GSQ} - V_T} \quad V_{GSQ} = -V_{SS}$$

thus

$$A_v = \frac{2I_{DQ} R}{[V_{SS} + V_T]}$$



Common Emitter

By Thevenin : Norton Transformations

$$R_{in} = \frac{1}{g_\pi}$$

$$A_{V0} = -\frac{g_m}{g_o}$$

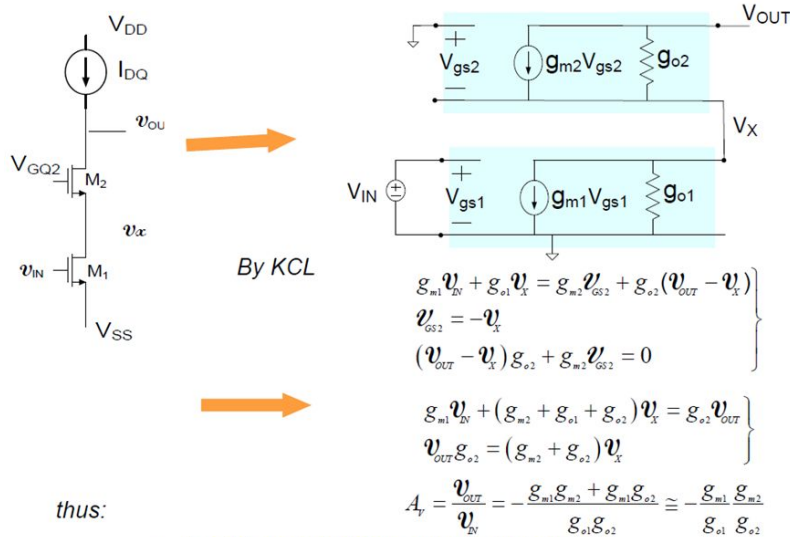
$$R_o = \frac{1}{g_o}$$

$$A_{VR} = 0$$

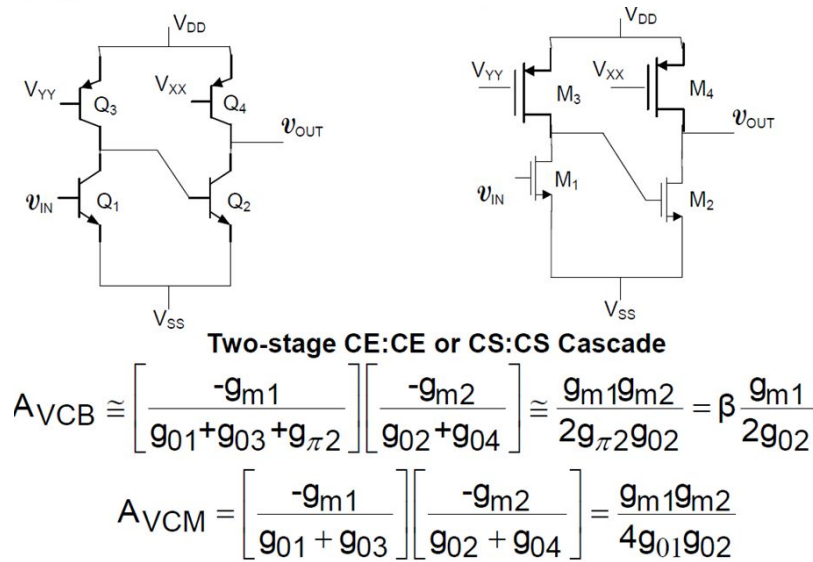
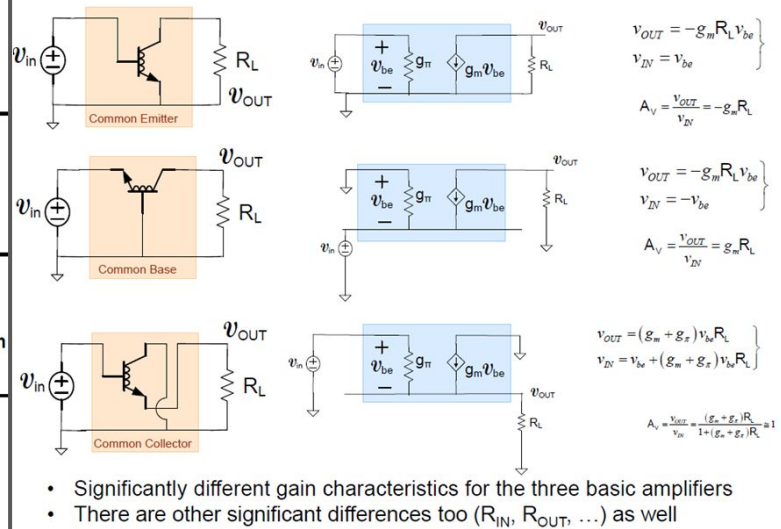
Basic Amplifier Characteristics Summary

CE/CS	<ul style="list-style-type: none"> Large inverting gain Moderate input impedance Moderate (or high) output impedance Widely used as the basic high gain inverting amplifier
CC/CD	<ul style="list-style-type: none"> Gain very close to +1 (little less) High input impedance for BJT (high for MOS) Low output impedance Widely used as a buffer
CB/CG	<ul style="list-style-type: none"> Large noninverting gain Low input impedance Moderate (or high) output impedance Used more as current amplifier or, in conjunction with CD/CS to form two-stage cascode
CEwRE/ CSwRS	<ul style="list-style-type: none"> Reasonably accurate but somewhat small gain (resistor ratio) High input impedance Moderate output impedance Used when more accurate gain is required

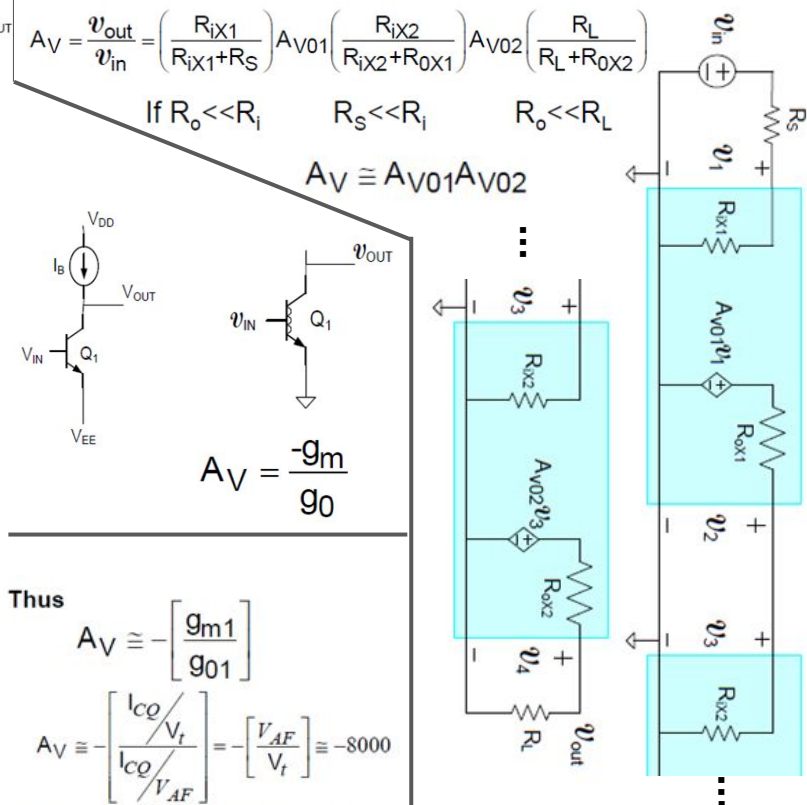
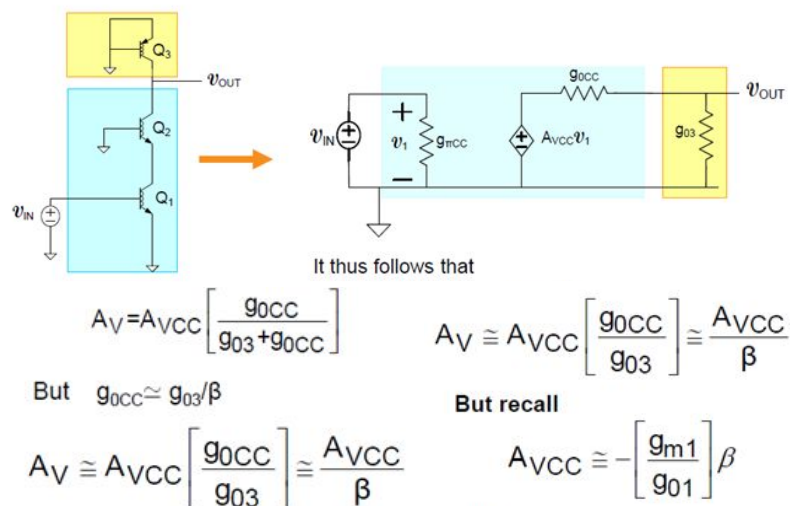
Example: Determine the small signal voltage gain $A_V = v_{OUT}/v_{IN}$. Assume M_1 and M_2 are operating in the saturation region and that $\lambda \neq 0$



The three basic amplifier types for both MOS and bipolar processes

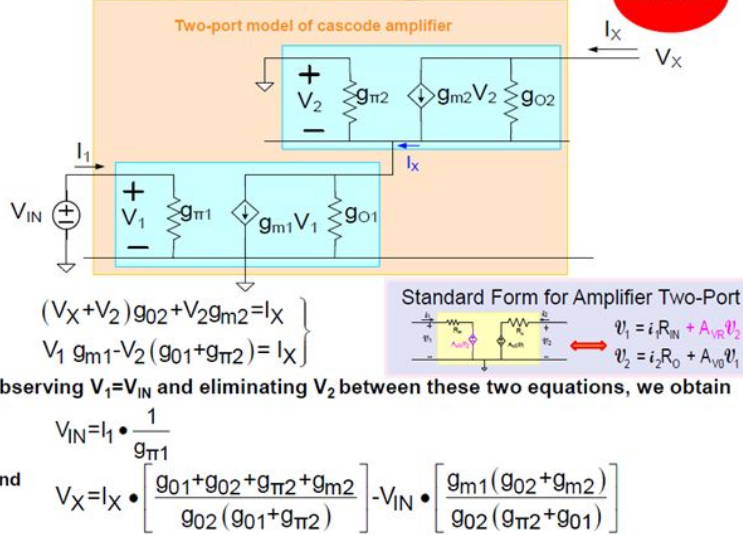


High-gain amplifier comparisons



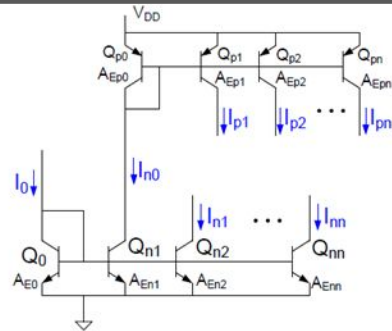
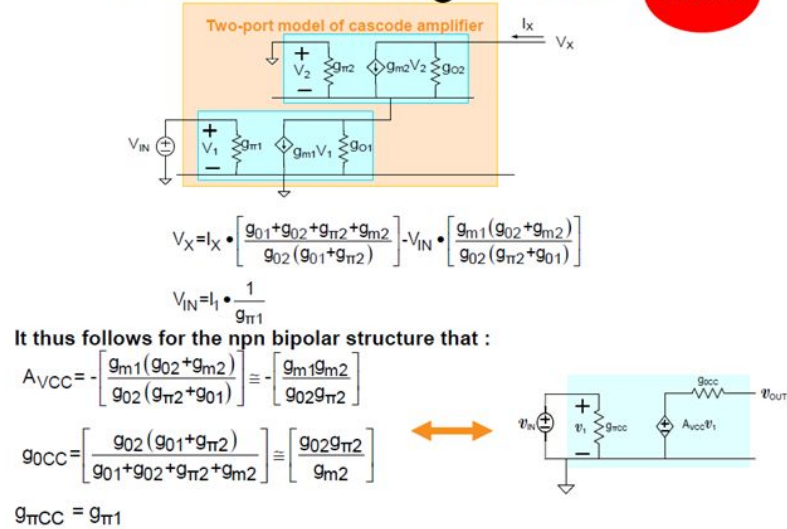
Cascode Configuration

Discuss



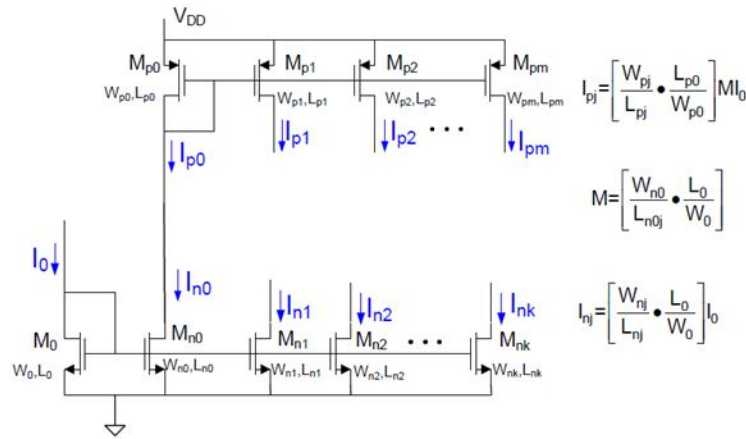
Cascode Configuration

Discuss



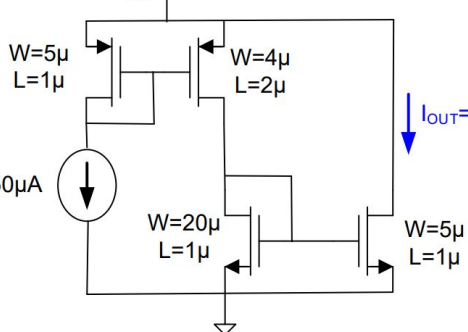
Multiple-Output Bipolar Current Source and Sink

$$I_{nk} = \left[\frac{A_{Enk}}{A_{E0}} \right] I_0 \quad I_{pk} = \left[\frac{A_{En1}}{A_{E0}} \right] \left[\frac{A_{Epk}}{A_{Ep0}} \right] I_0$$



m and k may be different
Often M=1

$V_{DD} = 2V$



This is two current mirrors,

$$I_{out} = \frac{(W2/L2)}{(W1/L1)} * \frac{(W4/L4)}{(W3/L3)} * I_{in} = \frac{(4/2)}{(5/1)} * \frac{(5/1)}{(20/1)} 250\mu A = \frac{2}{5} * \frac{5}{20} * 250\mu$$

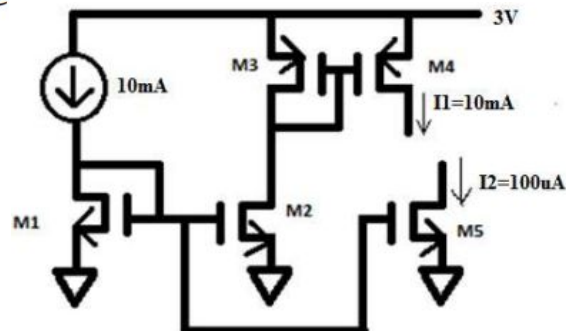
$$I_{out} = 25\mu A$$

Problem 2 Assume you have available a supply voltage of $V_{DD}=3V$ and a 10mA sourcing current (one end connected to V_{DD}).

- Design a current mirror that provides two outputs, a sinking current of 100uA and a sourcing current of 10mA using MOS transistors.
- What is the maximum voltage for the 10mA current source for your design if it is to work as a current mirror.

$$|V_{GS1}| - |V_{T1}| = \frac{I_1}{\frac{1}{2} \mu_p C_{ox} \frac{W_4}{L_4}} = \frac{33 * 10^{-6} * 2}{2 * 1} \frac{1}{(V_{GS1} - |V_{T1}|)^2} = 10mA$$

$$|V_{GS1}| - |V_{T1}| = \sqrt{\frac{2 * 1 * 10mA}{33 * 10^{-6} * 2}} = 17.41V$$



- With this we have to choose the sizes of M_1 and M_5 to determine the 100uA sinking current and M_1, M_2, M_3 , and M_4 to create the 10mA sourcing current

As we can choose whatever sizes we want, I am going to use $W_1 = L_1 = 1\mu$, so $\frac{W_1}{L_1} = 1$.

From there I can set M_5 , I want $\frac{W_5}{L_5} = \frac{100\mu A}{10mA} = 0.01$.

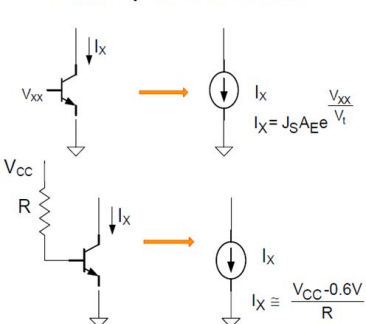
So I will use $W_5 = 1\mu$ and $L_5 = 100\mu$.

We then want to convert 10mA to 10mA, or times 1. This can be easily done in a single mirror, so I will use,

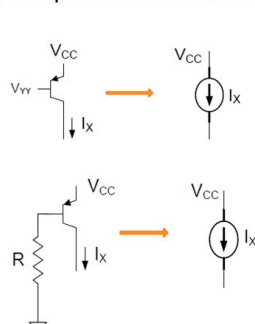
$$M_2 = L_2 = 1\mu, \quad M_3 = L_3 = 1\mu, \quad M_4 = 1\mu, L_4 = 1\mu$$

Basic Current Sources and Sinks

Basic Bipolar Current Sinks



Basic Bipolar Current Sources



- Very practical methods for biasing the BJTs (or MOSFETs) can be used
- Current Mirrors often used for generating sourcing and sinking currents
- Can think of biasing transistors with V_{xx} and V_{yy} in these current sources