

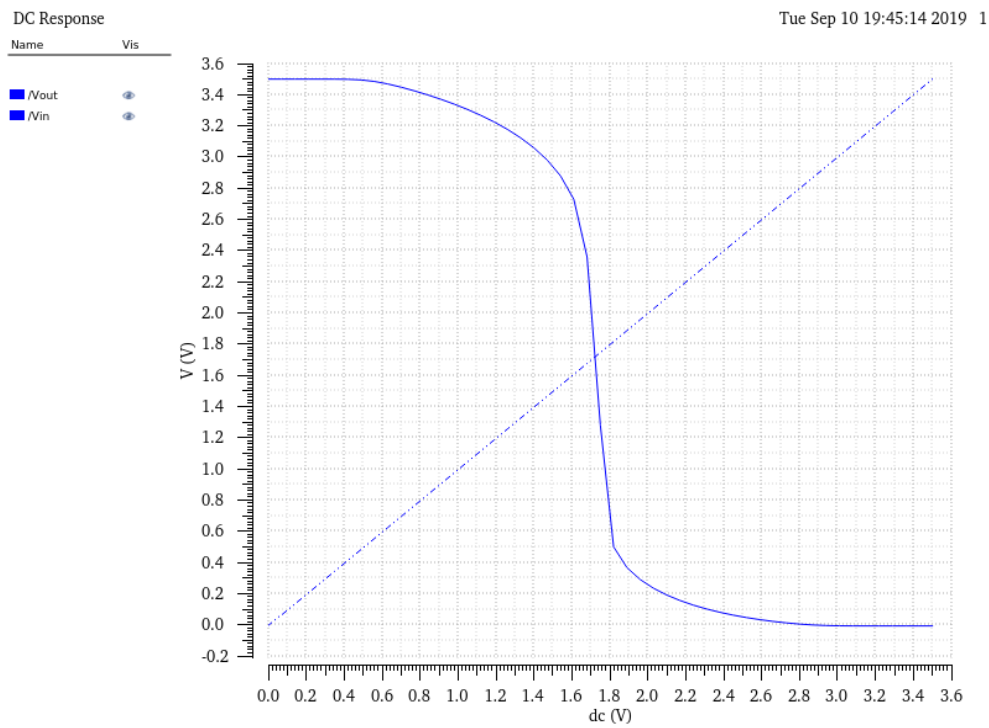
EE 330  
Section 5, 8:00 am

Laboratory 2: Basic Boolean Circuits

Sean Gordon  
Sgordon4

## Part 2.5:

Graph of the DC response for the circuit:

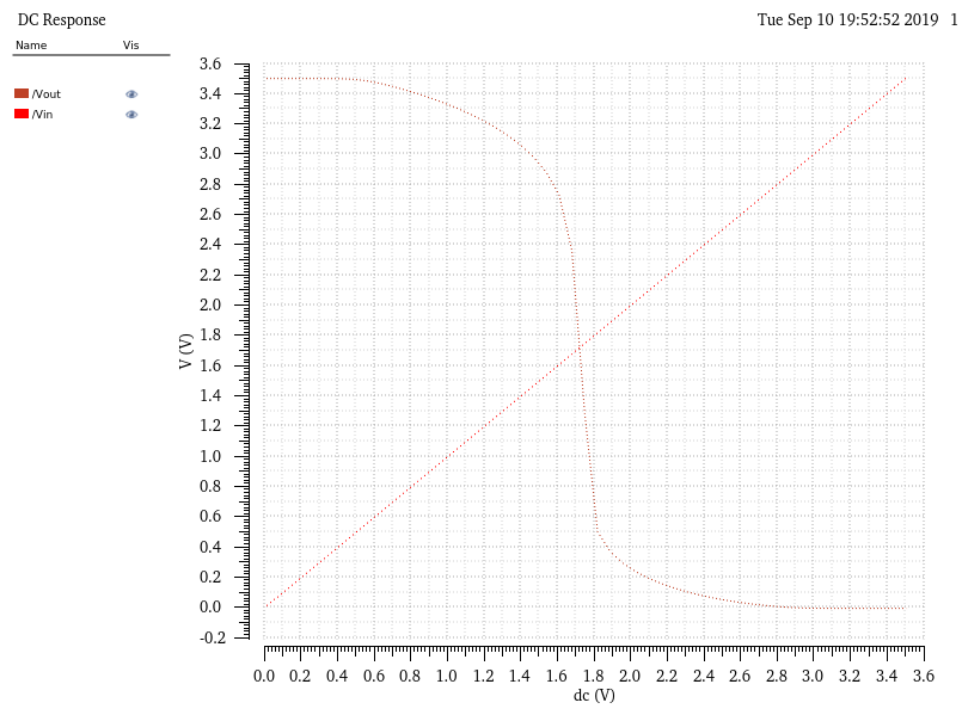


$$V_{in_{max}} = 1.6\text{V}$$

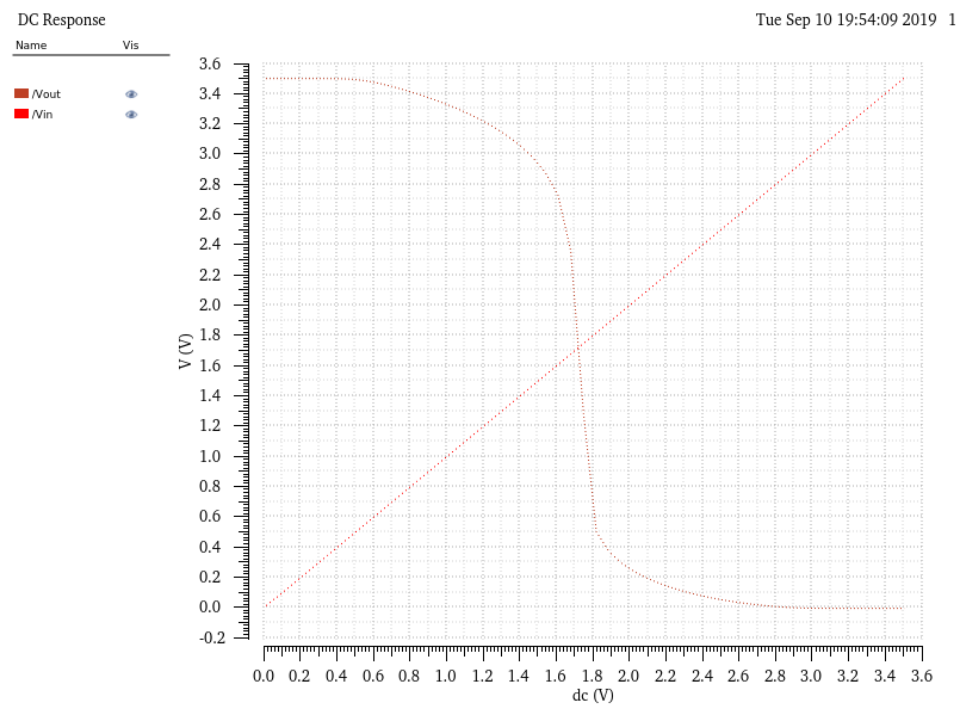
$$V_{in_{min}} = 1.8\text{V}$$

# Part 2.6:

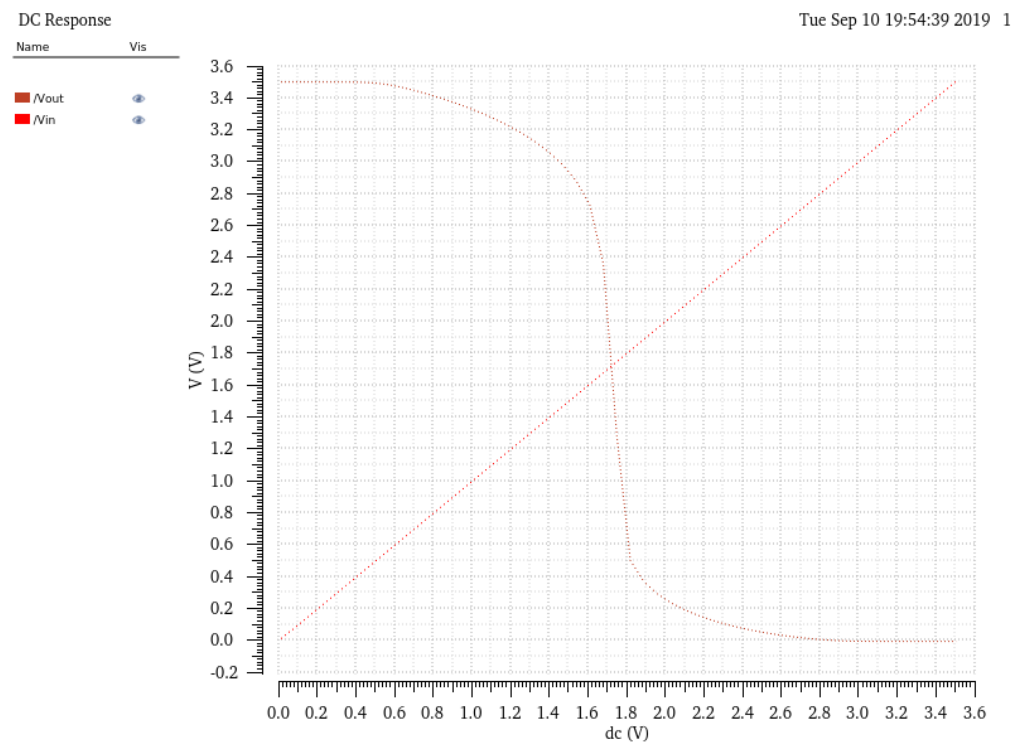
Transient analysis at 10pf:



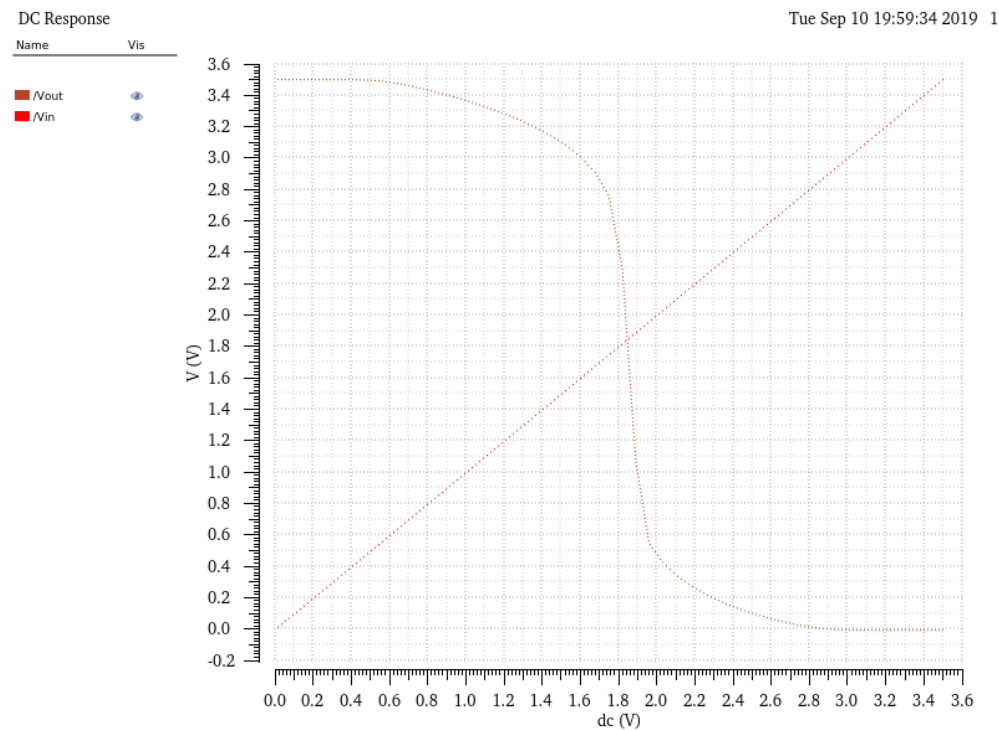
Transient analysis at 100pf:



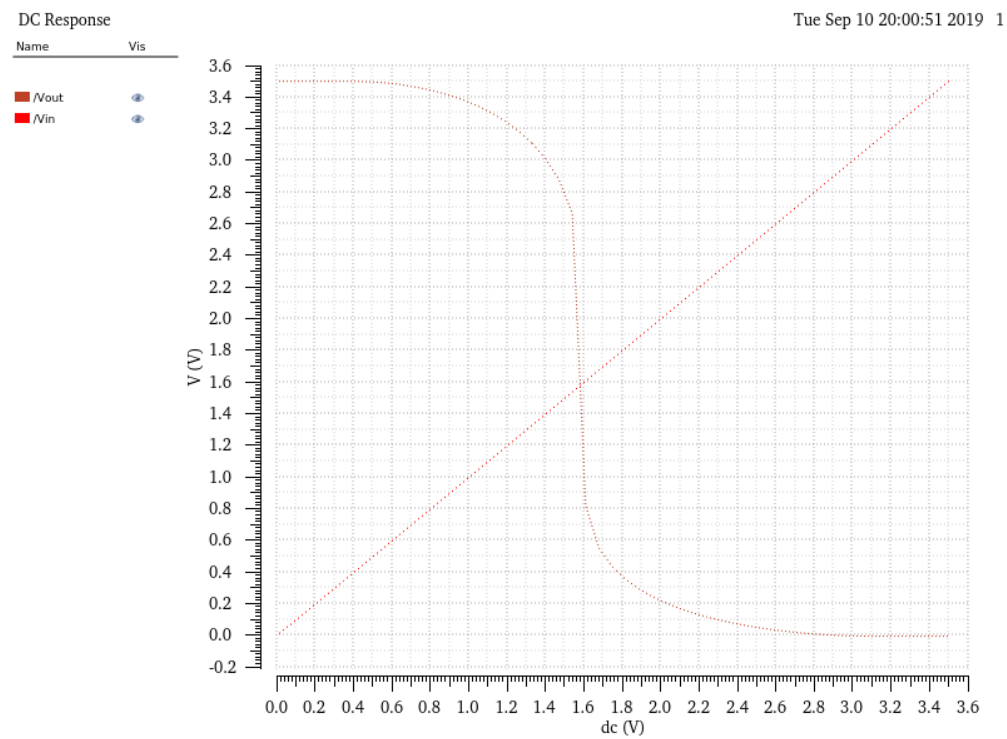
Transient analysis at 1uf:



Transient analysis at 10x the width:

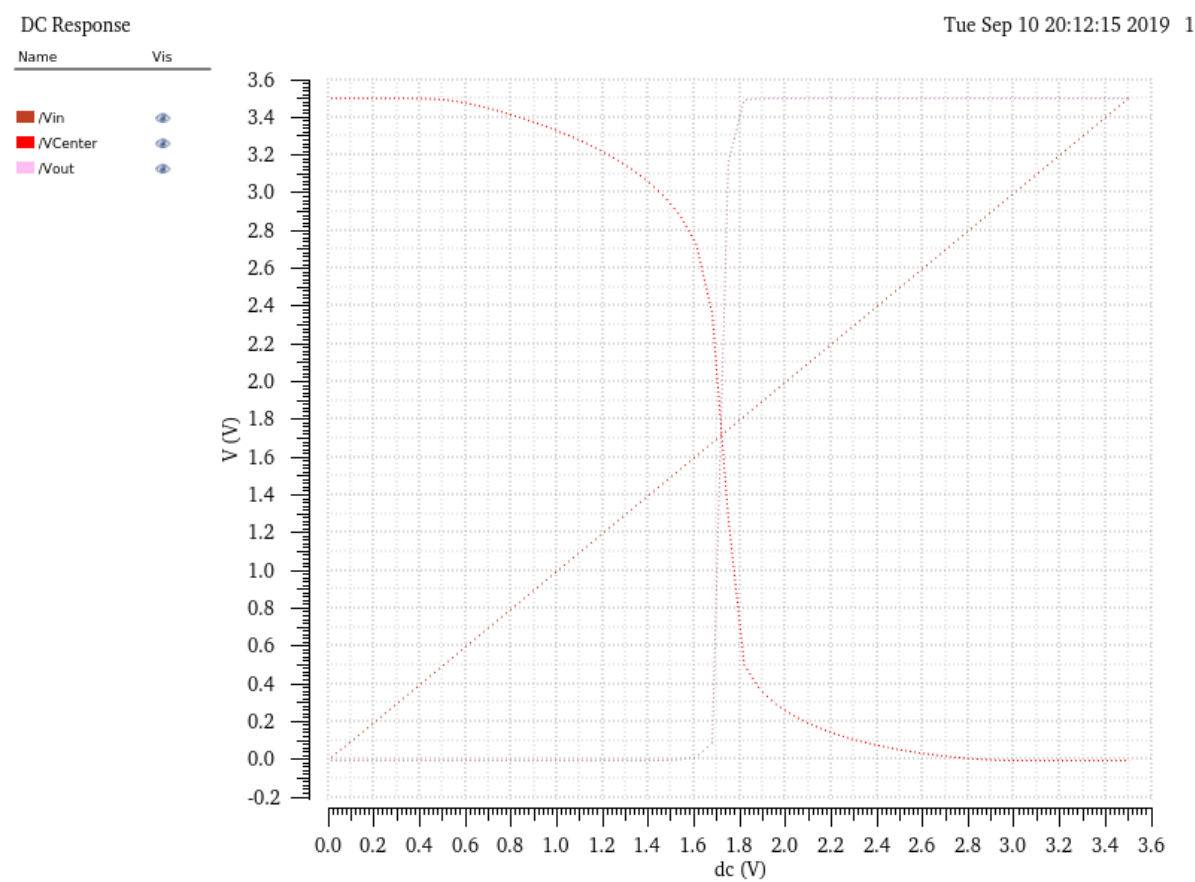


Transient analysis at 10x the width:



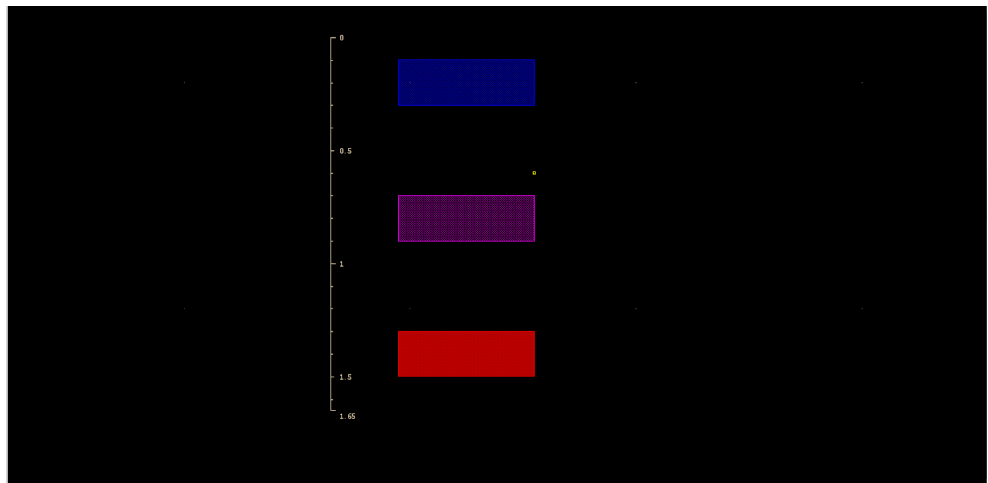
Part 3:

Transient analysis for the cascaded inverters:



Part 4:

Sample rectangles:



Transistor model:

