

Class 30 00" THO will become I on first clock cycle

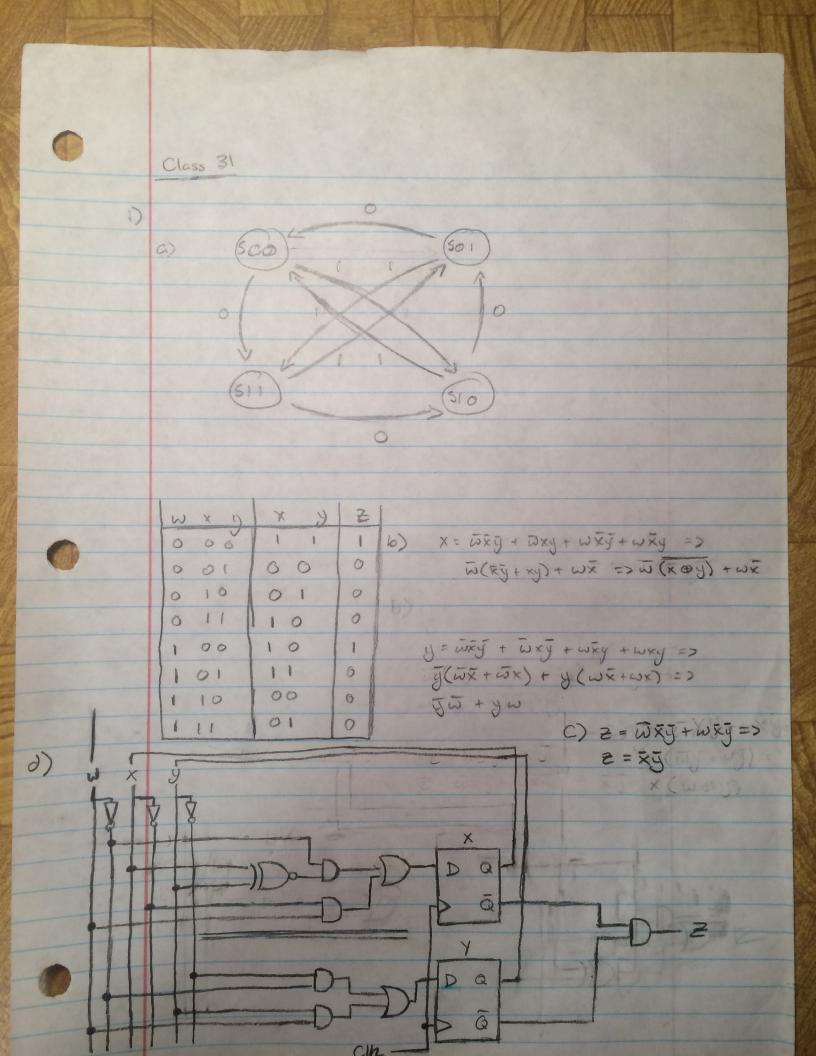
1) 0;1,2,7 will become I on second cycle because of THO, while THO will become of the will become of the will become of the will rot change;

and THO becomes I

2)  $t_{50} = 4$   $t_{n} = 4$   $t_{c0} = 4$   $t_{N} = 3$   $t_{N} = 3$   $t_{N} = 5$ And  $t_{N} = 4$   $t_{N} = 4$   $t_{N} = 4$   $t_{N} = 4$   $t_{N} = 4$ And  $t_{N} = 4$   $t_{N} = 4$  t

b) If the of logest delay 'tinger' is shorter than the min delay, there is a hold time violation.

17-25=-8 <0 : , there is a hold lime violation



2) ay 510

6)511 0)511

3)	present 1	l o w		Output		
3)	- The second of the second	province or control of the	-	2.2.20		
	50	Si	50	000		
a)	81	52	54	001		
	52	53	53	000		
	53	150	54	011		
	su	150	50	100		

1	Present	OW		Output		
	(X z X, Xo	1 y y y	929,92	22	飞,	20
6)	000	001	001	0	0	0
,	001	010	100	0	0	1
	010	011	011	0	1	0
	011	000	100	0	1	1
	100	000	000	1	0	0