EE 330
Section 5, 8:00 am
Lab 4: From Boolean Equation to Silicon

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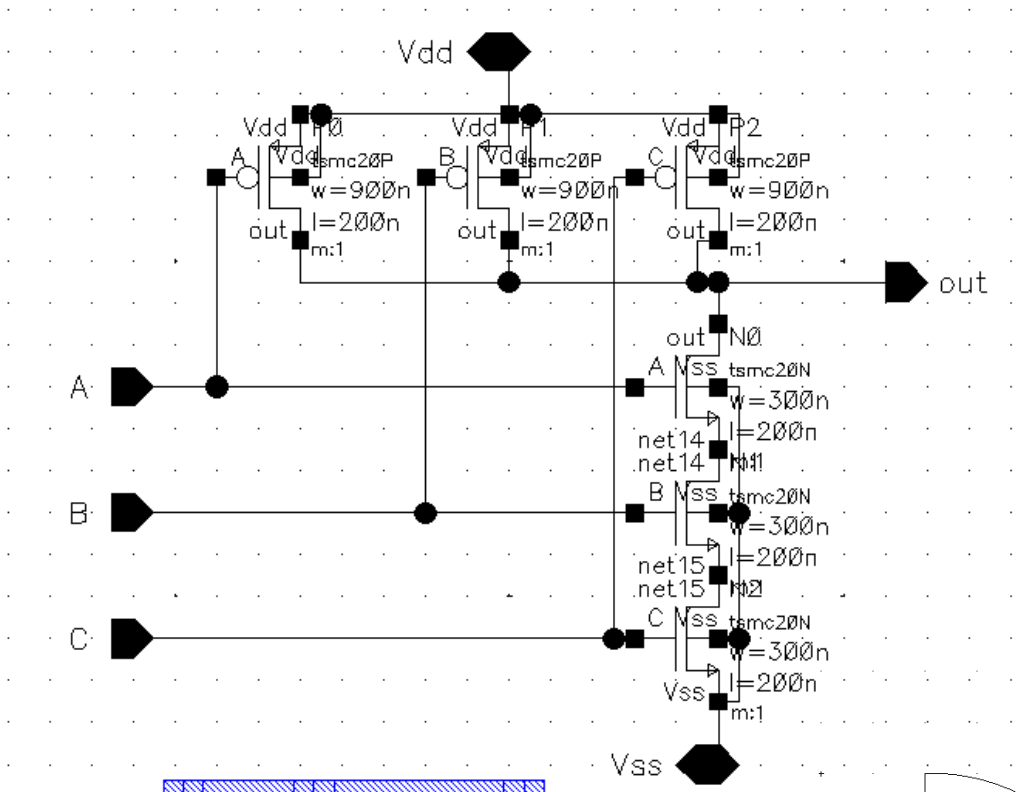
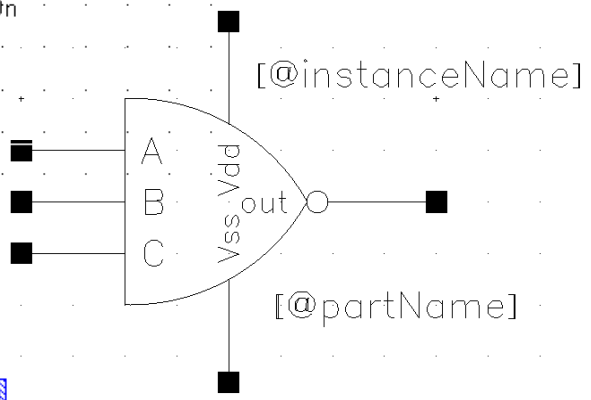
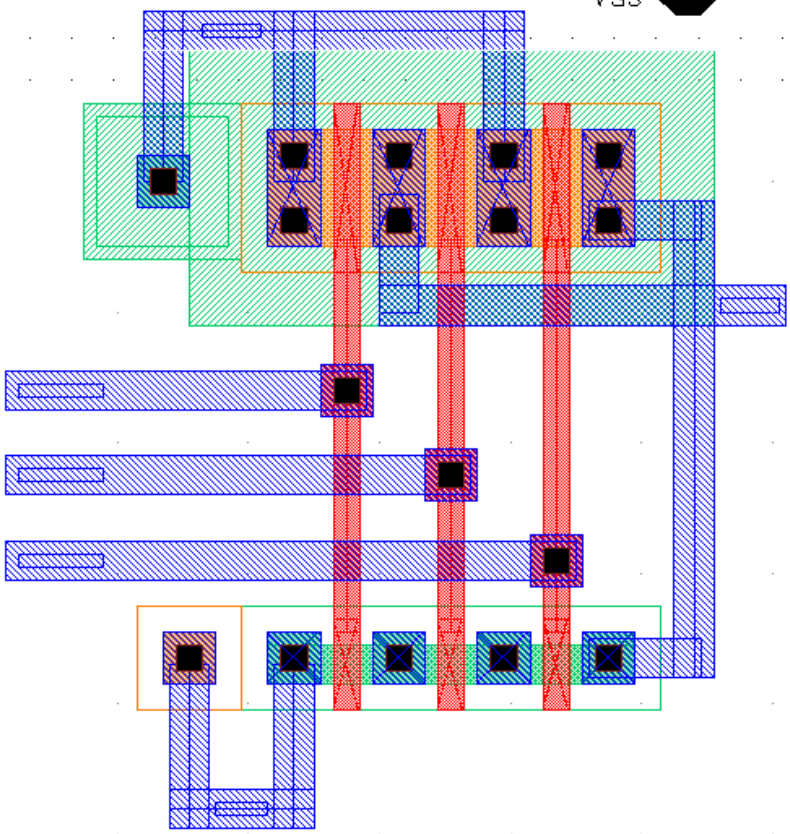
Overview:
This lab focused on the creation of a boolean logic function using the parts we have built in the previous few labs. The requirements for the final layout were unusually well communicated and left little ambiguity, allowing complete focus on the circuit being built. However, the centerpiece of the lab focused on group interaction, which never once in my life have I seen work for more than 1/5 people, and if taken seriously would likely have resulted in several fires.

Part 1: 3 - Input NAND or NOR Gate

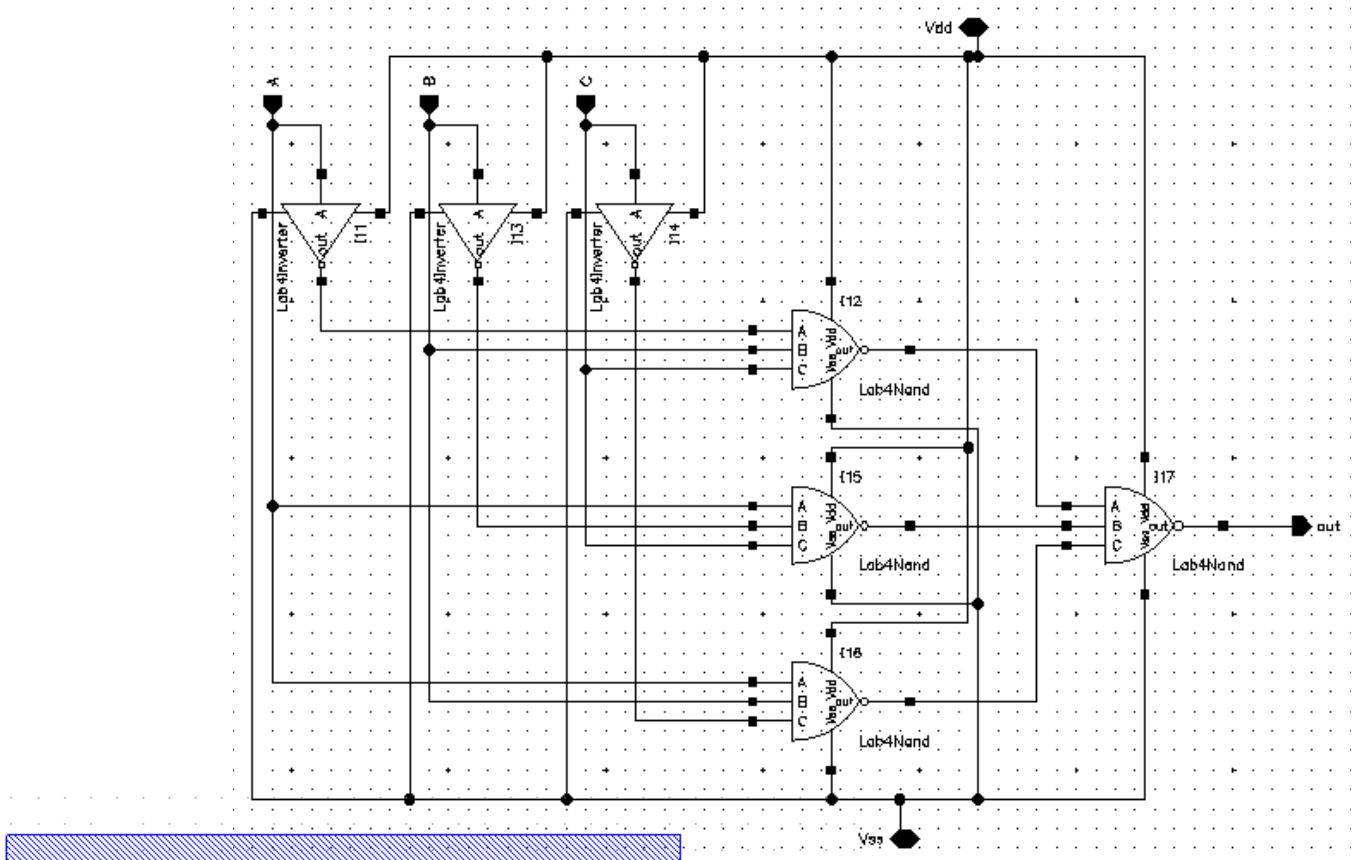


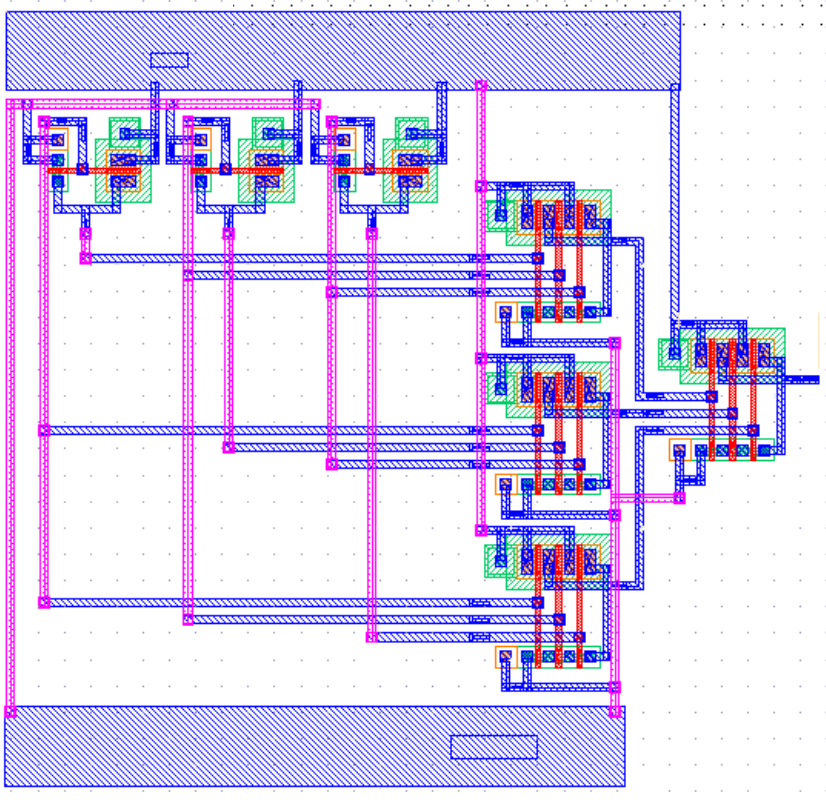


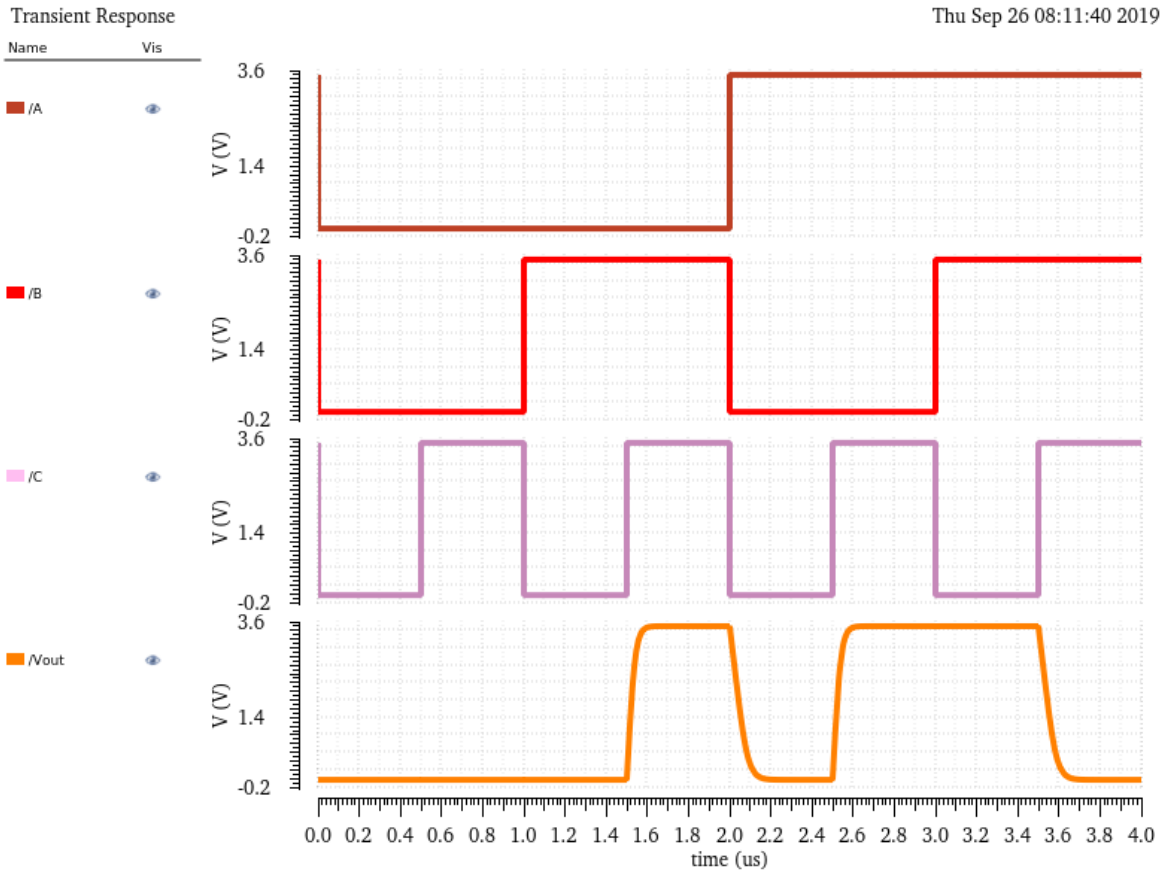


Part 3: Boolean Function

The function used in this portion of the lab was (!A*B*C)+(A*!B*C)+(A*B*!C)





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X = ( ! a && b && c ) || ( a && ! b && c ) ||
                ( a && b && ! c )
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a	b	c	X
0	0	0	0
0	0	1	0
0	1	0	0
0	1	1	1
1	0	0	0
1	0	1	1
1	1	0	1
1	1	1	0