

# Intro HPC: Blatt 8

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## **8.1 On the Memory Access Patterns of Supercomputer Applications: Benchmark Selection and Its Implications**

In this paper the author compares real supercomputer applications (from the Sandia National Laboratories) with integer and floating point benchmark suits. Thereby the main characteristics of a workload are: temporal locality (in order to use caches), spatial locality (as the applications's stride of memory access) and also the Data intensiveness (defined by the amount of unique data per interval of instructions).

In his results two points are crucial : The floating point SPEC suite it averages much less data intensiveness than Sandia workloads, which can have more influence on the performance than the actual efficiency of the storing. Secondly real integer workloads are much more harder on the memory system than the test suite. This means the performance is much more depending on the memory system.

This paper is showing that there are important differences in the behaviour between real applications and test suits. This is crucial since the test suits should actually tell hardware- and software designer what to focus at in order to achieve a higher performance in real applications not at test suits. Thus a significant difference could lead to wrong consequences.

## **8.2 On the Effects of Memory Latency and Bandwidth on Supercomputer Application Performance**

In this paper the author examines the memory performance of a suite of real applications (Sandia) on classicly on HP-systems used floating point workloads, but also on new emerging integer tasks. To determine this he analyses the importance of the latency ( as the time between a processors request of a memory value and the first arriving byte) and bandwidth (as the transfer speed of all the following data).

Thereby it is appearing that in floating point and even more in integer tasks the latency is more important than the bandwidth. Furthermore the integer workloads are much more memory sensitive. This has the consequences of: demonstrating some degree of "bandwidth headroom" in the construction of HPC-systems.

The author mentioned the future HPC to get much more memory sensitive, as a consequence of the emerging integer workloads, which he uses as given and is not talking very much about why they now occur. Therefore a good example are the problems of the field of graph theory which are getting more and more important also in the area of HPC. Often these are particularly challenging to a memory system.