

section <Eiger Port+ Refinement Proof Invariants (and important lemmas)>

```
theory CCv_Eiger_Port_modified_Invariants
  imports CCv_Eiger_Port_modified
begin
```

— <Invariants about kv store>

```
definition KVSNonEmp where
  "KVSNonEmp s  $\longleftrightarrow$  ( $\forall k. \text{DS } (\text{svrs } s \ k) \neq []$ )"
```

```
definition KVSNotAllPending where
  "KVSNotAllPending s k  $\longleftrightarrow$  ( $\exists i. i < \text{length } (\text{DS } (\text{svrs } s \ k)) \wedge \neg \text{v\_is\_pending } (\text{DS } (\text{svrs } s \ k) \ ! \ i)$ )"
```

```
definition KVSSNonEmp where
  "KVSSNonEmp s  $\longleftrightarrow$  ( $\forall k. \text{kvs\_of\_s } s \ k \neq []$ )"
```

— <Invariant about future and past transactions svrs>

```
definition FutureTIDInv where
  "FutureTIDInv s cl  $\longleftrightarrow$  ( $\forall n \ k. n > \text{txn\_sn } (\text{cls } s \ cl) \longrightarrow \text{wtxn\_state } (\text{svrs } s \ k) \ (\text{Tn\_cl } n \ cl) = \text{Ready}$ )"
```

```
definition PastTIDInv where
  "PastTIDInv s cl  $\longleftrightarrow$  ( $\forall n \ k. n < \text{txn\_sn } (\text{cls } s \ cl) \longrightarrow \text{wtxn\_state } (\text{svrs } s \ k) \ (\text{Tn\_cl } n \ cl) \in \{\text{Ready}, \text{Commit}\}$ )"
```

```
lemma other_sn_idle:
  assumes "FutureTIDInv s cl" and "PastTIDInv s cl"
  and "get_cl_txn t = cl" and "get_sn_txn t  $\neq$  txn_sn (cls s cl)"
  shows " $\wedge k. \text{wtxn\_state } (\text{svrs } s \ k) \ t \in \{\text{Ready}, \text{Commit}\}$ "
```

```
abbreviation not_committing_ev where
  "not_committing_ev e  $\equiv \forall cl \ kv\_map \ cts \ sn \ u. e \neq \text{RDone } cl \ kv\_map \ sn \ u \wedge e \neq \text{WCommit } cl \ kv\_map \ cts \ sn \ u$ "
```

```
abbreviation invariant_list_kvs where
  "invariant_list_kvs s  $\equiv \forall cl \ k. \text{FutureTIDInv } s \ cl \wedge \text{PastTIDInv } s \ cl \wedge \text{KVSNonEmp } s \wedge \text{KVSNotAllPending } s \ k$ "
```

subsection <Refinement Proof>

```
lemma pending_rtxn_inv:
  assumes " $\forall keys \ kv\_map. \text{txn\_state } (\text{cls } s \ cl) \neq \text{RtxnInProg } keys \ kv\_map$ "
  and " $\forall keys \ kv\_map. \text{txn\_state } (\text{cls } s' \ cl) \neq \text{RtxnInProg } keys \ kv\_map$ "
  and " $\forall cl'. cl' \neq cl \longrightarrow \text{cls } s' \ cl' = \text{cls } s \ cl$ "
  shows "pending_rtxn s' t = pending_rtxn s t"
```

```
lemma pending_wtxn_inv:
  assumes " $\forall kv\_map. \text{txn\_state } (\text{cls } s \ cl) \neq \text{WtxnPrep } kv\_map$ "
  and " $\forall kv\_map. \text{txn\_state } (\text{cls } s' \ cl) \neq \text{WtxnPrep } kv\_map$ "
  and " $\forall cl'. cl' \neq cl \longrightarrow \text{cls } s' \ cl' = \text{cls } s \ cl$ "
  shows "pending_wtxn s' t = pending_wtxn s t"
```

```
lemma kvs_of_s_inv:
  assumes "state_trans s e s'"
  and "invariant_list_kvs s"
  and "not_committing_ev e"
  shows "kvs_of_s s' = kvs_of_s s"
```

```
lemma finite_pending_wtxns:
  assumes "pending_wtxns (svrs s' k) t = Some x"
  and " $\forall k'. \text{finite } (\text{ran } (\text{pending\_wtxns } (\text{svrs } s \ k')))$ "
  and " $\forall k'. k' \neq k \longrightarrow \text{pending\_wtxns } (\text{svrs } s' \ k') = \text{pending\_wtxns } (\text{svrs } s \ k')$ "
  and " $\forall t'. t' \neq t \longrightarrow \text{pending\_wtxns } (\text{svrs } s' \ k) \ t' = \text{pending\_wtxns } (\text{svrs } s \ k) \ t$ "
  shows " $\forall k. \text{finite } (\text{ran } (\text{pending\_wtxns } (\text{svrs } s' \ k)))$ "
```

```
definition FinitePendingInv where
  "FinitePendingInv s svr  $\longleftrightarrow \text{finite } (\text{ran } (\text{pending\_wtxns } (\text{svrs } s \ svr)))$ "
```

```
lemma clock_monotonic:
  assumes "state_trans s e s'"
  shows "clock (svrs s' svr)  $\geq$  clock (svrs s svr)"
```

```
definition PendingWtsInv where
  "PendingWtsInv s  $\longleftrightarrow$  ( $\forall svr. \forall ts \in \text{ran } (\text{pending\_wtxns } (\text{svrs } s \ svr)). \ ts \leq \text{clock } (\text{svrs } s \ svr)$ )"
```

```
definition ClockLstInv where
  "ClockLstInv s  $\longleftrightarrow$  ( $\forall svr. \text{lst } (\text{svrs } s \ svr) \leq \text{clock } (\text{svrs } s \ svr)$ )"
```

```
lemma lst_monotonic:
  assumes "state_trans s e s'"
  shows "lst (svrs s' svr)  $\geq$  lst (svrs s svr)"

lemma gst_monotonic:
  assumes "state_trans s e s'"
  shows "gst (cls s' cl)  $\geq$  gst (cls s cl)"

lemma tm_view_inv:
  assumes "state_trans s e s'"
  and "not_committing_ev e"
  shows "cl_view (cls s' cl) = cl_view (cls s cl)"

end
```