

DISCIPLINE SPECIFIC CORE COURSE – DSC - 15: DIGITAL ELECTRONICS

Course Title & Code	Credits	Credit distribution of the course			Eligibility Criteria	Pre-requisite of the course
		Lecture	Tutorial	Practical		
Digital Electronics DSC – 15	4	3	0	1	Class XII pass with Physics and Mathematics as main subjects	NIL

LEARNING OBJECTIVES

The objective of the course is to introduce digital electronics and its simple applications to physics Honours students. The course is designed to familiarize the students with the different number systems (binary, octal and hexadecimal), laws of Boolean algebra, logic gates and combinational and sequential logic circuits utilised in designing counters and registers.

LEARNING OUTCOMES

This paper is one of the core papers in the Physics curriculum. After studying this paper students will become familiar with,

- Digital signals, positive and negative logic, Boolean variables, truth table, various number system codes and their inter-conversions.
- Students will be able to learn to minimise a given Boolean function using laws of Boolean algebra and Karnaugh map to minimise the hardware requirement of digital logic circuits.
- Understand the working principle of data processing circuits, arithmetic circuits, sequential logic circuits, registers, counters based on flip flops

SYLLABUS OF DSC - 15

THEORY COMPONENT

Unit – I - Integrated circuits

(2 Hours)

Integrated Circuits (Qualitative treatment only), active and passive components, discrete components, wafer, chip, advantages and drawbacks of ICs, scale of integration: SSI, MSI, LSI and VLSI (basic idea and definitions only), classification of ICs, examples of linear and digital ICs

Unit – II - Digital circuits and Boolean algebra

(14 Hours)

Difference between analog and digital circuits, binary number, decimal to binary and binary to decimal conversion, BCD, octal and hexadecimal numbers, AND, OR and NOT gates (realization using diodes and transistor), NAND and NOR gates as universal gates, XOR and XNOR gates and application as parity checkers

De Morgan's theorems, Boolean laws, simplification of logic circuit using Boolean algebra, fundamental products, idea of minterms and maxterms, conversion of truth table into equivalent logic circuit by (1) Sum of Products method and (2) Karnaugh map simplification (upto four variables).

Unit – III - Combinational Logic Circuits**(9 Hours)**

Data processing circuits: Multiplexers and its applications, de-multiplexers, decoders, encoders
Arithmetic logic circuits: Express binary number in signed and unsigned form, 1's and 2's complement representation, binary addition, binary subtraction using 2's complement, half and full Adders, half and full subtractors, 4-bit binary adder/subtractor using 2's complement method.

Unit – IV - Sequential Logic Circuits**(8 Hours)**

Flip Flops SR, D, and JK clocked (level and edge triggered) flip-flops, preset and clear operations, race-around conditions in JK flip-flop, master-slave JK flip-flop, conversion of one flip flop to another using an excitation table

Unit – V - Application of Sequential Logic Circuits**(9 Hours)**

Shift registers: Serial-in-Serial-out, Serial-in-Parallel-out, Parallel-in-Serial-out and Parallel-in-Parallel-out Shift Registers (only up to 4 bits).

Counters: Asynchronous counters, MOD-N synchronous counter designing using excitation table.

Unit – VI – Timers**(3 Hours)**

IC 555: Pin -out diagram, block diagram and its applications as astable multivibrator and monostable multivibrator

References:**Essential Readings:**

- 1) Digital Principles and Applications, A. P. Malvino, D. P. Leach and Saha, 7th edition, 2011, Tata McGraw
- 2) Fundamentals of Digital Circuits, A. Kumar, 2nd edition, 2009, PHI Learning Pvt. Ltd.
- 3) Digital Fundamentals, T. L. Floyd, 1994, Pearson Education Asia
- 4) Digital Principles and Applications, D. P. Leach and A. P. Malvino, 1995, Tata McGraw Hill
- 5) Digital Design, M. M. Mano and M. D. Ciletti, 2007, Pearson Education Asia
- 6) Digital Circuits and systems, Venugopal, 2011, Tata McGraw Hill.
- 7) Digital Electronics G. K. Kharate, 2010, Oxford University Press

Additional Readings:

- 1) Logic circuit design, S. P. Vingron, 2012, Springer
- 2) Digital Principles, R. L. Tokheim, 1994, Schaum's Outline Series, Tata McGraw-Hill
- 3) Solved Problems in Digital Electronics, S. P. Bali, 2005, Sigma Series, Tata McGraw-Hill
- 4) Digital Electronics: An Introduction To Theory And Practice, W. H. Gothmann, 2000, Prentice Hall of India
- 5) Modern Digital Electronics, R. P. Jain, 2003, Tata McGraw-Hill
- 6) Digital Electronics, S. Ghoshal, 2012, Cengage Learning
- 7) Digital Electronics, S. K. Mandal, 2010, 1st edition, McGraw Hill

PRACTICAL COMPONENT**(15 Weeks with 2 hours of laboratory session per week)**

**At least five experiments should be performed from the following list.
All designing should be done on the bread boards.**

- 1) (a) To design a combinational logic system for a specified truth table.
(b) To convert Boolean expression into logic circuit and design it using basic logic gate ICs
- 2) To minimize a given logic circuit using K-map and design using NAND gates.
- 3) Designing of Half Adder and Half Subtractor using NAND gates
- 4) Designing of 4-bit binary adder using adder IC.
- 5) To build Flip-Flop (RS, Clocked RS) circuits using NAND gates.
- 6) To build Flip-Flop (D-type and JK) circuits using NAND gate
- 7) To build a 3-bit Counter using D-type/JK Flip-Flop ICs and study timing diagrams.
- 8) To make a 4-bit Shift Register (serial and parallel) using D-type/JK Flip-Flop ICs.
- 9) To design an astable multivibrator of given specifications using 555 Timer.

References for laboratory work:

- 1) Digital Fundamentals, T. L. Floyd, 1994, Pearson Education Asia
- 2) Digital Principles and Applications, D. P. Leach and A. P. Malvino, 1995, Tata McGraw Hill
- 3) Digital Design, M. M. Mano and M. D. Ciletti, 2007, Pearson Education Asia
- 4) Digital Circuits and Systems, Venugopal, 2011, Tata McGraw Hill