6. Implement a Boolean function using 4 X 1 multiplexer.
7. To build a Flip- Flop Circuits using elementary gates. (RS, Clocked RS, D-type, JK, JK Master slave).
8. Design a SISO, SIPO shift register.
9. Design an asynchronous/ synchronous Up/Down counter using D/T/JK Flip-Flop.
10. Design a non sequential counter using D/T/JK Flip flop.
11. Design a R-2R DAC.
12. Design an ADC circuit using ADC0804.

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than ten.

Essential/recommended readings

1. M. Morris Mano, "Digital System Design," Pearson Education Asia.
2. Thomas L., "Flyod, Digital Fundamentals," Pearson Education Asia.
3. W. H. Gothmann, "Digital Electronics: An Introduction To Theory And Practice," Prentice Hall of India.
4. Millman & Grabel, "Microelectronics," Tata McGraw Hill.
5. Donald D. Givone, " Digital Principles and Design," Tata McGraw- Hill.
6. R. P. Jain, "Modern digital Electronics," Tata McGraw- Hill.

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.

DISCIPLINE SPECIFIC CORE COURSE– 6 (DSC-6): Analog Electronics-I

Credit distribution, Eligibility and Pre-requisites of the Course

Course title & Code	Credits	Credit distribution of the course			Eligibility criteria	Pre-requisite of the course (if any)
		Lecture	Tutorial	Practical/ Practice		
Analog Electronics-I	4	3	0	1	Class 12th Pass with PCM or Physics, Comp. Sc. & Maths.	Nil

Learning Objectives

The Learning Objectives of this course are as follows:

- Understand diodes (pn diode and Zener diode) and its applications in clipping and clamping circuits, rectifiers and voltage regulation (using Zener diodes) and concept of Power Supply.
- Understand frequency response of BJT and MOSFET amplifiers.
- Understand the concept of feedback and design feedback amplifiers and oscillators.
- Understand different power amplifiers and single tuned amplifiers.

Learning outcomes

After completion of the course, students will be able to-
Illustrate about rectifiers, transistor and MOSFET amplifiers and its biasing. Also compare the performances of its low frequency models.
Describe the frequency response of MOSFET and BJT amplifiers.
Explain the concepts of feedback and construct feedback amplifiers and oscillators.
Summarizes the performance parameters of amplifiers with and without feedback

SYLLABUS OF DSC-6

UNIT – I Diode applications (09 Hours)

Diode Circuits: Ideal diode, piecewise linear equivalent circuit, dc load line, static and dynamic resistance, Quiescent (Q) point. Clipping and clamping circuits. Rectifiers: HWR, FWR (center tapped and bridge). Circuit diagrams, working and waveforms, ripple factor & efficiency, Voltage doubler
Filters: Circuit diagram and explanation of shunt capacitor filter with waveforms.
Voltage Regulator: Zener diode regulator circuit diagram and explanation for load and line regulation

UNIT – II BJT based Amplifiers and Oscillator (12 Hours)

Transistor: Input and Output Characteristics, Concept of Biasing and its significance, Concept of DC and AC analysis. Overview of Common Emitter BJT amplifier, Concept of Darlington pair
Power Amplifiers: Difference between voltage and power amplifier, classification of power amplifiers (Class A, Class B, Class AB, Class C, Class D), Concept of Class A single ended power amplifier, Transformer coupled Class A power amplifier and complementary symmetry Class B push pull power amplifier, overall efficiency, concept of crossover distortion, harmonic distortion and heat sinks.

Feedback Amplifiers: Concept of feedback, negative and positive feedback, voltage (series and shunt), feedback amplifiers gain, input and output impedances. Barkhausen criterion for oscillations, RC phase shift oscillator

UNIT – III MOSFET Fundamentals (12 Hours)

MOSFET: Operation of n-channel and p-channel MOSFETs, Overview of Depletion and Enhancement MOSFET, Transfer Characteristics, Drain Characteristics, MOSFET as a switch. short channel effects, non-ideal effects in MOS transistors: the finite output resistance in the saturation region, the body effect, subthreshold conduction, breakdown effects, and temperature effects.

MOSFET DC analysis: Biasing circuits- drain feedback, voltage divider, source feedback, bias stability, Graphical analysis, load line.

UNIT – IV MOSFET based Amplifiers (12 Hours)

MOSFET AC analysis: AC equivalent circuit of MOSFET, MOSFET parameters,

MOSFET Amplifiers: circuit and small signal model of Common Source amplifier, small signal parameters: input resistance, output resistance and voltage gain, circuits of Common Drain and Common Gate configurations. Comparison of BJT based (CE, CB and CC) and MOSFET based (CS, CD, CG) - Qualitative only.

Multistage MOSFET circuits: Cascaded circuits and Cascode circuits, effect of multistage circuits on gain and bandwidth.

MOSFET Application circuits: CMOS as inverter circuit, depletion mode n-MOSFET and p-MOSFET as load device

Practical component (if any) - Analog Electronics-I Lab – 30 Hours

(Hardware and Circuit Simulation Software)

1. Study of the half wave or full wave rectifier
2. Study of Zener diode as voltage regulator.
3. Study of any two types of
   (a) clipping circuits
   (b) clamping circuits.
4. Study of a Single Stage CE amplifier.
5. Study of Class A or Class B Power Amplifiers.
6. Study of Voltage divider bias for MOSFET
7. Study of the frequency response of Common Source MOSFET amplifier.
8. Study of MOSFET based Phase Shift Oscillator

Note: Students shall sincerely work towards completing all the above listed practicals for this course. In any circumstance, the completed number of practicals shall not be less than seven.

Essential/recommended readings

1. Electronic Devices and circuit theory, Robert Boylestad and Louis Nashelsky, 9th Edition, 2013, PHI
2. Donald A. Neamen, Electronic Circuit Analysis and Design, Tata McGraw Hill (2002)
3. Electronic devices, David A Bell, Reston Publishing Company
4. Giovanni Saggio, Principles of Analog Electronics, CRC Press (2014)
5. D. L. Schilling and C. Belove, Electronic Circuits: Discrete and Integrated, Tata McGraw Hill (2002)
6. J. Millman and C. C. Halkias, Integrated Electronics, Tata McGraw Hill (2001)
7. J. J. Cathey, 2000 Solved Problems in Electronics, Schaum's outline Series, Tata McGraw

Note: Examination scheme and mode shall be as prescribed by the Examination Branch, University of Delhi, from time to time.