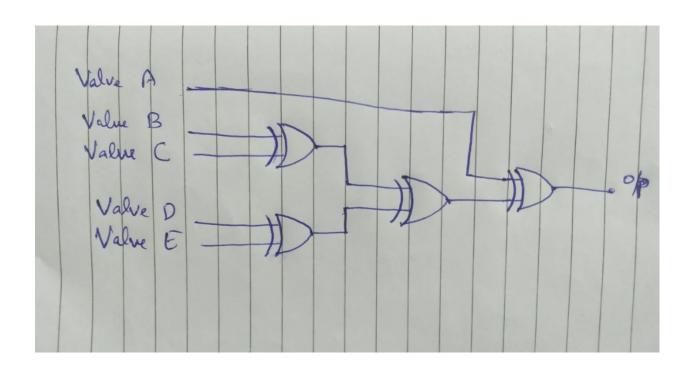
LEVEL-4 ANSWER KEY

ANSWER: 1



ANSWER: 2

Ani). The quition had mentioned of Mod2d Mod5 scountee, so the contestants could have used a single 10 7490 to get the sequired pattern or even using Mod2 & Mod5 sequetilips also good.

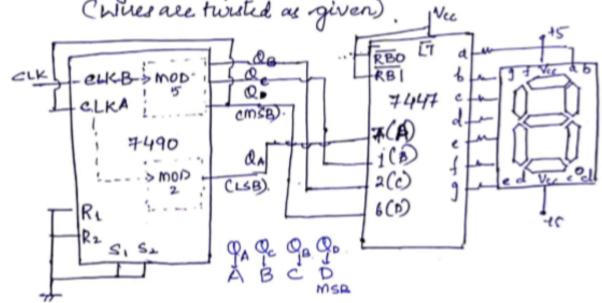
Ø The number sequence to be generated is:

A→ E → C→ G → J → B → F → D → H → J

O→ 4 → 2 → 6 → 8 → 1 → 5 → 3 → 7 → 9

Toget this pattern, give the mainclock to Mad5's clk. Lormest the MEB of Mod5 say Qo to the ilp clock of Mod 2 counter. Indoing so, you will generate the pattern as 0-2-4-6-8-1-3-5-7-9.

But to get the required pattern, here is the twist. The up of Fregment display driver should be given as QDQQQA, where Qz is the MSB.



ANSWER: 3

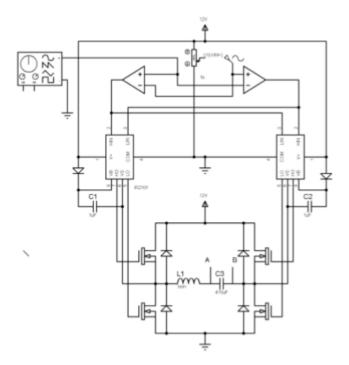
To produce a sine wave at the output, the control signals driving the mosfet drivers (IR2101) must be changed. Currently it is running in fixed PWM mode. (Triangle with a single reference from potentiometer). 50% duty cycle

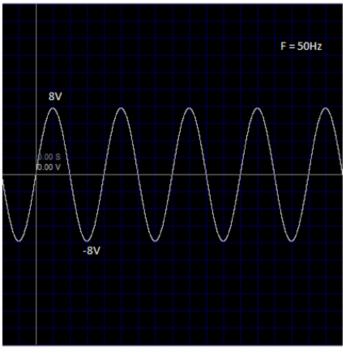
Change the control signal by applying a 50Hz sine wave A = 5Vpp < 8Vpp(TRI-wave) with offset = 4V(Triangle has an offset of <math>4V) to the comparators, Instead of the POT.

Also the triangle wave frequency must be increased from 50Hz to 5-10KHz.

Modified Circuit.

Sine wave ,F = 50Hz,A = 5Vpp Triangle wave F = 5-10KHz,A = 8Vpp Add a LC low pass filter at the load to get sine wave at the output.





LC low pass with cut off < 1KHz Add sine wave f = 50Hz,offset = 4V Increase triangle wave freq 5-10KHz Add Ic low pass filter.

ANSWER: 4

Χ	Υ	Transfer Chara
3V 400Hz Sin Phase Shift=0	3V 400Hz Sin Phase Shift=270	
Wave 1:4V 400Hz Sin,Phase Shift=0 Wave 2:1V 2.4KHz Sin,Phase Shift=180	Wave 1:4V 400 Hz Sin, Phase shift=270 Wave 2:1V 2.4Khz Sin, Phase Shift=90	
5V 400Hz <u>Sin, Phase</u> Shift=0	Wave 1:1.5V800Hz Sin, Phase shift=180 Wave 2:3V800Hz Sin, Phase shift=0	
Ground	2V 800Hz Sin, Phase shift=0	

The above wave forms are generated and multiplexed using 4051 mux at 200Hz

