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Quiz 1 Questions

Multiple Choice

1 point possible (graded, results hidden)

The purpose of a hardware port is to provide:

☐ Security☐ Data Encryption.☒ Data Transfer.☐ None of the above.[Submit](#)


You have used 1 of 1 attempt

Answer submitted.

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Now suppose the following values occurred at the same time:

D1 -> e1 =0 and f1 =1, D2-> e2 =0 and f2 = 1.

In this situation which of the following actions would occur?

☐ D1 will read from D2

☐ D1 will write to D2


☐ D2 will write to D1

☐ D2 will read from D1

☒ Bus Error/Conflict.

Submit

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 Answer submitted.

Multiple Choice

1 point possible (graded, results hidden)

Which of the following is false about External Bus Interface?


☐ It can use serial transmission.

☐ It can use parallel transmission.

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☐ None of the above.

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 Answer submitted.

Multiple Choice

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If you want to screen share a movie from your laptop to your TV and you can use only one port. What will you use?

☐ VGA.

☐ USB.

☒ HDMI.

☐ Firewire.

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etc.

☐ Things on the hard drive need to be located, read and sent to ROM before they can be processed.

☐ The CPU can run the programs directly, which are stored in the computer's hard-drive / hard-disk.

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i Answer submitted.

Multiple Choice

1 point possible (graded, results hidden)

Which devices perform as both input/output devices?

☐ Monitor, Memory Card, Mouse.

☐ Disk Drive, Printer, Tape Drive.

☒ USB Flash Drive, Tape Drive, Memory Card.

☐ Headphones, USB Flash Drive, Disk Drive.


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Suppose two devices A and A' are communicating with a two bit data bus. In the bus controller e is used for read enable and f is used for write enable, also e and f are for A alongside e' and f' are for A'. Now if you want to disconnect A entirely from the system, what should you do?

☐ e = 1, f = 0.


☐ e = 1, f = 1.

☒ e = 0, f = 0.

☐ e' = 0, f' = 0.

Submit

You have used 1 of 1 attempt

 Answer submitted.

Multiple Choice

1 point possible (graded, results hidden)

Which of the following is false for a Peripheral Device?

☐ It is often dependent on the host computer.

☒ It forms part of the core computer architecture.

☐ It increases the host computer's abilities.

☐ None of the above.

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i Answer submitted.

Multiple Choice

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Select the correct information that is a characteristic of Serial Transmission.

☒ The data bits are organized in a specific order.

☐ It requires many simultaneous data lines.

☐ Example: Sending data from CPU to RAM.

☐ Unsynchronized data bits result in a higher chance of Crosstalk.

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i Answer submitted.

Multiple Choice

1 point possible (graded, results hidden)

Suppose you bought a gaming headphone with built in microphone for game streaming. What kind of device is it?:


☐ Input Device.

☐ Output Device.

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 Answer submitted.

Multiple Choice

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What is the purpose of the external bus interface?

☐ Allows the CPU to read data from RAM.


☒ Allows peripheral devices to interface with the computer.

☐ It cannot carry data from a keyboard to the computer.

☐ Provides interfacing between the internal components of a computer.

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Multiple Choice


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Two components A and B started driving the bus at the same time which caused a short circuit? It was because..

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☒ A and B were writing at the same time.

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Multiple Choice

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Which statement is true?

☒ To disconnect a device from a two bit bus only read and write control should be disabled.

☐ Address line in Bus is Bidirectional


☐ Interface is only responsible for transmitting analog to digital signal

☐ Control Line in Bus is responsible for carrying binary data

☐ All of the mentioned

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Multiple Choice

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- ☐ It interfaces at low level devices.
- ☐ The output of it is the input for a microcontroller.
- ☐ It converts non-electrical signals to electrical signals for further processing by the Processing unit.
- ☒ All of them mentioned above.

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You have used 1 of 1 attempt

 Answer submitted.

Multiple Choice

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Which is not the cause of Bus Error?

- ☒ Multiple devices used as input

☐ Glitch

☐ Wait states

☐ All of the mentioned

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