Digital Electronics- 2CS303

UNIT-1 Binary Codes

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Binary Codes

- 1. Weighted Codes (8421, 5421)
- 2. Non Weighted codes (Excess-3, BCD, Gray/Reflected codes)
- 3. Error Detection Codes (PARITY Bits)
- 4. Alphanumeric Codes (ASCII)

Codes

- The **problem** found with computer is how to represent numerals, alphabets, and special characters
- Since data is in the binary form, it must be **converted to a more readable** form known as **Coded form**

1: Weighted Codes:

Weighted binary codes are those binary codes which obey the positional weight principle. Each position of the number represents a

specific weight.

Decimal.	8421	7/101	1710	*	*	*	*	*	
ar you to	would.	7.43	5421	5211	4221	3321	2421	8421	7421
0	0000	0000	0000	0 0 0 0	0000	0000	0000	0000	0000
1+ 20	0001		0001						
. (2)	0010		0 0 10						100000000000000000000000000000000000000
3	0011	0011	0 0 11	0 1 0 1	0011	0100	0011	0101	1010
4	0100	0100	0 1 0 0	0111	1000	0101	0100	0100	0100
5	0101	0101	1000	1000	1001	0110	0101	1011	1010
	0110	0110	1001	1010	1010	1100	0110	1010	1001
0	0111	1000							
8	1000	1001	0011	1110	1110	1110	0110	10001	111
9	1001	1010	FLAN	1111	-11			1000	100

2: Non-Weighted Codes:

They do **not have a fixed weight** assigned to each symbol position in the code word. For example, ASCII, BCD, Exess-3, and Gray code.

2.1: Excess-3 is a non-weighted coding method. With excess-3, we add 3 to a decimal number before converting it to binary.

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Example: (0001)2 = (0100)Excess-3 (0010)2 = (0101)Excess-3
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2.2: BCD (Binary Coded Decimals) is a non-weighted coding method. Individual decimal digits are converted into equivalent binary bits.

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Example:

(321)2 = (0011 0010 0000)BCD

(000)2 = (0000 0000 0000)BCD

(80)2 = (1000 0000)BCD

(00)2 = (0000 0000)BCD

(10)10 = (0001 0000) BCD

(11)10 = (0001 0001) BCD
```

BCD

Delim	ial.	8421 BCD.
0		0000 1
00002	1100	0001
1003	PSIA	0010
0004		0011 0010
5	0110	0100 Unbacked BC
6		0101
901 7	1110	0110
1018	000 f	0111
110	- 4001	1000
1110	013	1001
11		0001 00007
12		0001 0001
1007		0001 00 10 > Packed BCD.
13		0001 00 11
15		0001 0100
13		0001 0101

2: Non-Weighted Codes:

2.3: Gray Codes: It is also called as Reflected Binary codes. It is generated via getting mirror image of given data. Only 1 bit will change each time when the decimal number is incremented. Where as the binary system requires all four bits to change when going from 7 to 8

Example: 4 bit Gray codes.

Deli mat	0	1	2	3	4.	5	6	7	8	9	10	41	12
Gray.	0000	0001	0011	0010	0110	0111	0101	0100	1100	1101	HIP	1110	1010
Decimal,	13	14		15							11	10	
Gray.	1011	100	01 1	000								01	

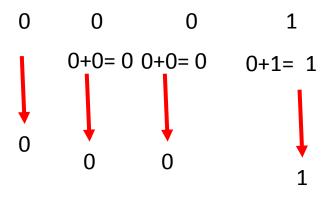
2: Code Conversion:

Converting Binary Codes to Gray codes

Method:

- 1. Copy MSB of Binary code to MSB of Gray code. (As it is)
- 2. Add MSB of Binary with Next MSB of Binary to get next Gray code.
- 3. Discard the carry
- 4. Repeat the same process till we get the LSB

Example: Convert (0001)2 into Gray code.



Decemal.	Binany.	Gray.
0	0000	0000
1	0001	0001
2	0010	0011
3	0011	0010
4	0100	0110
5	0101	0111
6	0110	0 101
8131	0111	0 100
8	1000	1100
1001	1001	110100
10	1010	1111.13
11	1011	1010
12	1100	1010
13	1101	1011
14	11.10	1001
15	1111	1000

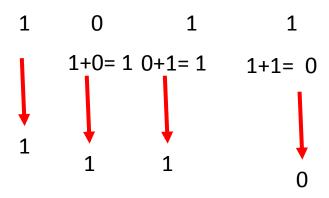
2: Code Conversion:

Converting Binary Codes to Gray codes

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- 1. Copy MSB of Binary code to MSB of Gray code.
- 2. Add MSB of Binary with Next MSB of Binary to get next Gray code.
- 3. Discard the carry
- 4. Repeat the same process till we get the LSB

Example: Convert (1011)2 into Gray code.



Decemal.	Binany.	Gray.
0	0000	0000
1	0001	0001
2	0010	0011
3	0011	0010
4	0100	0110
5	0101	0111
6	0110	0 101
87131	0111	0 100
8	1000	1100
1231	1001	1101001
10	1010	1111111
11	1011	1010
12	1100	1010
13	1101	1011
14	11.10	1
15	1111	1001

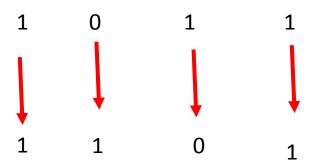
2: Code Conversion:

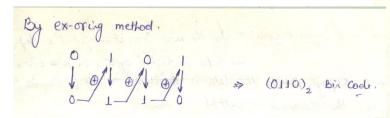
Converting Gray Codes to Binary codes

Method:

- 1. Copy MSB of Gray code to MSB of Binary code. (As it is)
- 2. If next bit of Gray code is "1", then invert the present binary bit as next bit.
- 3. If next bit of Gray code is "0" then copy the present binary bit as next bit.

Example: Convert (1011) gray into Binary code.





processor and the same of the		
Decimal	Gray	Binary.
0	0000	0000
1	0001	0001
2	0011	0010
3	0010	0 0 1 1
4	0110	0100
6	0111	0101
7	0101	0110
8	0100	0111
9	1100	1 000
10	1101	1 0 0 1
19 0	1111	01010
	1110	1011
12	1010	1100
14	1011	1101
15	1001	1110
13	1000	1111

Binary to BCD and BCD to Binray:

Decimal 1	Bin BCD	Delin	al BCD	RAIGH
00	00 0000			Binary
1 00	01 0001	0	0000	0000
2 00		1	0001	0001
3 00		2	0010	0010
4 010	20	3	0011	0011
5 010	0100	4	0100	0 000
6	0101	2011 25010	0101	0101
7 011	0110	6	0110	
8 011	0111	1	0111	
9 100	0 1000	8	1000	1000
100	1001	9 3	1001	
1010	00010000	10	0001 0000	1001
2 1011		The short	000 0001	1010
1100	00010001	12	00010010	1011
1101	00010010		00010011	1100
1116	00010011		00010100	1101
1111	00010100	2 / 12 / 9 1 1	00010101	1110

BCD to Excess-3 and Excess-3 to BCD:

37 BCD to Ex	uss-3:-	4r Excus-3 to BCD:-				
O 0000 1 0000 2 0010 3 0011 5 0100 6 0101 6 0110 9 1000 1000	Excess-3 0011 0100 0101 0110 0111 1000 1001 1010	Decimal 0 1 2 3 4 5 6 10 1 1 7 10 20	Excess-3 0011 0100 0101 0110 0111 1000 1011	BCD. 0000 0001 0010 0011 0100 0101 0110		

Binary to Excess-3 and Excess-3 to Binary:

Delinal	Bin.	Excess-3.	Decimal	Excess-3	Bon
0	0000	001100	300	0011	0000
1 2	0001	0100		0100	0001
3	0010	0101	0.20	0101	0010
4	0011	0110	3	0110	0011
5	0100	0111	0343	0111	0100
6	0101	1000	5	1000	0101
7	0.110	1001010	06 0	1001	0110
8	0111	1010	7	1010	0111
9	1000	101101	80.	1011	1000
10	1010	1100	9	1100	1001
11	1011	1101	100:	1101	1010
12	1100	1110	12011	1110	1011

BCD Arithmetic:

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Example 1: (83) +(11) =(94)

83     1000 0011

1001 0100 = (94)
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Error Detecting Codes

ERROR DETECTING CODES: - Digital System must- be accurate to the diget, errors can become a Serious problem Now error detecting codes plays on important role they identify the error & then Send the message in the form of acknowledgement . ir Parity :- This is the Simple technique for error delicting. Accord to this technique, adding an extra bit known as panily bil., to each word being transmitted. For odd panily this bit, Set to all or o at the 1x Such that the Sum of the I bits in the entire Word is odd. DalaBels. Total no. of 13 1010110 In above e.g. for each case total no of words to in the word has be odd in cluding the porty bit. At the receiving end. if a word is received that has an Even no. of 1's, the receiver will request a retransmission. Since most-errors occurs as a single-bit inversion, this system works quite well. But if two errors occurs within the word, they Would remain undetected.

Error Detecting Codes

```
Alond it is totally depends upon the User that wheath he or She require odd panily or even panily. In Case of even panily the Sum of no of 1's will be equal to even.

Cog. Panilyber Databer Total no of 1's.

1 1010111 6

Problem:- 1 0000111 4

Attach even panily bits to the message 'HELP' in Ascus coole.
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Message HELLO using Ascusods

With odd panty.

H 1 1001000

E 0 1000101

L 0 1001100

Data bik.

Odd panty bits.
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