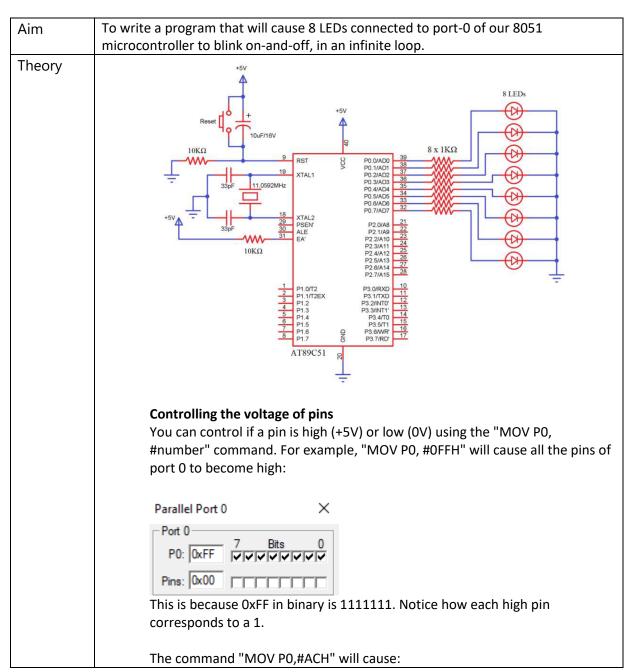
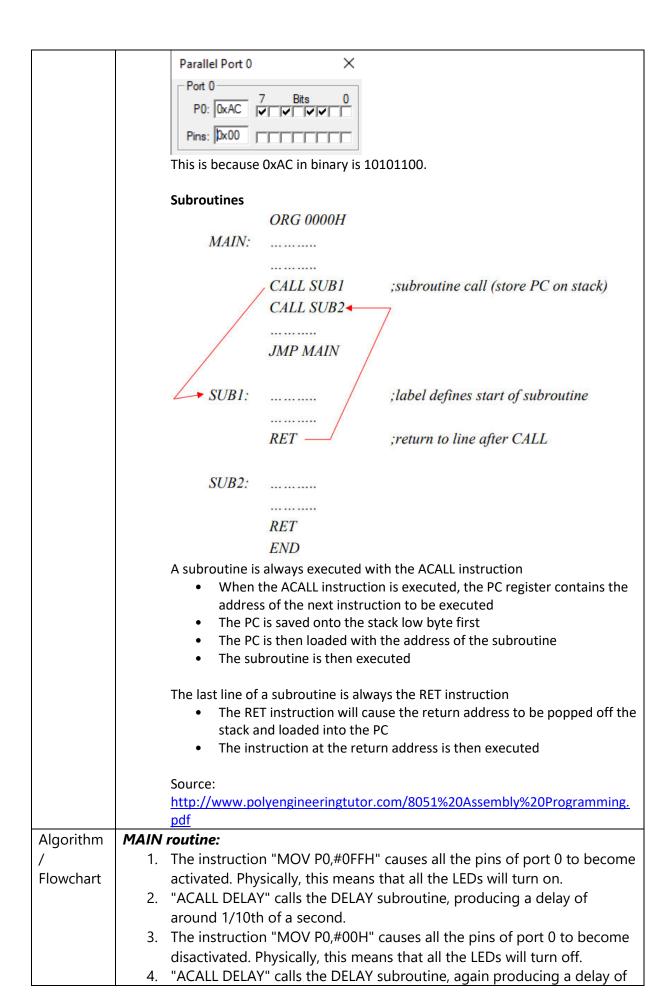
Date: 19/02/2021

Student's name	Anuj Shah
Roll Number	18104B0024
Name of	Professor Mohit Gujar
Professor	

Experiment	3
number	
Experiment title	Interfacing of LED's with 8051 ports
Hardware	_
requirement	
Software	Keil uVision5
requirement	





around 1/10th of a second.

5. "JMP MAIN" causes the whole MAIN routine to be executed again.

Thus, we see that the MAIN routine produces an infinite loop, in which all the LEDs are on for 0.1 seconds, and then they are off for 0.1 seconds. We thus get a blinking effect.

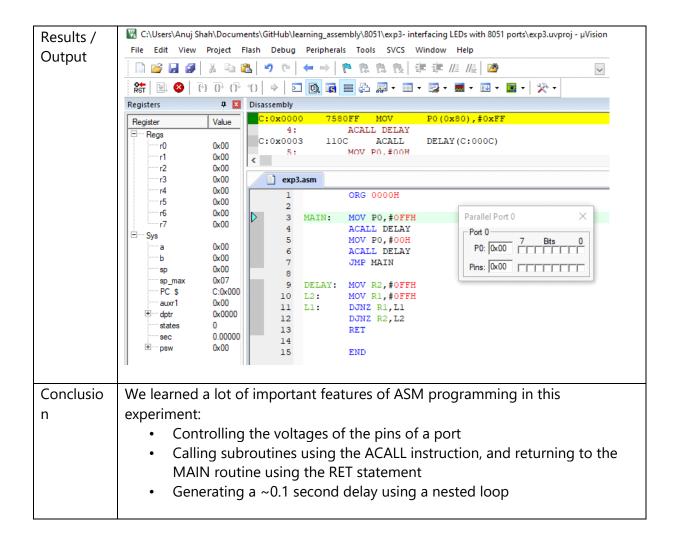
## **DELAY** subroutine:

- 1. The instruction "MOV R2,#0FFH" causes the hex number 0xFF (decimal = 255) to be loaded into register R2.
- 2. The instruction "MOV R1,#0FFH" causes the hex number 0xFF (decimal = 255) to be loaded into register R1.
- 3. The instruction "DJNZ R1,L1" decrements register R1 (eg. 0xFF to 0xFE), and as long as R1 doesn't become 0x00, it will keep jumping back to this step.
- 4. The instruction "DJNZ R2,L2" decrements register R2 (eg. 0xFF to 0xFE), and as long as R2 doesn't become 0x00, it will jump to step 2.
- 5. The "RET" statement let's us leave the DELAY subroutine, and go back to the MAIN routine.

The delay subroutine is a nested loop. Both L1 (the inner loop) and L2 (the outer loop) are executed 255 (FF) times. Because each machine cycle is approximately 1 microsecond, and because a DJNZ instruction requires 2 machine cycles, we thus get a total delay of approximately:

(2\*255\*255)\*1 microsecond = 130050 micrseconds ~ 130 milliseconds = 0.13 seconds ~ 1/10th of a second

## Program exp3.asm 1 ORG 0000H 2 3 MAIN: MOV PO, #OFFH 4 ACALL DELAY 5 MOV PO, #00H ACALL DELAY 6 7 JMP MAIN 8 9 DELAY: MOV R2, #0FFH 10 L2: MOV R1,#0FFH DJNZ R1,L1 11 L1: 12 DJNZ R2, L2 13 RET 14 15 END



Faculty Sign

Grade received