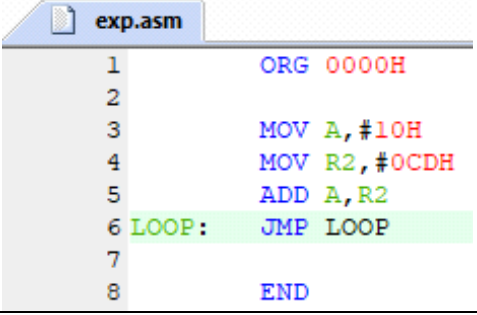
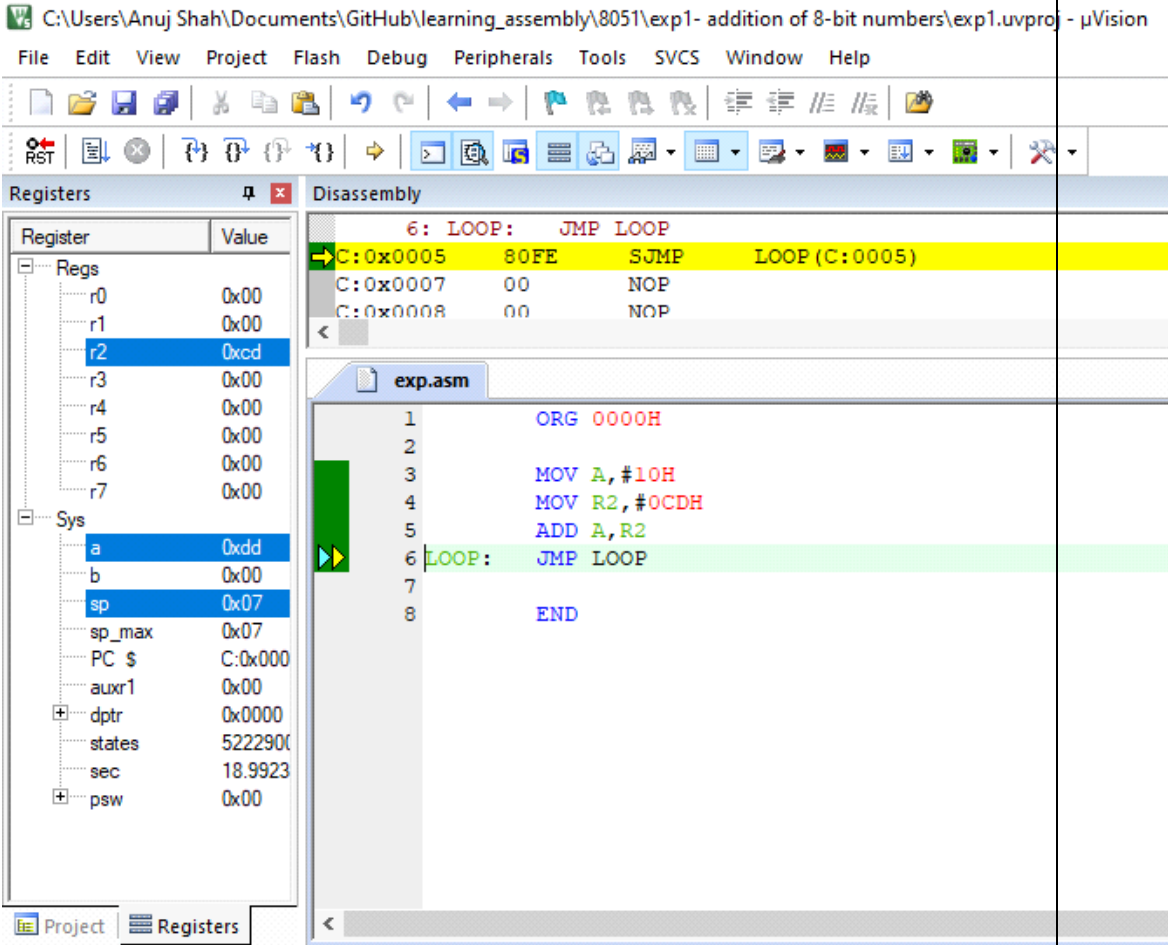


Date: 21/02/2021

Student's name	Anuj Shah
Roll Number	18104B0024
Name of Professor	Professor Mohit Gujar

Experiment number	1
Experiment title	Add two 8-bit numbers
Hardware requirement	–
Software requirement	Keil uVision5

Aim	To write a program to add two 8-bit numbers.
Theory	<p>An assembler program is made up of 3 elements:</p> <ul style="list-style-type: none">• Instructions: for example, MOV, ADD, SJMP• Assembler directives: for example, ORG, END• Assembler controls <p>The 8051 uses 4 addressing modes:</p> <ul style="list-style-type: none">• Immediate addressing: MOV A,#10 (moves the decimal number 10 into register A), MOV R0, #0AH (moves the Hex number 0xA into register R0)• Register addressing: MOV A,R0 (copies the contents of register R0 to register A)• Direct addressing: MOV A,20H (copies the contents of address 0x20 to register A), MOV 30H,40H (copies the contents of address 0x40 to address 0x30), MOV P1,A (moves the contents of register A to port 1)• Indirect addressing: The most powerful addressing mode. MOV R0, #20H (moves the Hex number 0x20 into register R0), MOV @R0, #55H (moves the Hex number 0x55 to the address contained in R0, which is 0x20; R0 acts as a pointer to address 0x20), MOV A,@R0 (copy the contents of address 0x20 to register A) <p>Here are 2 useful directives in 8051:</p> <ul style="list-style-type: none">• END: Last line of code. Assembler will not compile after this line.• ORG: Origin directive. Sets the location counter address for the following instructions. <p>Program branching: Normal program execution is sequential. However, program branching instructions allow the programmer to alter the program execution sequence.</p> <ul style="list-style-type: none">• Unconditional jump (JMP) : This instruction will automatically load the PC (program counter) with a new address, and will automatically jump to the instruction at that address.• Conditional jumps: These instructions will only jump if a certain condition is true. They are similar to "If" statements in C. <p>Source:</p>

	http://www.polyengineeringtutor.com/8051%20Assembly%20Programming.pdf																																						
Algorithm/ Flowchart	<ol style="list-style-type: none"> 1. The directive "ORG 0000H" indicates the start of the program. 2. The command "MOV A,#10H" moves the hex number 10 (decimal = 16) to register A. 3. The command "MOV R2,#0CDH" moves the hex number CD (decimal = 205) to register B. 4. The command "ADD A,B" adds the value in register B with the value in register A, and stores the result in register A. In our program, the sum of 10h and CDh is DDh (decimal = 221). 5. The command "LOOP: SJMP LOOP" creates an infinite loop. This command is often included at the end of 8051 programs, because there is no other way to halt the microcontroller. https://stackoverflow.com/questions/43459467/why-do-some-8051-program-ends-with-the-code-loop-sjmp-loop 6. The directive "END" indicates the end of the program. 																																						
Program	 <pre> 1 ORG 0000H 2 3 MOV A, #10H 4 MOV R2, #0CDH 5 ADD A, R2 6 LOOP: JMP LOOP 7 8 END </pre>																																						
Results/ Output	 <p>C:\Users\Anuj Shah\Documents\GitHub\learning_assembly\8051\exp1- addition of 8-bit numbers\exp1.uvproj - uVision</p> <p>File Edit View Project Flash Debug Peripherals Tools SVCS Window Help</p> <p>Registers</p> <table border="1"> <thead> <tr> <th>Register</th> <th>Value</th> </tr> </thead> <tbody> <tr><td>r0</td><td>0x00</td></tr> <tr><td>r1</td><td>0x00</td></tr> <tr><td>r2</td><td>0xcd</td></tr> <tr><td>r3</td><td>0x00</td></tr> <tr><td>r4</td><td>0x00</td></tr> <tr><td>r5</td><td>0x00</td></tr> <tr><td>r6</td><td>0x00</td></tr> <tr><td>r7</td><td>0x00</td></tr> <tr><td>a</td><td>0xdd</td></tr> <tr><td>b</td><td>0x00</td></tr> <tr><td>sp</td><td>0x07</td></tr> <tr><td>sp_max</td><td>0x07</td></tr> <tr><td>PC</td><td>0x0000</td></tr> <tr><td>auxr1</td><td>0x00</td></tr> <tr><td>dptr</td><td>0x0000</td></tr> <tr><td>states</td><td>522290</td></tr> <tr><td>sec</td><td>18.9923</td></tr> <tr><td>psw</td><td>0x00</td></tr> </tbody> </table> <p>Disassembly</p> <pre> 6: LOOP: JMP LOOP C:0x0005 80FE SJMP LOOP (C:0005) C:0x0007 00 NOP C:0x0008 00 NOP </pre> <p>exp.asm</p> <pre> 1 ORG 0000H 2 3 MOV A, #10H 4 MOV R2, #0CDH 5 ADD A, R2 6 LOOP: JMP LOOP 7 8 END </pre>	Register	Value	r0	0x00	r1	0x00	r2	0xcd	r3	0x00	r4	0x00	r5	0x00	r6	0x00	r7	0x00	a	0xdd	b	0x00	sp	0x07	sp_max	0x07	PC	0x0000	auxr1	0x00	dptr	0x0000	states	522290	sec	18.9923	psw	0x00
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Conclusion	<p>We learned a lot of fundamentals of 8051 programming in this experiment:</p> <ul style="list-style-type: none"> • ORG and END directives, used to start and stop a program. • Immediate addressing • JMP (unconditional jumps) and Infinite loops

Faculty Sign

Grade received