

## TP-Link Archer C5 v4

This page was created specifically for the Archer C5 v4 which uses a MediaTek SoC. (Earlier C5 v1.x and v2.x HW versions use Qualcomm and Broadcom SoC respectively)

*This is a Dual band router with 5 Gigabit (1000Mbps) ethernet ports and a single USB2 port. It is advertised as AC1200 for its 867Mbps (2x2) 5GHz band and 300 Mbps (2x2) 2.4GHz band.*

There is also a 'C5 W v4' offered in some countries such as Brazil.

Front - Back - Version



## Supported Versions

Brand	Model	Version	Current Release	OEM Info	Forum Topic	Technical Data
TP-Link	Archer C5	v4	external image	<a href="https://www.tp-link.com/us/support/download/archer-c5/">https://www.tp-link.com/us/support/download/archer-c5/</a> [ <a href="https://www.tp-link.com/us/support/download/archer-c5/">https://www.tp-link.com/us/support/download/archer-c5/</a> ]	<a href="https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889">https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889</a> [ <a href="https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889">https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889</a> ]	View/Edit data

## Hardware Highlights

Model	Version	SoC	CPU MHz	Flash MB	RAM MB	WLAN Hardware	WLAN2.4	WLAN5.0	Gbit ports	USB
Archer C5	v4	MediaTek MT7620A	580	8	64	MediaTek MT7620A, MediaTek MT7612E	b/g/n	a/n/ac	5	1x 2.0

## Installation

→ [Install OpenWrt \(generic explanation\)](#)

The good news : OpenWrt is working properly on the device. However, the official device support is still work in progress.

The following procedure works only on the **V4** model. Please, don't try this on any other revision.

You can check which version your Archer C5 is by looking at the bottom of it or, in the router's web interface, **Status** page, **Hardware Version** information, if you are using stock firmware.

You can download the unofficial images from this location <https://github.com/benwht/openwrt/releases> [<https://github.com/benwht/openwrt/releases>]

## Installation via TFTP (Recovery mode)

It is possible to install OpenWrt over TFTP on this device. However, in some older bootloader versions this function is broken and you can't use it. More info can be found in the U-Boot section.

→ [generic.flashing.tftp](#)

Download the **tftp-recovery** image from <https://github.com/benwht/openwrt/releases> [<https://github.com/benwht/openwrt/releases>]:

[openwrt-19.07.3-ramips-mt7620-tplink\\_c5-v4-squashfs-tftp-recovery.bin](#)

[[https://github.com/benwht/openwrt/releases/download/19.07.3/openwrt-19.07.3-ramips-mt7620-tplink\\_c5-v4-squashfs-tftp-recovery.bin](https://github.com/benwht/openwrt/releases/download/19.07.3/openwrt-19.07.3-ramips-mt7620-tplink_c5-v4-squashfs-tftp-recovery.bin)]

To flash the image:

1. rename the **openwrt-19.07.3-ramips-mt7620-tplink\_c5-v4-squashfs-tftp-recovery.bin** to **tp\_recovery.bin**
2. start a TFTP server from IP address **192.168.0.66** and serve the image named **tp\_recovery.bin**
3. connect your device to the router LAN port (1-4)
4. to start the TFTP recovery process on the router, press and hold the “Reset button” and then power up the router. Keep the “Reset button” pressed until the WPS LED turns on (it's the LED with two arrows pointing in different directions)
5. If everything went well, you should see a read request on your TFTP server

## Installation via serial connection

OpenWrt can be flashed via serial connection too (e.g. in case the TFTP method is not working)

Download the **factory.bin** image from <https://github.com/benwht/openwrt/releases> [<https://github.com/benwht/openwrt/releases>]:

[openwrt-19.07.2-ramips-mt7620-tplink\\_archer-c5-v4-squashfs-factory.bin](#)

[[https://github.com/benwht/openwrt/releases/download/19.07.2/openwrt-19.07.2-ramips-mt7620-tplink\\_archer-c5-v4-squashfs-factory.bin](https://github.com/benwht/openwrt/releases/download/19.07.2/openwrt-19.07.2-ramips-mt7620-tplink_archer-c5-v4-squashfs-factory.bin)]

[openwrt-18.06.7-ramips-mt7620-tplink\\_c5-v4-squashfs-factory.bin](#) [[https://github.com/benwht/openwrt/releases/download/18.06.7/openwrt-18.06.7-ramips-mt7620-tplink\\_c5-v4-squashfs-factory.bin](https://github.com/benwht/openwrt/releases/download/18.06.7/openwrt-18.06.7-ramips-mt7620-tplink_c5-v4-squashfs-factory.bin)]

To flash the image:

1. rename the **openwrt-19.07.2-ramips-mt7620-tplink\_archer-c5-v4-squashfs-factory.bin** to **test.bin**
2. start a TFTP server from IP address **192.168.0.225** and serve the image named **test.bin**
3. connect your device to the router LAN port (1-4)
4. power up the router and press **4** on the console to stop the boot process
5. enter the following commands on the router console

```
tftp 0x80060000 test.bin
erase tplink 0x20000 0x7a0000
cp.b 0x80060000 0x20000 0x7a0000
reset
```

After a successful flash with the above methods, you will be able to directly upgrade OpenWrt via web interface.

More information is provided in the [OpenWrt forum - C5 v4 \[https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889\]](https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889).

The above installation procedure is also successful for the 'Archer C5 W', offered in some countries such as Brazil. [OpenWrt forum - C5 W Brazil \[https://forum.openwrt.org/t/archer-c5-w-brazil-version/46901/6\]](https://forum.openwrt.org/t/archer-c5-w-brazil-version/46901/6)

The brazilian Archer C5(W) V4 (at least those using Archer\_C5(BRWISP)v4\_3.16.0\_0.9.1\_up(190404)\_2019-04-04\_09.56.27.bin) can be flashed using TFTP Recovery (without serial cable). With images generated with this patch (<https://github.com/openwrt/openwrt/pull/2174> [https://github.com/openwrt/openwrt/pull/2174]), you need to add 131072 bytes (0x20000 in hex) before the image:

```
dd if=openwrt-ramips-mt7620-tplink_archer-c5-v4-squashfs-factory.bin bs=512 seek=256 of=tp_recovery.bin
```

After that, offer that file using tftp at 192.168.0.66. Start the tftp firmware recovery holding reset while powering on.

## Upgrade

Download the unofficial 19.07.3 or 18.06.7 **sysupgrade.bin** image from (<https://github.com/benwht/openwrt/releases> [https://github.com/benwht/openwrt/releases]):

[openwrt-19.07.3-ramips-mt7620-tplink\\_c5-v4-squashfs-sysupgrade.bin](https://github.com/benwht/openwrt/releases/download/19.07.3/openwrt-19.07.3-ramips-mt7620-tplink_c5-v4-squashfs-sysupgrade.bin)  
[https://github.com/benwht/openwrt/releases/download/19.07.3/openwrt-19.07.3-ramips-mt7620-tplink\_c5-v4-squashfs-sysupgrade.bin] (with MediaTek's RTL8367s driver)

[openwrt-18.06.7-ramips-mt7620-tplink\\_c5-v4-squashfs-sysupgrade.bin](https://github.com/benwht/openwrt/releases/download/18.06.7/openwrt-18.06.7-ramips-mt7620-tplink_c5-v4-squashfs-sysupgrade.bin)  
[https://github.com/benwht/openwrt/releases/download/18.06.7/openwrt-18.06.7-ramips-mt7620-tplink\_c5-v4-squashfs-sysupgrade.bin] (with MediaTek's RTL8367s driver)

## Back to stock firmware

Download the official image from your country support page [https://www.tp-link.com/hu/support/download/archer-c5-v4/#Firmware].

E.g. in case of Hungary : [Archer\\_C5\\_EUWISP\\_1206.rar](https://static.tp-link.com/2019/201902/20190204/Archer_C5_EUWISP_1206.rar) [https://static.tp-link.com/2019/201902/20190204/Archer\_C5\_EUWISP\_1206.rar].

### restoring back to the stock firmware via TFTP (Recovery mode)

To flash the original TP-Link factory image:

1. extract the image and rename the **Archer\_C5(EUWISP)v4\_3.16.0\_0.9.1\_up\_boot(181119)\_2018-11-20\_13.35.25.bin** to **tp\_recovery.bin**
2. the stock firmware will not work directly! Cut 512 bytes from the beginning, e.g.:

```
dd if=Archer_C5\ (BRWISP\ )v4_3.16.0_0.9.1_up_boot\ (190404\ )_2019-04-04_09.56.27.bin of=tp_recovery.bin bs=512 skip=1
```

1. start a TFTP server from IP address **192.168.0.66** and serve the image named **tp\_recovery.bin**
2. connect your device to the router (LAN port 1-4)
3. to start the TFTP recovery process on the router, press and hold the “Reset button” and then power up the router. Keep the “Reset button” pressed until the WPS LED turns on (it's the LED with two arrows pointing in different directions)
4. If everything went well, you should see a read request on your TFTP server

### restoring back to the stock firmware via serial connection

To flash the original TP-Link factory image:

1. extract the stock firmware image and rename the **Archer\_C5(EUWISP)v4\_3.16.0\_0.9.1\_up\_boot(181119)\_2018-11-20\_13.35.25.bin** to **tp\_recovery.bin**
2. start a TFTP server from IP address **192.168.0.225** and serve the image named **tp\_recovery.bin**
3. connect your device to the router (LAN port 1-4)
4. power up the router and press **4** on the console to stop the boot process.

5. enter the following commands on the router console

```
tftp 0x80060000 tp_recovery.bin
erase tpLINK 0x20000 0x7a0000
cp.b 0x80080200 0x20000 0x7a0000
reset
```

The router will reboot itself and the original TP-Link software will boot up.

## How to compile a new version

Here is how you can compile a new OpenWrt version for yourself \* prepare a build environment ([quickstart-build-images](#))

```
git clone https://git.openwrt.org/openwrt/openwrt.git/
cd openwrt
git fetch --tags
git checkout v18.06.4
./scripts/feeds update -a
./scripts/feeds install -a
wget -qO- https://github.com/benwht/openwrt/commit/b57307fa1e498e9b82fe53cdc58e6005a73baef.patch | git apply -v
wget -qO .config https://downloads.openwrt.org/releases/18.06.4/targets/ramips/mt7620/config.seed
make defconfig
make menuconfig
```

- set Target System: **MediaTek Ralink MIPS**
- set Subtarget: **MT7620..**
- set Target Profile: **TP-Link Archer C5 v4**
- **SAVE !**

```
make -j1 V=s
```

- during the build process include the RTL8367s switch patch, select **Y(es)**

## How to compile 19.07.1 version

The patch can't be picked on v19.07.1 tag without conflict, but it can be easily modified to work. (The change of platform.sh can be dropped.)

## How to compile image with pre-build kmod (kernel module) package compatibility

Read the following article: <https://hamy.io/post/0015/how-to-compile-openwrt-and-still-use-the-official-repository/>  
[<https://hamy.io/post/0015/how-to-compile-openwrt-and-still-use-the-official-repository/>]

The most important part of the solution is step 4.

## Hardware

### Opening/closing the case

Note: If you do it carefully then this will NOT void your warranty as there are no parts which would be damaged

The case of the Archer C5 v4 is composed of 2 pieces:

- Top cover (white)
  - Bottom cover (gray)
1. Remove the two screws on the bottom cover.
  2. Use a thin object to release the top cover from the bottom cover

### Putting it back together

1. Put the top cover back on

2. Press until it clicks back nicely to the bottom cover
3. Put back the screws

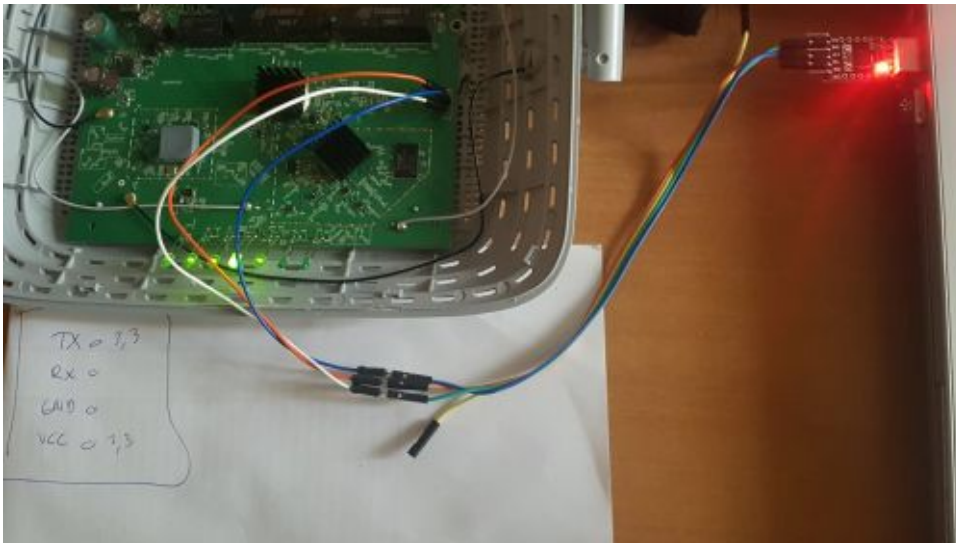
## Serial connection

Serial port: , voltage, 115200 bps, 8N1.


Serial port layout is (NO hardware modification needed):

- (1) TX
- (2) RX
- (3) GND
- [4] VCC (3.3V)

*Serial connection (click to enlarge) :*



Soldering is not needed, you can connect to the serial header as shown in the picture. Don't forget that the TX pin of the serial port must be linked to the RX pin of the router and the RX to TX!

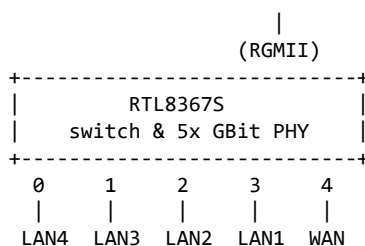
**Warning:** Don't connect the Vcc pin of the serial port to a -to-USB adapter. This might damage the board. There's no need to connect it, communication works without Vcc.

## Switch

The Archer C5 v4 uses an external Gbit switch, connected by MDIO bus. Network traffic to the CPU is passed through the internal switch of the MT7620A SoC.

MediaTek already provided a patch [<https://github.com/objelf/linux/commit/32326d5eb43fc183329985c6331832d9ed155491>] for Linux kernel 4.14 which adds support to the RTL8367S switch. This is what is used in the current OpenWrt v18.06 versions.

An updated OpenWrt RTL8367 driver with RTL8367S chip support is available for OpenWrt since 2019 summer, however the code is still under review process as it might affect other devices negatively. This will deliver official OpenWrt support for the device, till that happens it's backported and used in my v19.07 versions.



## GPIO

LED  
\* GPIO42: POWER  
\* GPIO14: 2.4G LED  
\* GPIO12: 5G LED  
\* GPIO09: INTERNET\_ORANGE  
\* GPIO10: INTERNET\_GREEN  
\* GPIO08: LAN LED  
\* GPIO07: USB LED  
\* GPIO01: WPS LED  
BTN  
\* GPIO13: RESET  
\* GPIO02: WIFI/WPS  
CTRL  
\* GPIO60: RESET Switch

## U-Boot

U-Boot 1.1.3 (Nov 19 2018 - 11:11:36) Ralink UBoot Version: 5.0.0.0

Press 't' or '4' to enter U-Boot command line (CLI)

4: System Enter Boot Command Line Interface.

U-Boot 1.1.3 (Nov 19 2018 - 11:11:36)  
MT7620 #

Press '7' to download new U-Boot code

7: System Load Boot Loader then write to Flash via Serial.  
## Ready for binary (kermit) download to 0x82000000 at 115200 bps...

Unfortunately the stock images dated before April of 2019 had a bug and the TP recovery function was not working on them. This can be fixed by updating the U-Boot (mtd0) to a newer version.

**Latest U-Boot versions** (by country, as of Feb, 2020):

- HU boot(181119), TP recovery is not working
- ES boot(190115), TP recovery is not working
- BR boot(190404), TP recovery is working properly
- BG/CZ/PL/RO/SK boot(190815), TP recovery is working properly **(tested on a HU device)**
- RU/UA boot(191017), TP recovery is working properly

## Bootlogs

### OEM bootlog

U-Boot 1.1.3 (Nov 19 2018 - 11:11:36) Board: Ralink APSoC DRAM: 64 MB relocate\_code Pointer at: 83fb4000 enable ephy clock...done.  
rf reg 29 = 5 SSC disabled. spi\_wait\_nsec: 29 spi device id: c8 40 17 c8 40 (4017c840) Warning: un-recognized chip ID, please update  
bootloader! ===== Ralink UBoot Version: 5.0.0.0 -----  
----- ASIC 7620\_MP (Port5<->GigaSW) DRAM component: 512 Mbits DDR, width 16 DRAM bus: 16 bit Total memory: 64  
MBytes Flash component: SPI Flash Date:Nov 19 2018 Time:11:11:36  
===== icache: sets:512, ways:4, linesz:32 ,total:65536 dcache: sets:256,  
ways:4, linesz:32 ,total:32768 ##### The CPU freq = 580 MHZ ##### estimate memory size =64 Mbytes rt\_rtl8367\_init(1705):Begin  
Wait for RTL8367C Ready . RTL8367C is ready now! rt\_rtl8367\_init(1749):Call Func rt\_rtl8367\_enableRgmii() continue to starting  
system. 0 disable switch forward... 3: System Boot system code via Flash.(0xbc020000) do\_bootm:argc=2, addr=0xbc020000 ## Booting  
image at bc020000 ... Uncompressing Kernel Image ... OK No initrd ## Transferring control to Linux (at address 804799e0) ... ## Giving  
linux memsize in MB, 64 Starting kernel ... LINUX started... THIS IS ASIC SDK 5.0.S.0 Linux version 3.10.14 (root@cxg) (gcc version  
4.6.3 (Buildroot 2012.11.1) ) #1 Mon Nov 19 11:16:02 CST 2018 The CPU feqence set to 580 MHz PCIE: bypass PCIE DLL. PCIE:  
Elastic buffer control: Addr:0x68 -> 0xB4 disable all power about PCIE CPU0 revision is: 00019650 (MIPS 24KEc) Software DMA cache

coherency Determined physical RAM map: memory: 04000000 @ 00000000 (usable) Initrd not found or empty - disabling initrd Zone ranges: Normal [mem 0x00000000-0x03ffffff] Movable zone start for each node Early memory node ranges node 0: [mem 0x00000000-0x03ffffff] Primary instruction cache 64kB, 4-way, VIPT, linesize 32 bytes. Primary data cache 32kB, 4-way, PIPT, no aliases, linesize 32 bytes Built 1 zonelists in Zone order, mobility grouping on. Total pages: 16256 Kernel command line: console=ttyS1,115200n8 root=/dev/mtdblock2 rootfstype=squashfs PID hash table entries: 256 (order: -2, 1024 bytes) Dentry cache hash table entries: 8192 (order: 3, 32768 bytes) Inode-cache hash table entries: 4096 (order: 2, 16384 bytes) Writing ErrCtl register=0000cd83 Readback ErrCtl register=0000cd83 Memory: 58476k/65536k available (4621k kernel code, 7060k reserved, 1159k data, 296k init, 0k highmem) NR\_IRQS:128 console [ttyS1] enabled Calibrating delay loop... 385.02 BogoMIPS (lpj=770048) pid\_max: default: 4096 minimum: 301 Mount-cache hash table entries: 512 ftrace: allocating 13580 entries in 27 pages NET: Registered protocol family 16 RALINK\_GPIOMODE = 1ab41d RALINK\_GPIOMODE = 18b41d PPLL\_CFG1=0xe7c000 MT7620 PPLL lock PPLL\_DRV = 0x80080504 start PCIe register access RALINK\_RSTCTRL = 2400000 RALINK\_CLKCFG1 = 75afffc0 \*\*\*\*\* MT7620 PCIe RC mode \*\*\*\*\* PCIE0 enabled Port 0 N\_FTS = 1b105000 init\_rt2880pci done bio: create slab <bio-0> at 0 PCI host bridge to bus 0000:00 pci\_bus 0000:00: root bus resource [mem 0x20000000-0x2ffffff] pci\_bus 0000:00: root bus resource [io 0x10160000-0x1016ffff] pci\_bus 0000:00: No busn resource found for root bus, will use [bus 00-ff] pci 0000:00:00.0: bridge configuration invalid ([bus 00-00]), reconfiguring pci 0000:00:00.0: BAR 0: can't assign mem (size 0x80000000) pci 0000:00:00.0: BAR 8: assigned [mem 0x20000000-0x200ffff] pci 0000:00:00.0: BAR 9: assigned [mem 0x20100000-0x201ffff pref] pci 0000:00:00.0: BAR 1: assigned [mem 0x20200000-0x2020ffff] pci 0000:01:00.0: BAR 0: assigned [mem 0x20000000-0x200ffff 64bit] pci 0000:01:00.0: BAR 6: assigned [mem 0x20100000-0x2010ffff pref] pci 0000:00:00.0: PCI bridge to [bus 01] pci 0000:00:00.0: bridge window [mem 0x20000000-0x200ffff] pci 0000:00:00.0: bridge window [mem 0x20100000-0x201ffff pref] BAR0 at slot 0 = 0 bus=0x0, slot = 0x0 res[0]->start = 0 res[0]->end = 0 res[1]->start = 20200000 res[1]->end = 2020ffff res[2]->start = 0 res[2]->end = 0 res[3]->start = 0 res[3]->end = 0 res[4]->start = 0 res[4]->end = 0 res[5]->start = 0 res[5]->end = 0 bus=0x1, slot = 0x0 res[0]->start = 20000000 res[0]->end = 200ffff res[1]->start = 0 res[1]->end = 0 res[2]->start = 0 res[2]->end = 0 res[3]->start = 0 res[3]->end = 0 res[4]->start = 0 res[4]->end = 0 res[5]->start = 0 res[5]->end = 0 Switching to clocksource MIPS NET: Registered protocol family 2 TCP established hash table entries: 512 (order: 0, 4096 bytes) TCP bind hash table entries: 512 (order: -1, 2048 bytes) TCP: Hash tables configured (established 512 bind 512) TCP: reno registered UDP hash table entries: 256 (order: 0, 4096 bytes) UDP-Lite hash table entries: 256 (order: 0, 4096 bytes) NET: Registered protocol family 1 Load Ralink Timer0 Module Load Ralink Timer1 Module MTK/Ralink EHCI/OHCI init. squashfs: version 4.0 (2009/01/31) Phillip Lougher NTFS driver 2.1.30 [Flags: R/W]. fuse init (API version 7.22) msgmni has been set to 114 io scheduler noop registered (default) Serial: 8250/16550 driver, 4 ports, IRQ sharing enabled serial8250: ttyS0 at MMIO 0x10000500 (irq = 37) is a 16550A serial8250: ttyS1 at MMIO 0x10000c00 (irq = 12) is a 16550A Ralink gpio driver initialized brd: module loaded deice id : c8 40 17 c8 40 (4017c840) Warning: un-recognized chip ID, please update SPI driver! GD25Q64B(c8 40170000) (8192 Kbytes) mtd .name = raspi, .size = 0x00800000 (8M) .erasesize = 0x00010000 (64K) .numeraseregions = 0 Creating 7 MTD partitions on "raspi": 0x000000000000-0x0000000020000 : "boot" 0x0000000020000-0x0000000220000 : "kernel" 0x0000000220000-0x00000007c0000 : "rootfs" mtd: partition "rootfs" set to be root filesystem 0x00000007c0000-0x00000007d0000 : "config" 0x00000007d0000-0x00000007e0000 : "romfile" 0x00000007e0000-0x00000007f0000 : "ispconfig" 0x00000007f0000-0x0000000800000 : "radio" Register flash device:flash0 PPP generic driver version 2.4.2 PPP MPPE Compression module registered NET: Registered protocol family 24 register rt2860 <-- RTMPAllocTxRxRingMemory, Status=0 <-- RTMPAllocAdapterBlock, Status=0 device\_id =0x7662 ==>rtl\_wlan\_chip\_onoff(): OnOff:1, Reset= 1, pAd->WlanFunCtrl:0x0, Reg-WlanFunCtrl=0x20a RtmpChipOpsEepromHook::e2p\_type=2, inf\_Type=5 NVM is FLASH mode (pAd->flash\_offset = 0x7f8000) get\_dev\_name\_prefix(): dev\_idx = 1, dev\_name\_prefix=rai rdm\_major = 253 spiflash\_ioctl\_read, Read from 0x007df100 length 0x6, ret 0, retlen 0x6 Read MAC from flash( 7df100) ffffffff8-0d-17-xx-xx-ffffffxx netif\_napi\_add() called with weight 128 on device eth0 SMACCR1 -- : 0x0000d80d SMACCR0 -- : 0x17xxxxxx Ralink APSoC Ethernet Driver Initialization. v3.1 512 rx/tx descriptors allocated, mtu = 1500! NAPI enable, Tx Ring = 512, Rx Ring = 512 spiflash\_ioctl\_read, Read from 0x007df100 length 0x6, ret 0, retlen 0x6 Read MAC from flash( 7df100) ffffffff8-0d-17-xx-xx-ffffffxx SMACCR1 -- : 0x0000d80d SMACCR0 -- : 0x17xxxxxx PROC INIT OK! Mirror/redirect action on u32 classifier Performance counters on input device check on Actions configured Netfilter messages via NETLINK v0.30. nf\_conntrack version 0.5.0 (913 buckets, 3652 max) gre: GRE over IPv4 demultiplexor driver ip\_tables: (C) 2000-2006 Netfilter Core Team Type=Linux TCP: cubic registered NET: Registered protocol family 10 ip6\_tables: (C) 2000-2006 Netfilter Core Team sit: IPv6 over IPv4 tunneling driver NET: Registered protocol family 17 Ebtables v2.0 registered 8021q: 802.1Q VLAN Support v1.8 registered taskstats version 1 VFS: Mounted root (squashfs filesystem) readonly on device 31:2. Freeing unused kernel memory: 296K (805a6000 - 805f0000) starting pid 713, tty ": '/etc/init.d/rcS' SCSI subsystem initialized usbcore: registered new interface driver usbfs usbcore: registered new interface driver hub usbcore: registered new device driver usb ehci\_hcd: USB 2.0 'Enhanced' Host Controller (EHCI) Driver ehci-platform: EHCI generic platform driver ehci-platform ehci-platform: EHCI Host Controller ehci-platform ehci-platform: new USB bus registered, assigned bus number 1 ehci-platform ehci-platform: irq 18, io mem 0x101c0000 ehci-platform ehci-platform: USB 2.0 started, EHCI 1.00 hub 1-0:1.0: USB hub found hub 1-0:1.0: 1 port detected ohci\_hcd: USB 1.1 'Open' Host Controller (OHCI) Driver ohci-platform ohci-platform: Generic Platform OHCI Controller ohci-platform ohci-platform: new USB bus registered, assigned bus number 2 ohci-platform ohci-platform: irq 18, io mem 0x101c1000 hub 2-0:1.0: USB hub found hub 2-0:1.0: 1 port detected usbcore: registered new interface driver usb-storage tp\_domain init ok PPTP driver version 0.8.5 l2tp\_core: L2TP core driver, V2.0 l2tp\_ppp: PPPoL2TP kernel driver, V2.0 Please press Enter to activate this console. [

```

ipt_init ] cmd: /var/tmp/dconf/rc.router [ dm_readFile ] 2193: can not open xml file /var/tmp/pc/reduced_data_model.xml!, about to
open file /etc/reduced_data_model.xml spiflash_iocctl_read, Read from 0x007c0000 length 0x10000, ret 0, retlen 0x10000
spiflash_iocctl_read, Read from 0x007c0000 length 0x948a, ret 0, retlen 0x948a ==>Enter routerspiflash_iocctl_read, Read from
0x007df100 length 0x6, ret 0, retlen 0x6 mode spiflash_iocctl_read, Read from 0x007df500 length 0x29, ret 0, retlen 0x29
spiflash_iocctl_read, Read from 0x007df600 length 0x21, ret 0, retlen 0x21 spiflash_iocctl_read, Read from 0x007df700 length 0x10, ret 0,
retlen 0x10 spiflash_iocctl_read, Read from 0x007df200 length 0x4, ret 0, retlen 0x4 spiflash_iocctl_read, Read from 0x00020000 length
0x200, ret 0, retlen 0x200 [ rsl_initDevInfo ] cmd: echo 0 > /proc/tplink/mspiflash_iocctl_read, Read from 0x007df100 length 0x6, ret 0,
retlen 0x6 anufacture_flag [ rsl_getManagementServerObj ] 492: cannot set connectionRequestURL yet because no WAN intf is up [
rsl_getManagementServerObj ] 492: cannot set connectionRequestURL yet because no WAN intf is up [ rsl_getManagementServerObj ]
492: cannot set connectionRequestURL yet because no WAN intf is up [ tr143_main ] 162: 909 start select, maxFd: 7, msgFd.fd: 5,
compFd: 7 sendto: No such file or directory pid 786 send 2001 error [ oal_startDynDns ] cmd: dyndns /var/tmp/dconf/dyndns.conf [
oal_startNoipDns ] cmd: noipdns /var/tmp/dconf/noipdns.conf [ oal_vlan_fetchState ] 351: Failed to read VLAN file. iocctl: No such
device [ oal_br_addBridge ] cmd: brctl addbr br0 [ oal_br_addBridge ] cmd: brctl setfd br0 0 [ oal_br_addBridge ] cmd: brctl stp br0 off [
oal_ipt_addLanRules ] cmd: iptables -t filter -A INPUT -i br+ -j ACCEPT [ oal_intf_setIntf ] cmd: ifconfig br0 192.168.0.0 use br_hw_addr!
.1 netmask 255.255.255.0 up [ oal_util_setProcLanAddr ] cmd: echo "br0 16820416," > /proc/net/c 0: 0: C:4C: 5:FFFFFFF80
ontract_LocalAdRaeth v3.1 (dr [ oal_intf_eNAPI nableIntf ] cmd:,SkbRecycle ifconfig eth0 u) p phy_tx_ring = 0x02d28000, tx_ring =
0xa2d28000 phy_rx_ring0 = 0x02d2a000, rx_ring0 = 0xa2d2a000 RTL8367C is ready now! SMACCR1 -- : 0x0000d80d SMACCR0 -- :
0x17xxxxxx ESW: Link Status Changed - Port5 Link UP CDMA_CSG_CFG = 81000000 GDMA1_FWD_CFG = 20710000 [
oal_intf_enableIntf ] cmd: ifconfig eth0.3 up ifconfig: iocctl 0x8913 failed: No such device [ oal_intf_enableIntf ] cmd: ifconfig eth0.4 up
ifconfig: iocctl 0x8913 failed: No such device [ oal_intf_enableIntf ] cmd: ifconfig eth0.5 up ifconfig: iocctl 0x8913 failed: No such device [
oal_intf_enableIntf ] cmd: ifconfig eth0.6 up ifconfig: iocctl 0x8913 failed: No such device [ rsl_getUnusedVlan ] 1153: GET UNUSED
VLAN TAG 1 : [3] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN
TAG 3 : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_addVlanTagIntf ] cmd: vconfig add eth0 3 [
oal_intf_enableIntf ] cmd: ifconfig eth0.3 up set if eth0.3 to *not wan dev [ oal_intf_enableIntf ] cmd: ifconfig eth0.3 up [
oal_addVlanTagIntf ] cmd: vconfig add eth0 4 [ oal_intf_enableIntf ] cmd: ifconfig eth0.4 up set if eth0.4 to *not wan dev [
oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_addVlanTagIntf ] cmd: vconfig add eth0 5 [ oal_intf_enableIntf ] cmd: ifconfig
eth0.5 up set if eth0.5 to *not wan dev [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_addVlanTagIntf ] cmd: vconfig add eth0 6 [
oal_intf_enableIntf ] cmd: ifconfig eth0.6 up set if eth0.6 to *not wan dev [ oal_intf_disableIntf ] cmd: ifconfig eth0.6 down [
oal_addVlanTagIntf ] cmd: vconfig add eth0 2 [ oal_intf_enableIntf ] cmd: ifconfig eth0.2 up set if eth0.2 to wan dev [
oal_br_delIntfFromBridge ] cmd: brctl delif br0 eth0.3 brctl: bridge br0: Invalid argument [ oal_intf_enableIntf ] device eth0.3 entered
promiscuous mode cmd: ifconfig edevice eth0 entered promiscuous mode th0.3 up [ oal_br0: port 1(eth0.3) entered forwarding state
br_addIntfIntoBrbr0: port 1(eth0.3) entered forwarding state idge ] cmd: brctl addif br0 eth0.3 [ rsl_getUnusedVlan ] 1153: GET
UNUSED VLAN TAG 1 : [3] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ rsl_getUnusedVlan ] 1153: GET
UNUSED VLAN TAG 3 : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_intf_enableIntf ] cmd: ifconfig eth0.3
up [ oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_intf_disableIntf ] cmd:
ifconfig eth0.6 down [ oal_br_delIntfFromBridge ] cmd: brctl delif br0 eth0.4 brctl: bridge br0: Invalid argument [ oal_intf_enableIntf ]
cmd: ifconfig eth0.4 up device eth0.4 entered promiscuous mode [ oal_br_addIntfbr0: port 2(eth0.4) entered forwarding state IntoBridge ]
cmdbr0: port 2(eth0.4) entered forwarding state : brctl addif br0 eth0.4 [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 1 : [3] [
rsl_getUnubr0: port 2(eth0.4) entered disabled state sedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ rsl_getUnusedVlan ] 1153: GET
UNUSED VLAN TAG 3 : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_intf_enableIntf ] cmd: ifconfig eth0.3
up [ oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_intf_disableIntf ] cmd:
ifconfig eth0.6 down [ oal_br_delIntfFromBridge ] cmd: brctl delif br0 eth0.5 brctl: bridge br0: Invalid argument [ oal_intf_enableIntf ]
cmd: ifconfig eth0.5 up device eth0.5 entered promiscuous mode [ oal_br_addIntfbr0: port 3(eth0.5) entered forwarding state IntoBridge ]
cmdbr0: port 3(eth0.5) entered forwarding state : brctl addif br0 eth0.5 [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 1 : [3] [
rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ br0: port 3(eth0.5) entered disabled state rsl_getUnusedVlan ] 1153: GET
UNUSED VLAN TAG 3 : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_intf_enableIntf ] cmd: ifconfig eth0.3
up [ oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_intf_disableIntf ] cmd:
ifconfig eth0.6 down [ oal_br_delIntfFromBridge ] cmd: brctl delif br0 eth0.6 brctl: bridge br0: Invalid argument [ oal_intf_enableIntf ]
cmd: ifconfig eth0.6 up device eth0.6 entered promiscuous mode [ oal_br_addIntfbr0: port 4(eth0.6) entered forwarding state IntoBridge ]
cmdbr0: port 4(eth0.6) entered forwarding state : brctl addif br0 eth0.6 [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 1 : [3] [
rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 3br0: port
4(eth0.6) entered disabled state : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_intf_enableIntf ] cmd: ifconfig
eth0.3 up [ oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_intf_disableIntf ] cmd:
ifconfig eth0.6 down [ oal_eth_setIGMPSnoopParam ] cmd: sh /etc/igmp/igmp_snoop.sh 1 [ oal_eth_setIGMPSnoopParam ] cmd: echo
1 > /proc/tplink/eth_igmp_snooping [ oal_wlan_ra_setCountryRegion ] cmd: cp /etc/SingleSKU.dat
/var/Wireless/RT2860AP/SingleSKU.dat [ oal_wlan_ra_setCountryRegion ] cmd: iwpriv ra0 set CountryRegion=1 ra0 no private ioctls. [

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[illegible]

mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x22, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0x0(0), 0x13B4: 0x1b0f0400  
mt76x2\_single\_sku::sku\_base\_pwr = 0x20, DefaultTargetPwr = 0x22, ch\_pwr\_adj = 0xffffffe(-2), 0x13B4: 0x1b0f043e APStartUp(): AP  
Set CentralFreq at 42(Prim=36, HT-CentCh=38, VHT-CentCh=42, BBP\_BW=2) The 8-BSSID mode is enabled, the BSSID byte5 MUST  
be the multiple of 8 @@@@ ed\_monitor\_init : ==> @@@@ ed\_monitor\_init : <=== Main bssid = d8:0d:17:xx:xx:xx  
mt76x2\_reinit\_agc\_gain:original agc\_vga0 = 0x48, agc\_vga1 = 0x48 mt76x2\_reinit\_agc\_gain:updated agc\_vga0 = 0x48, agc\_vga1 = 0x48  
mt76x2\_reinit\_hi\_lna\_gain:original hi\_lna0 = 0x33, hi\_lna1 = 0x33 mt76x2\_reinit\_hi\_lna\_gain:updated hi\_lna0 = 0x33, hi\_lna1 = 0x33  
<===== rt28xx\_init, Status=0 get\_dev\_name\_prefix(): dev\_idx = 1, dev\_name\_prefix=rai get\_dev\_name\_prefix(): dev\_idx = 1,  
dev\_name\_prefix=rai get\_dev\_name\_prefix(): dev\_idx = 1, dev\_name\_prefix=rai get\_dev\_name\_prefix(): dev\_idx = 1,  
dev\_name\_prefix=rai get\_dev\_name\_prefix(): dev\_idx = 1, dev\_name\_prefix=apcli RTMPDrvOpen(1):Check if PDMA is idle!  
RTMPDrvOpen(2):Check if PDMA is idle! @@@@ ed\_monitor\_init : ==> @@@@ ed\_monitor\_init : <=== [ oal\_wlan\_controlWlanLed  
] cmd: echo "1" > /proc/tplink/led\_wlan\_5G [ oal\_wlanThe 8-BSSID mode is enabled, the BSSID byte5 MUST be the multiple of 8  
\_ra\_closeVap ] cmd: ifconfig rai0 down @@@@ APStop: go to ed\_monitor\_exit()!! @@@@ ed\_monitor\_exit : ==> @@@@  
ed\_monitor\_exit : <=== andes\_pci\_erasefw ==>rtl\_wlan\_chip\_onoff(): OnOff:0, Reset= 0, pAd->WlanFunCtrl:0x20b, Reg-  
WlanFunCtrl=0x20b RTMP\_TimerListRelease: release timer obj c0191338! RTMP\_TimerListRelease: release timer obj c0294fb4!  
RTMP\_TimerListRelease: release timer obj c021a4f0! RTMP\_TimerListRelease: release timer obj c021a4c4! RTMP\_TimerListRelease:  
release timer obj c021a51c! RTMP\_TimerListRelease: release timer obj c021a498! RTMP\_TimerListRelease: release timer obj c01953bc!  
RTMP\_TimerListRelease: release timer obj c0194fa0! RTMP\_TimerListRelease: release timer obj c019538c! RTMP\_TimerListRelease:  
release timer obj c01956c8! RTMP\_TimerListRelease: release timer obj c0195608! RTMP\_TimerListRelease: release timer obj c0195638!  
RTMP\_TimerListRelease: release timer obj c01986d0! RTMP\_TimerListRelease: release timer obj c01982b4! RTMP\_TimerListRelease:  
release timer obj c01986a0! RTMP\_TimerListRelease: release timer obj c01989dc! RTMP\_TimerListRelease: release timer obj c019891c!  
RTMP\_TimerListRelease: release timer obj c019894c! RTMP\_TimerListRelease: release timer obj c019b9e4! RTMP\_TimerListRelease:  
release timer obj c019b5c8! RTMP\_TimerListRelease: release timer obj c019b9b4! RTMP\_TimerListRelease: release timer obj c019bcf0!  
RTMP\_TimerListRelease: release timer obj c019bc30! RTMP\_TimerListRelease: release timer obj c019bc60! RTMP\_TimerListRelease:  
release timer obj c019ecf8! RTMP\_TimerListRelease: release timer obj c019e8dc! RTMP\_TimerListRelease: release timer obj c019ecc8!  
RTMP\_TimerListRelease: release timer obj c019f004! RTMP\_TimerListRelease: release timer obj c019ef44! RTMP\_TimerListRelease:  
release timer obj c019ef74! RTMP\_TimerListRelease: release timer obj c01a200c! RTMP\_TimerListRelease: release timer obj c01a1bf0!  
RTMP\_TimerListRelease: release timer obj c01a1fdc! RTMP\_TimerListRelease: release timer obj c01a2318! RTMP\_TimerListRelease:  
release timer obj c01a2258! RTMP\_TimerListRelease: release timer obj c01a2288! RTMP\_TimerListRelease: release timer obj c01a5320!  
RTMP\_TimerListRelease: release timer obj c01a4f04! RTMP\_TimerListRelease: release timer obj c01a52f0! RTMP\_TimerListRelease:  
release timer obj c01a562c! RTMP\_TimerListRelease: release timer obj c01a556c! RTMP\_TimerListRelease: release timer obj c01a559c!  
RTMP\_TimerListRelease: release timer obj c01a8634! RTMP\_TimerListRelease: release timer obj c01a8218! RTMP\_TimerListRelease:  
release timer obj c01a8604! RTMP\_TimerListRelease: release timer obj c01a8940! RTMP\_TimerListRelease: release timer obj c01a8880!  
RTMP\_TimerListRelease: release timer obj c01a88b0! RTMP\_TimerListRelease: release timer obj c01ab948! RTMP\_TimerListRelease:  
release timer obj c01ab52c! RTMP\_TimerListRelease: release timer obj c01ab918! RTMP\_TimerListRelease: release timer obj c01abc54!  
RTMP\_TimerListRelease: release timer obj c01abb94! RTMP\_TimerListRelease: release timer obj c01abbc4! RTMP\_TimerListRelease:  
release timer obj c021c974! RTMP\_TimerListRelease: release timer obj c021c558! RTMP\_TimerListRelease: release timer obj c021c944!  
RTMP\_TimerListRelease: release timer obj c021cc80! RTMP\_TimerListRelease: release timer obj c021c9a4! RTMP\_TimerListRelease:  
release timer obj c021c9d4! RTMP\_TimerListRelease: release timer obj c021ca04! RTMP\_TimerListRelease: release timer obj c0240504!  
RTMP\_TimerListRelease: release timer obj c0240620! RTMP\_TimerListRelease: release timer obj c0240530! RTMP\_TimerListRelease:  
release timer obj c021cff8! RTMP\_TimerListRelease: release timer obj c02405b4! RTMP\_TimerListRelease: release timer obj c019280c!  
RTMP\_TimerListRelease: release timer obj c0195b20! RTMP\_TimerListRelease: release timer obj c0198c34! RTMP\_TimerListRelease:  
release timer obj c019c148! RTMP\_TimerListRelease: release timer obj c019f45c! RTMP\_TimerListRelease: release timer obj c01a2770!  
RTMP\_TimerListRelease: release timer obj c01a5a84! RTMP\_TimerListRelease: release timer obj c01a8d98! RTMP\_TimerListRelease:

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release timer obj c021ccfc! RTMP_TimerListRelease: release timer obj c0195710! RTMP_TimerListRelease: release timer obj c019573c!
RTMP_TimerListRelease: release timer obj c0198a24! RTMP_TimerListRelease: release timer obj c0198a50! RTMP_TimerListRelease:
release timer obj c019bd38! RTMP_TimerListRelease: release timer obj c019bd64! RTMP_TimerListRelease: release timer obj c019f04c!
RTMP_TimerListRelease: release timer obj c019f078! RTMP_TimerListRelease: release timer obj c01a2360! RTMP_TimerListRelease:
release timer obj c01a238c! RTMP_TimerListRelease: release timer obj c01a5674! RTMP_TimerListRelease: release timer obj c01a56a0!
RTMP_TimerListRelease: release timer obj c01a8988! RTMP_TimerListRelease: release timer obj c01a89b4! RTMP_TimerListRelease:
release timer obj c01abc9c! RTMP_TimerListRelease: release timer obj c01abcc8! RTMP_TimerListRelease: release timer obj c0226390!
@@@ RTMPDrvClose: go to ed_monitor_exit()!! @@@ ed_monitor_exit : ==> @@@ ed_monitor_exit : <=== [
oal_wlan_ra_closeVap ] cmd: echo 0 > /proc/tplink/led_wlan_5G [ oal_wlan_ra_closeVap ] cmd: killall -q wscd_5G [
oal_wlan_ra_closeVap ] cmd: killall -q -SIGINT rtinacpd [ oal_br_addIntfIntoBridge ] cmd: brctl addif brdevice rai0 entered promiscuous
mode 0 rai0 [ oal_br_addIntfIntoBridge ] cmd: brctl addif brdevice aplii0 entered promiscuous mode 0 aplii0 [ oal_br_addIntfIntoBridge
] cmd: brctl addif brdevice rai2 entered promiscuous mode 0 rai2 [ oal_br_addIntfIntoBridge ] cmd: brctl addif brdevice rai3 entered
promiscuous mode 0 rai3 [ oal_br_addIntfIntoBridge ] cmd: brctl addif brdevice rai4 entered promiscuous mode 0 rai4 [
wlan_handleL2Desc ] 2401: br0 [ oal_br_addIntfIntoBridge ] cmd: brctl addif br0 rai1
wlNetlinkTool is already there. sendto: No such file or directory pid 786 send 2030 error [ oal_startUPnP ] cmd: upnpd -L br0 -W eth0.2 -
en 1 -nat 0 -port 80 -url "http://www.tp-link.com" -ma "TP-Link" -mn "Archer_C5" -mv "4.0" -desc "AC1200 Wireless Dual Band
Gigabit Router" & [ oal_ovpn_createDir ] cmd: mkdir /var/openvpn [ oal_ovpn_createDir ] cmd: chmod 777 /var/openvpn [
oal_ovpn_createDir ] cmd: mkdir /var/easy-rsa/ [ oal_ovpn_createDir ] cmd: chmod 777 /var/easy-rsa/ [ oal_ovpn_createDir ] cmd:
mkdir /var/easy-rsa/keys/ [ oal_ovpn_createDir ] cmd: chmod 700 /var/easy-rsa/keys/ [ oal_ovpn_setupTunMod ] cmd: insmod
/lib/modules/kmdir/kernel/drivers/net/tun.ko tun: Universal TUN/TAP device driver, 1.6 tun: (C) 1999-2004 Max Krasnyansky
<maxk@qualcomm.com> [ oal_pvpn_setupNeededMod ] cmd: insmod /lib/modules/kmdir/kernel/drivers/net/ppp_mppe.ko insmod:
can't insert '/lib/modules/kmdir/kernel/drivers/net/ppp_mppe.ko': No such file or directory [ oal_startDhcpd ] cmd: dhcpd
/var/tmp/dconf/udhcpd.conf [ oal_lan6_startDhcp6s ] cmd: dhcp6s -c /var/tmp/dconf/dhcp6s_br0.conf -P /var/run/dhcp6s_br0.pid
br0 & [ oal_lan6_startRadvd ] cmd: radvd -C /var/tmp/dconf/radvd_br0.conf -p /var/run/radvd_br0.pid & [ oal_snmp_updateCfg ]
cmd: iptables -t filter -D ACL -p udp --dport 161 -j ACCEPT iptables: Bad rule (does a matching rule exist in that chain?). iptables: Bad
rule (does a matching rule exist in that chain?). [ oal_startSnmp ] cmd: snmpd -f /var/tmp/dconf/snmpd.conf sh: can't create
/proc/sys/kernel/core_pattern: nonexistent directory [ oal_br_delIntfFromBridge ] cmd: brctl delif br0 eth0.2 radvd starting brctl: bridge
br0: Invalid argument [ oal_br_delIntfFromBridge ] cmd: brctl delif br0 eth0.2 brctl: bridge br0: Invalid argument [ rsl_getUnusedVlan ]
1153: GET UNUSED VLAN TAG 1 : [3] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ rsl_getUnusedVlan ] 1153:
GET UNUSED VLAN TAG 3 : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_intf_enableIntf ] cmd: ifconfig
eth0.3 up [ oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_intf_disableIntf ] cmd:
ifconfig eth0.6 down [ util_enFuncQueue ] 099: Function queue not support RDP_OPT_ADD_OBJ operation [ util_enFuncQueue ] 099:
Function queue not support RDP_OPT_ADD_OBJ operation set eth status, stack = 2 [ 1 1 0 0 0 ] [ rsl_getUnusedVlan ] 1153: GET
UNUSED VLAN TAG 1 : [3] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 2 : [4] [ rsl_getUnusedVlan ] 1153: GET
UNUSED VLAN TAG 3 : [5] [ rsl_getUnusedVlan ] 1153: GET UNUSED VLAN TAG 4 : [6] [ oal_intf_enableIntf ] cmd: ifconfig eth0.3
up [ oal_intf_disableIntf ] cmd: ifconfig eth0.4 down [ oal_intf_disableIntf ] cmd: ifconfig eth0.5 down [ oal_intf_disableIntf ] cmd:
ifconfig eth0.6 down [ oal_intf_setIfMac ] cmd: ifconfig eth0.2 down [ oal_intf_setIfMac ] cmd: ifconfig eth0.2 hw ether
D8:0D:17:xx:xx:xx up [ oal_intf_enableIntf ] cmd: ifconfig eth0.2 up [ oal_ipt_setDDoSRules ] cmd: iptables -F FIREWALL_DDOS [
ddos_clearAll ] cmd: rm -f /var/tmp/dosHost [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_ftp.ko [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/ipv4/netfilter/nf_nat_ftp.ko [ oal_openAlg ] cmd: iptables -D FORWARD_VPN_PASSTHROUGH -p
udp --dport 500 -j DROP iptables: Bad rule (does a matching rule exist in that chain?). [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_proto_gre.ko insmod: can't insert
'/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_proto_gre.ko': File exists [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/ipv4/netfilter/nf_nat_proto_gre.ko [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_pptp.ko insmod: can't insert
'/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_pptp.ko': File exists [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/ipv4/netfilter/nf_nat_pptp.ko [ oal_openAlg ] cmd: iptables -D FORWARD_VPN_PASSTHROUGH -
p tcp --dport 1723 -j DROP iptables: Bad rule (does a matching rule exist in that chain?). [ oal_openAlg ] cmd: iptables -D
FORWARD_VPN_PASSTHROUGH -p udp --dport 1701 -j DROP iptables: Bad rule (does a matching rule exist in that chain?). [
setupModules ] cmd: insmod /lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_tftp.ko [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/ipv4/netfilter/nf_nat_tftp.ko [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_h323.ko [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/ipv4/netfilter/nf_nat_h323.ko [ setupModules ] cmd: insmod
/lib/modules/kmdir/kernel/net/netfilter/nf_conntrack_sip.ko [ setupModules ] cmd: insmod /IRalink HW NAT Module Enabled
ib/modules/kmdir/kernel/net/ipv4/netfilter/nf_nat_sip.ko [ oal_wan_HWNat_enable ] cmd: insmod

```

```
/lib/modules/kmdir/kernel/net/nat/hw_nat.ko [ oal_initIp6FirewallObj ] cmd: ip6tables -F [ oal_initIp6FirewallObj ] cmd: ip6tables -X [ oal_initIp6FirewallObj ] cmd: ip6tables -P INPUT DROP [ oal_initIp6FirewallObj ] cmd: ip6tables -P FORWARD DROP [ oal_initIp6FirewallObj ] cmd: ip6tables -P OUTPUT ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -N ACLV6 [ oal_initIp6FirewallObj ] cmd: ip6tables -N FIREWALL [ oal_initIp6FirewallObj ] cmd: ip6tables -N FWRULE [ oal_initIp6FirewallObj ] cmd: ip6tables -N INPUT_FIREWALL [ oal_initIp6FirewallObj ] cmd: ip6tables -N INPUT_FWRULE [ oal_initIp6FirewallObj ] cmd: ip6tables -N PARENTCTL [ oal_initIp6FirewallObj ] cmd: ip6tables -N PCRULE [ oal_initIp6FirewallObj ] cmd: ip6tables -N PCDROP [ oal_initIp6FirewallObj ] cmd: ip6tables -N INPUT_PARENTCTL [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -i lo -p ALL -j ACCEPT -m comment --comment "loop back" [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -m conntrack --ctstate RELATED,ESTABLISHED -j ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -j ACLV6 [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -p icmpv6 ! --icmpv6-type echo-request -j ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -p udp ! -i br+ --sport 547 --dport 546 -j ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -p udp -i br+ --sport 546 --dport 547 -j ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -p udp --sport 521 --dport 521 -j ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -j INPUT_PARENTCTL [ oal_initIp6FirewallObj ] cmd: ip6tables -A INPUT -j INPUT_FIREWALL [ oal_initIp6FirewallObj ] cmd: ip6tables -A FORWARD -o br+ -m conntrack --ctstate RELATED,ESTABLISHED -j ACCEPT [ oal_initIp6FirewallObj ] cmd: ip6tables -A FORWARD -j PARENTCTL [ oal_initIp6FirewallObj ] cmd: ip6tables -A FORWARD -j FIREWALL [ oal_initIp6FirewallObj ] cmd: ip6tables -A FORWARD -i br+ -j ACCEPT [ oal_ip6_setLanHttpPort ] cmd: ip6tables -A INPUT -i br+ -p tcp --dport 80 -j ACCEPT [ oal_fw6_setFwEnabeld ] cmd: ip6tables -D FIREWALL -j ACCEPT ip6tables: Bad rule (does a matching rule exist in that chain?). [ oal_fw6_setFwEnabeld ] cmd: ip6tables -F FIREWALL [ oal_fw6_setSpiFirewall ] cmd: ip6tables -t filter -D FORWARD ! -i br+ -m conntrack --ctstate NEW -j ACCEPT ip6tables: Bad rule (does a matching rule exist in that chain?). [ oal_initFirewallObj ] cmd: ebtables -N FIREWALL [ oal_fw_setSpiFirewall ] cmd: iptables -t filter -D FORWARD ! -i br+ -m conntrack --ctstate NEW -j ACCEPT iptables: Bad rule (does a matching rule exist in that chain?). [ rsl_setStorageServiceObj ] 1394: mountFlag is 3,We start usb server [ oal_killDlnaMediaSeverProcess ] cmd: killall ushare killall: ushare: no process killed uShare (version 1.1a), a lightweight UPnP A/V and DLNA Media Server. Benjamin Zores (C) 2005-2007, for GeeXboX Team. See http://ushare.geexbox.org/ for updates. [ oal_ipt_setWanPort ] cmd: iptables -t filter -D INPUT -p tcp --dport 21 -j ACCEPT iptables: Bad rule (does a matching rule exist in that chain?). [ oal_ipt_setWanPort ] cmd: iptables -t nat -D PREROUTING -p tcp --dport 21 -j ACCEPT iptables: Bad rule (does a matching rule exist in that chain?). [ oal_ipt_setWanPort ] cmd: ip6tables -t filter -D ACLV6 -p tcp --dport 21 -j ACCEPT ip6tables: Bad rule (does a matching rule exist in that chain?). [ oal_ipt_setAcIrules ] cmd: iptables -t filter -A ACL -i br+ -p icmp -j ACCEPT [ oal_ipt_setAcIrules ] cmd: ip6tables -t filter -A ACLV6 -i br+ -p icmpv6 --icmpv6-type echo-request -j ACCEPT [ oal_sys_enablePowerLed ] cmd: echo 1 > /proc/tplink/led_sys [ cos_init ] cmd: echo 3 > /proc/sys/vm/drop_caches Found 0 files and subdirectories. open DNS error: No such file or directory [ oal_sys_unsetTZ ] cmd: echo "" > /etc/TZ [ oal_sys_unsetTZ ] cmd: echo "" > /etc/TZ ^@| SysRq : HELP : loglevel(0-9) reboot(b) crash(c) terminate-all-tasks(e) memory-full-oom-kill(f) kill-all-tasks(i) thaw-filesystems(j) show-memory-usage(m) nice-all-RT-tasks(n) poweroff(o) show-registers(p) show-all-timers(q) sync(s) show-task-states(t) unmount(u) show-blocked-tasks(w) dump-ftrace-buffer(z) | q | | | SysRq : HELP : loglevel(0-9) reboot(b) crash(c) terminate-all-tasks(e) memory-full-oom-kill(f) kill-all-tasks(i) thaw-filesystems(j) show-memory-usage(m) nice-all-RT-tasks(n) poweroff(o) show-registers(p) show-all-timers(q) sync(s) show-task-states(t) unmount(u) show-blocked-tasks(w) dump-ftrace-buffer(z) ^@| | | SysRq : HELP : loglevel(0-9) reboot(b) crash(c) terminate-all-tasks(e) memory-full-oom-kill(f) kill-all-tasks(i) thaw-filesystems(j) show-memory-usage(m) nice-all-RT-tasks(n) poweroff(o) show-registers(p) show-all-timers(q) sync(s) show-task-states(t) unmount(u) show-blocked-tasks(w) dump-ftrace-buffer(z) ^@| | | starting pid 787, tty ": '/sbin/getty -L ttyS1 115200 vt100' Archer C5 login:
```

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BusyBox v1.28.4 () built-in shell (ash) _____ | | .-----| | | .---| | _ - | _ | | | | | | _ | _ |
| _____ | | _ | _____ | _ | | _____ | _ | W I R E L E S S F R E E D O M -----
---- OpenWrt 18.06.4, r7808-ef686b7292 ----- root@ArcherC5v4:~# dmesg [ 0.000000] Linux
version 4.14.131 (ubuntu@ubuntu-VirtualBox) (gcc version 7.3.0 (OpenWrt GCC 7.3.0 r7808-ef686b7292)) #0 Thu Jun 27 12:18:52 2019 [
0.000000] Board has DDR2 [ 0.000000] Analog PMU set to hw control [ 0.000000] Digital PMU set to hw control [ 0.000000] SoC Type:
MediaTek MT7620A ver:2 eco:6 [ 0.000000] bootconsole [early0] enabled [ 0.000000] CPU0 revision is: 00019650 (MIPS 24KEc) [
0.000000] MIPS: machine is TP-Link Archer C5 v4 [ 0.000000] Determined physical RAM map: [ 0.000000] memory: 04000000 @
00000000 (usable) [ 0.000000] Initrd not found or empty - disabling initrd [ 0.000000] Primary instruction cache 64kB, VIPT, 4-way,
linesize 32 bytes. [ 0.000000] Primary data cache 32kB, 4-way, PIPT, no aliases, linesize 32 bytes [ 0.000000] Zone ranges: [ 0.000000]
Normal [mem 0x0000000000000000-0x0000000003ffffff] [ 0.000000] Movable zone start for each node [ 0.000000] Early memory node
ranges [ 0.000000] node 0: [mem 0x0000000000000000-0x0000000003ffffff] [ 0.000000] Initmem setup node 0 [mem
0x0000000000000000-0x0000000003ffffff] [ 0.000000] On node 0 totalpages: 16384 [ 0.000000] free_area_init_node: node 0, pgdat
80463690, node_mem_map 81000040 [ 0.000000] Normal zone: 128 pages used for memmap [ 0.000000] Normal zone: 0 pages reserved [

```

0.000000] Normal zone: 16384 pages, LIFO batch:3 [ 0.000000] random: get\_random\_bytes called from start\_kernel+0x90/0x478 with crng\_init=0 [ 0.000000] pcpu-alloc: s0 r0 d32768 u32768 alloc=1\*32768 [ 0.000000] pcpu-alloc: [0] 0 [ 0.000000] Built 1 zonelists, mobility grouping on. Total pages: 16256 [ 0.000000] Kernel command line: console=ttyS0,115200 rootfstype=squashfs,jffs2 [ 0.000000] PID hash table entries: 256 (order: -2, 1024 bytes) [ 0.000000] Dentry cache hash table entries: 8192 (order: 3, 32768 bytes) [ 0.000000] Inode-cache hash table entries: 4096 (order: 2, 16384 bytes) [ 0.000000] Writing ErrCtl register=00040001 [ 0.000000] Readback ErrCtl register=00040001 [ 0.000000] Memory: 59956K/65536K available (3475K kernel code, 181K rwdma, 840K rodata, 168K init, 212K bss, 5580K reserved, 0K cma-reserved) [ 0.000000] SLUB: HWalign=32, Order=0-3, MinObjects=0, CPUs=1, Nodes=1 [ 0.000000] NR\_IRQS: 256 [ 0.000000] CPU Clock: 580MHz [ 0.000000] clocksource: systick: mask: 0xffff max\_cycles: 0xffff, max\_idle\_ns: 583261500 ns [ 0.000000] systick: enable autosleep mode [ 0.000000] systick: running - mult: 214748, shift: 32 [ 0.000000] clocksource: MIPS: mask: 0xffffffff max\_cycles: 0xffffffff, max\_idle\_ns: 6590553264 ns [ 0.000010] sched\_clock: 32 bits at 290MHz, resolution 3ns, wraps every 7405115902ns [ 0.007591] Calibrating delay loop... 385.84 BogoMIPS (lpj=1929216) [ 0.073541] pid\_max: default: 32768 minimum: 301 [ 0.078241] Mount-cache hash table entries: 1024 (order: 0, 4096 bytes) [ 0.084618] Mountpoint-cache hash table entries: 1024 (order: 0, 4096 bytes) [ 0.097174] clocksource: jiffies: mask: 0xffffffff max\_cycles: 0xffffffff, max\_idle\_ns: 19112604462750000 ns [ 0.106708] futex hash table entries: 256 (order: -1, 3072 bytes) [ 0.112699] pinctrl core: initialized pinctrl subsystem [ 0.118245] NET: Registered protocol family 16 [ 0.384004] PCI host bridge /pcie@10140000 ranges: [ 0.388613] MEM 0x0000000002000000..0x000000002ffffff [ 0.393667] IO 0x0000000010160000..0x000000001016ffff [ 0.415732] rt2880\_gpio 10000600.gpio: registering 24 gpios [ 0.421184] rt2880\_gpio 10000600.gpio: registering 24 irq handlers [ 0.427487] rt2880\_gpio 10000660.gpio: registering 32 gpios [ 0.432903] rt2880\_gpio 10000660.gpio: registering 32 irq handlers [ 0.439619] PCI host bridge to bus 0000:00 [ 0.443588] pci\_bus 0000:00: root bus resource [mem 0x20000000-0x2ffffff] [ 0.450208] pci\_bus 0000:00: root bus resource [io 0xffffffff] [ 0.455964] pci\_bus 0000:00: root bus resource [??? 0x00000000 flags 0x0] [ 0.462550] pci\_bus 0000:00: No busn resource found for root bus, will use [bus 00-ff] [ 0.470290] pci 0000:00:00.0: [1814:0801] type 01 class 0x060400 [ 0.470331] pci 0000:00:00.0: reg 0x10: [mem 0x00000000-0x7ffffff] [ 0.470347] pci 0000:00:00.0: reg 0x14: [mem 0x20200000-0x2020ffff] [ 0.470420] pci 0000:00:00.0: supports D1 [ 0.470431] pci 0000:00:00.0: PME# supported from D0 D1 D3hot [ 0.470837] pci 0000:01:00.0: [14c3:7662] type 00 class 0x028000 [ 0.470894] pci 0000:01:00.0: reg 0x10: [mem 0x00000000-0x000ffff 64bit] [ 0.470948] pci 0000:01:00.0: reg 0x30: [mem 0x00000000-0x000ffff pref] [ 0.471045] pci 0000:01:00.0: PME# supported from D0 D3hot D3cold [ 0.471253] pci\_bus 0000:01: busn\_res: [bus 01-ff] end is updated to 01 [ 0.471272] pci\_bus 0000:00: busn\_res: [bus 00-ff] end is updated to 01 [ 0.471319] pci 0000:00:00.0: BAR 0: no space for [mem size 0x80000000] [ 0.477680] pci 0000:00:00.0: BAR 0: failed to assign [mem size 0x80000000] [ 0.484490] pci 0000:00:00.0: BAR 8: assigned [mem 0x20000000-0x200ffff] [ 0.491038] pci 0000:00:00.0: BAR 9: assigned [mem 0x20100000-0x201ffff pref] [ 0.498061] pci 0000:00:00.0: BAR 1: assigned [mem 0x20200000-0x2020ffff] [ 0.504653] pci 0000:01:00.0: BAR 0: assigned [mem 0x20000000-0x200ffff 64bit] [ 0.511769] pci 0000:01:00.0: BAR 6: assigned [mem 0x20100000-0x2010ffff pref] [ 0.518759] pci 0000:00:00.0: PCI bridge to [bus 01] [ 0.523589] pci 0000:00:00.0: bridge window [mem 0x20000000-0x200ffff] [ 0.530171] pci 0000:00:00.0: bridge window [mem 0x20100000-0x201ffff pref] [ 0.541228] clocksource: Switched to clocksource systick [ 0.547512] NET: Registered protocol family 2 [ 0.552535] TCP established hash table entries: 1024 (order: 0, 4096 bytes) [ 0.559257] TCP bind hash table entries: 1024 (order: 0, 4096 bytes) [ 0.565481] TCP: Hash tables configured (established 1024 bind 1024) [ 0.571764] UDP hash table entries: 256 (order: 0, 4096 bytes) [ 0.577391] UDP-Lite hash table entries: 256 (order: 0, 4096 bytes) [ 0.583768] NET: Registered protocol family 1 [ 0.587992] PCI: CLS 0 bytes, default 32 [ 0.590640] rt-timer 10000100.timer: maximum frequency is 1220Hz [ 0.597325] Crashlog allocated RAM at address 0x3f00000 [ 0.604074] workingset: timestamp\_bits=30 max\_order=14 bucket\_order=0 [ 0.615568] squashfs: version 4.0 (2009/01/31) Phillip Lougher [ 0.621168] jffs2: version 2.2 (NAND) (SUMMARY) (LZMA) (RTIME) (CMODE\_PRIORITY) (c) 2001-2006 Red Hat, Inc. [ 0.640482] io scheduler noop registered [ 0.644285] io scheduler deadline registered (default) [ 0.650080] Serial: 8250/16550 driver, 2 ports, IRQ sharing disabled [ 0.657300] console [ttyS0] disabled [ 0.660750] 10000c00.uartlite: ttyS0 at MMIO 0x10000c00 (irq = 20, base\_baud = 2500000) is a Palmchip BK-3103 [ 0.670424] console [ttyS0] enabled [ 0.677450] bootconsole [early0] disabled [ 0.686110] cacheinfo: Failed to find cpu0 device node [ 0.691388] cacheinfo: Unable to detect cache hierarchy for CPU 0 [ 0.702218] spi spi0.0: force spi mode3 [ 0.708602] m25p80 spi0.0: gd25q64 (8192 Kbytes) [ 0.713425] 6 fixed-partitions partitions found on MTD device spi0.0 [ 0.719903] Creating 6 MTD partitions on "spi0.0": [ 0.724809] 0x000000000000-0x000000020000 : "u-boot" [ 0.730728] 0x000000020000-0x00000007c0000 : "firmware" [ 1.166519] 2 tplink-fw partitions found on MTD device firmware [ 1.172603] 0x000000020000-0x000000018df67 : "kernel" [ 1.178543] 0x000000018df68-0x00000007c0000 : "rootfs" [ 1.184410] mtd: device 3 (rootfs) set to be root filesystem [ 1.191710] 1 squashfs-split partitions found on MTD device rootfs [ 1.198037] 0x00000003e8000-0x00000007c0000 : "rootfs\_data" [ 1.204449] 0x00000007c0000-0x00000007d0000 : "config" [ 1.210275] 0x00000007d0000-0x00000007e0000 : "rom" [ 1.215928] 0x00000007e0000-0x00000007f0000 : "romfile" [ 1.221944] 0x00000007f0000-0x0000000800000 : "radio" [ 1.228350] libphy: Fixed MDIO Bus: probed [ 1.240124] gsw: setting port4 to ephy mode [ 1.244767] libphy: mdio: probed [ 1.251965] mtk\_soc\_eth 10100000.ethernet: using fixed link parameters [ 1.260198] mtk\_soc\_eth 10100000.ethernet: loaded mt7620 driver [ 1.266910] mtk\_soc\_eth 10100000.ethernet eth0: mediatek frame engine at 0xb0100000, irq 5 [ 1.275875] rt2880\_wdt 10000120.watchdog: Initialized [ 1.282301] NET: Registered protocol family 10 [ 1.291658] Segment Routing with IPv6 [ 1.295500] NET: Registered protocol family 17 [ 1.300080] 8021q: 802.1Q VLAN Support v1.8 [ 2.497182] rtl8367s\_swconfig\_init [ 2.509239] VFS: Mounted root (squashfs filesystem) readonly on device 31:3. [ 2.516503] random: fast init done [ 2.520890] Freeing unused kernel memory: 168K [ 2.525458] This architecture does not have kernel memory protection. [ 3.386287] init: Console is alive [ 3.389992] init: - watchdog - [ 4.307366] kmodloader: loading kernel modules from

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/etc/modules-boot.d/* [ 4.477717] usbcore: registered new interface driver usbfs [ 4.483476] usbcore: registered new interface driver hub [
4.488997] usbcore: registered new device driver usb [ 4.499948] ehci_hcd: USB 2.0 'Enhanced' Host Controller (EHCI) Driver [ 4.508403]
ehci-platform: EHCI generic platform driver [ 4.524131] phy phy-usbphy.0: remote usb device wakeup disabled [ 4.530176] phy phy-
usbphy.0: UTMI 16bit 30MHz [ 4.534733] ehci-platform 101c0000.ehci: EHCI Host Controller [ 4.540649] ehci-platform 101c0000.ehci:
new USB bus registered, assigned bus number 1 [ 4.548859] ehci-platform 101c0000.ehci: irq 26, io mem 0x101c0000 [ 4.564158] ehci-
platform 101c0000.ehci: USB 2.0 started, EHCI 1.00 [ 4.571540] hub 1-0:1.0: USB hub found [ 4.575815] hub 1-0:1.0: 1 port detected [
4.583062] ohci_hcd: USB 1.1 'Open' Host Controller (OHCI) Driver [ 4.591039] ohci-platform: OHCI generic platform driver [ 4.596711]
ohci-platform 101c1000.ohci: Generic Platform OHCI controller [ 4.603666] ohci-platform 101c1000.ohci: new USB bus registered,
assigned bus number 2 [ 4.611862] ohci-platform 101c1000.ohci: irq 26, io mem 0x101c1000 [ 4.645674] hub 2-0:1.0: USB hub found [
4.649891] hub 2-0:1.0: 1 port detected [ 4.659488] kmloader: done loading kernel modules from /etc/modules-boot.d/* [ 4.671046] init:
- preinit - [ 5.781920] mtk_soc_eth 10100000.ethernet eth0: port 5 link up (1000Mbps/Full duplex) [ 5.790611] IPv6:
ADDRCONF(NETDEV_UP): eth0: link is not ready [ 5.796613] 8021q: adding VLAN 0 to HW filter on device eth0 [ 5.802655] IPv6:
ADDRCONF(NETDEV_CHANGE): eth0: link becomes ready [ 7.033183] jffs2_scan_eraseblock(): End of filesystem marker found at
0x3000 [ 7.040816] jffs2_build_filesystem(): unlocking the mtd device... [ 7.040855] done. [ 7.049139] jffs2_build_filesystem(): erasing all
blocks after the end marker... [ 47.032285] done. [ 47.041961] jffs2: notice: (393) jffs2_build_xattr_subsystem: complete building xattr
subsystem, 0 of xdatum (0 unchecked, 0 orphan) and 0 of xref (0 dead, 0 orphan) found. [ 47.059213] mount_root: overlay filesystem has
not been fully initialized yet [ 47.073017] mount_root: switching to jffs2 overlay [ 47.107189] overlayfs: upper fs does not support tmpfile. [
47.461772] urandom-seed: Seed file not found (/etc/urandom.seed) [ 47.574527] mtk_soc_eth 10100000.ethernet eth0: port 5 link down [
47.591426] procd: - early - [ 47.595262] procd: - watchdog - [ 48.102469] procd: - watchdog - [ 48.105963] procd: - ubus - [ 48.307506]
random: ubusd: uninitialized urandom read (4 bytes read) [ 48.315172] random: ubusd: uninitialized urandom read (4 bytes read) [
48.322235] random: ubusd: uninitialized urandom read (4 bytes read) [ 48.329752] procd: - init - [ 48.780009] kmloader: loading kernel
modules from /etc/modules.d/* [ 48.793115] ip6_tables: (C) 2000-2006 Netfilter Core Team [ 48.813061] Loading modules backported
from Linux version wt-2017-11-01-0-gfe248fc2c180 [ 48.821303] Backport generated by backports.git v4.14-rc2-1-31-g86cf0e5d [
48.832584] ip_tables: (C) 2000-2006 Netfilter Core Team [ 48.847339] nf_conntrack version 0.5.0 (1024 buckets, 4096 max) [ 48.917806]
xt_time: kernel timezone is -0000 [ 48.991876] mt76x2e 0000:01:00.0: card - bus=0x1, slot = 0x0 irq=4 [ 48.998455] mt76x2e 0000:01:00.0:
ASIC revision: 76120044 [ 49.199733] mt76x2e 0000:01:00.0: ROM patch build: 20141115060606a [ 49.211268] mt76x2e 0000:01:00.0:
Firmware Version: 0.0.00 [ 49.216876] mt76x2e 0000:01:00.0: Build: 1 [ 49.221104] mt76x2e 0000:01:00.0: Build Time: 201507311614____
[ 49.236125] mt76x2e 0000:01:00.0: Firmware running! [ 49.242164] ieee80211 phy0: Selected rate control algorithm 'minstrel_ht' [
49.269108] PPP generic driver version 2.4.2 [ 49.277276] NET: Registered protocol family 24 [ 49.382690] rt2800_wmac 10180000.wmac:
loaded eeprom from mtd device "radio" [ 49.389974] ieee80211 phy1: rt2x00_set_rt: Info - RT chipset 6352, rev 0500 detected [
49.398036] ieee80211 phy1: rt2x00_set_rf: Info - RF chipset 7620 detected [ 49.405700] ieee80211 phy1: Selected rate control algorithm
'minstrel_ht' [ 49.411210] kmloader: done loading kernel modules from /etc/modules.d/* [ 50.131682] urandom_read: 5 callbacks
suppressed [ 50.131692] random: jshn: uninitialized urandom read (4 bytes read) [ 50.324423] random: jshn: uninitialized urandom read (4
bytes read) [ 62.087681] mtk_soc_eth 10100000.ethernet eth0: port 5 link up (1000Mbps/Full duplex) [ 62.096405] 8021q: adding VLAN 0
to HW filter on device eth0 [ 62.133370] br-lan: port 1(eth0.1) entered blocking state [ 62.138962] br-lan: port 1(eth0.1) entered disabled
state [ 62.144809] device eth0.1 entered promiscuous mode [ 62.149759] device eth0 entered promiscuous mode [ 62.238757] br-lan: port
1(eth0.1) entered blocking state [ 62.244283] br-lan: port 1(eth0.1) entered forwarding state [ 62.250251] IPv6:
ADDRCONF(NETDEV_UP): br-lan: link is not ready [ 63.191137] IPv6: ADDRCONF(NETDEV_CHANGE): br-lan: link becomes
ready [ 87.447316] random: crng init done

```

## Credits and forum thread

The following forum threads have been used to discuss the Archer C5 v4:

- <https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889> [https://forum.openwrt.org/t/support-for-new-archer-c5-v4/15889]

Main contributors

- Serge (providing a modification to the RTL8367 switch code to support the RTL8367S chip)
- Qingfang and Luizluca (creating/maintaining a patch from Serge's source code to provide official support for the device)
- LGA1150 (his patch [https://github.com/LGA1150/openwrt/commit/4765ce0ba41deb5893e62f3344581251132c96b6] adds OpenWrt support for the device)
- gaspare
- leks
- ccc

- [catalinii](#)
- [blue](#)
- [Balazs](#) (providing help with the official kernel module packages compatibility issue during compilation)
- [benwh](#) (preparing this docs, image and a [git \[https://github.com/benwh/openwrt/tree/ArcherC5v4\]](https://github.com/benwh/openwrt/tree/ArcherC5v4) for easy image building and update

Thanks to everyone who contributed to make this possible.

## Tags

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