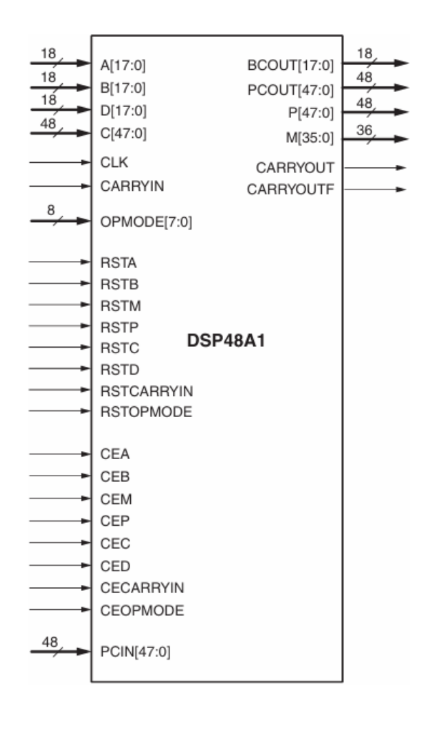
**Spartan6 - DSP48A1**

By: **Shahd Gamal Mahmoud**

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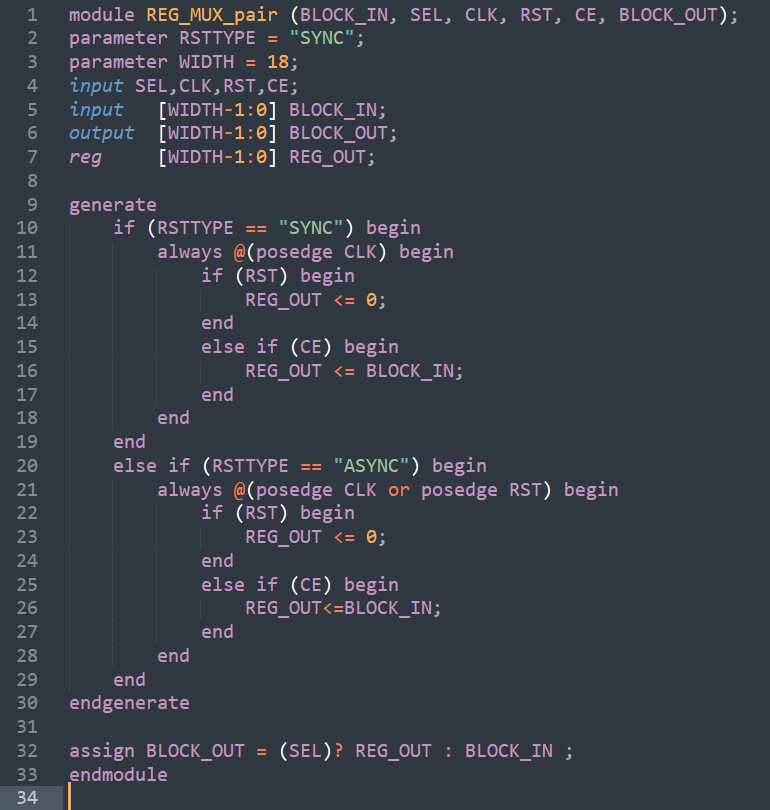
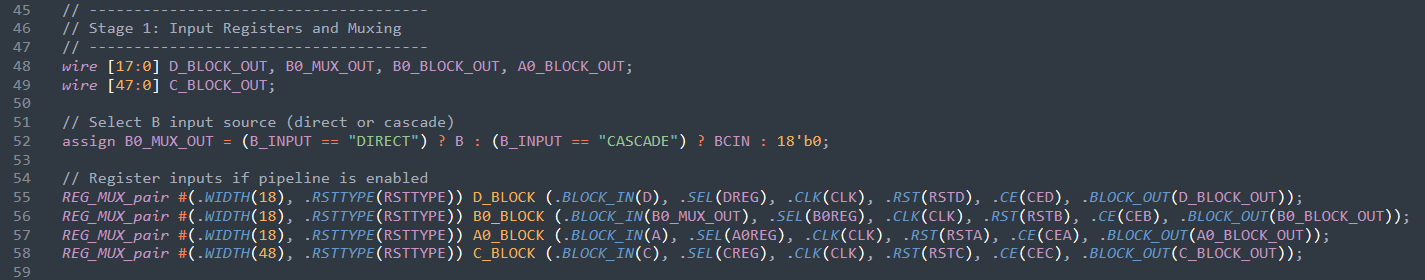
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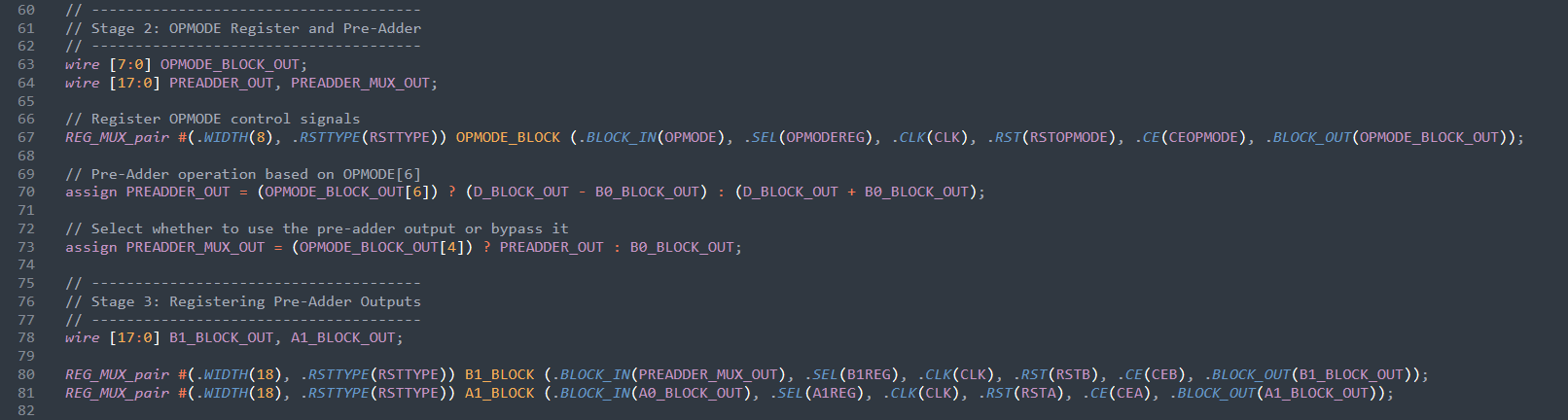
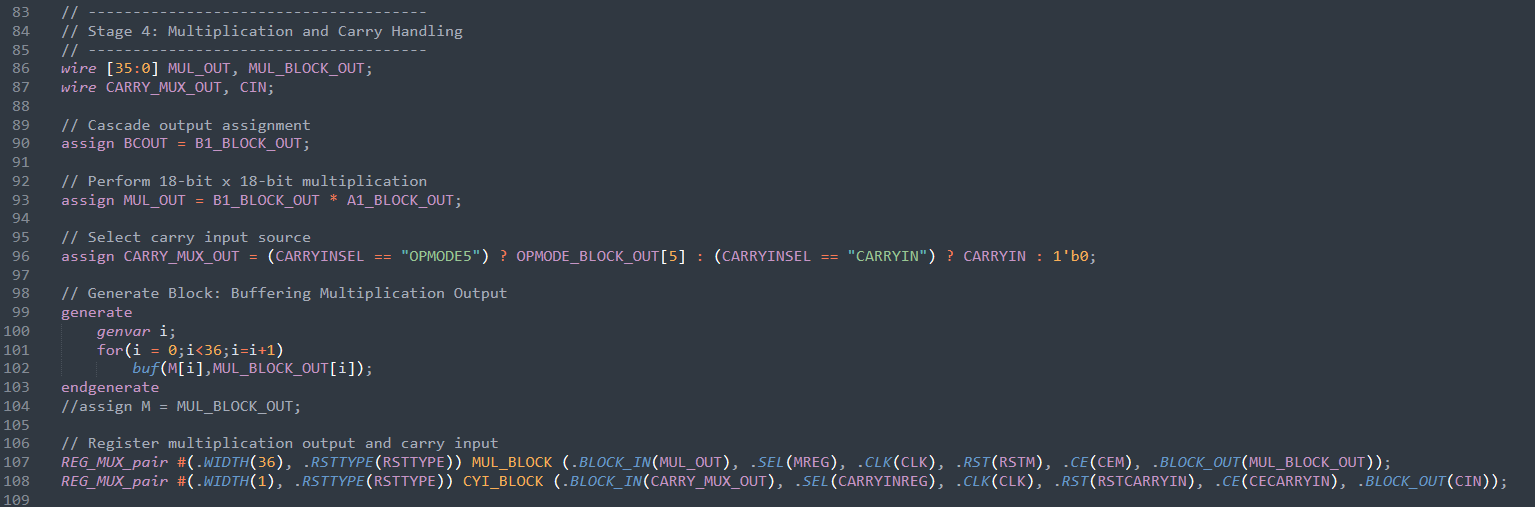
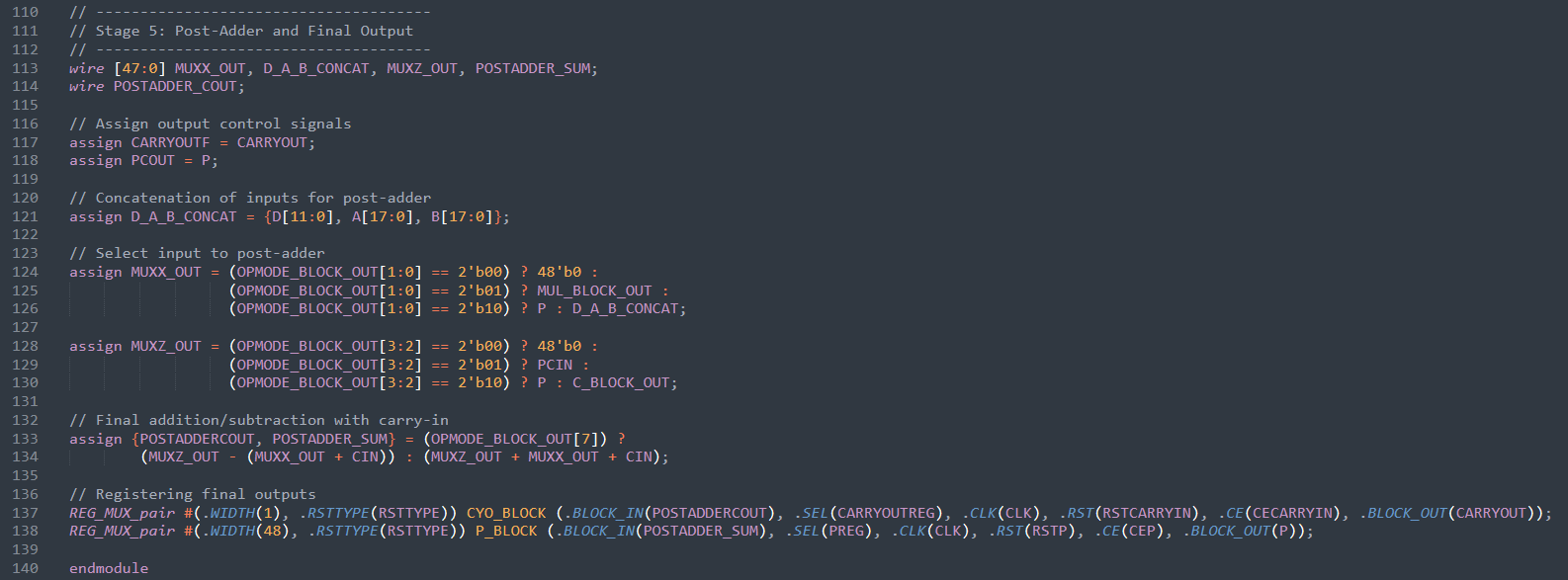
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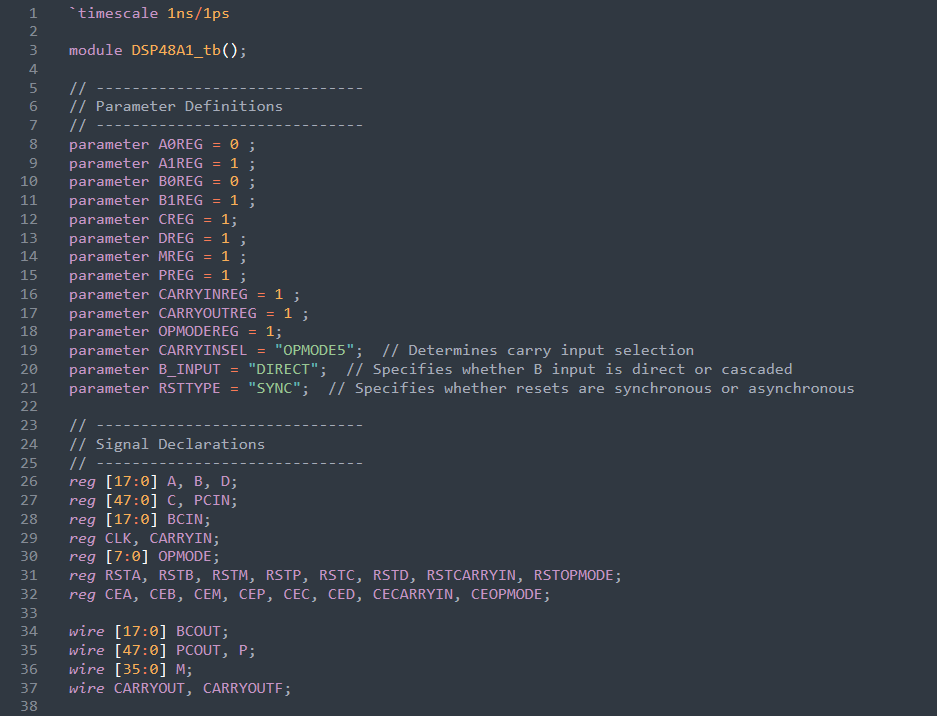
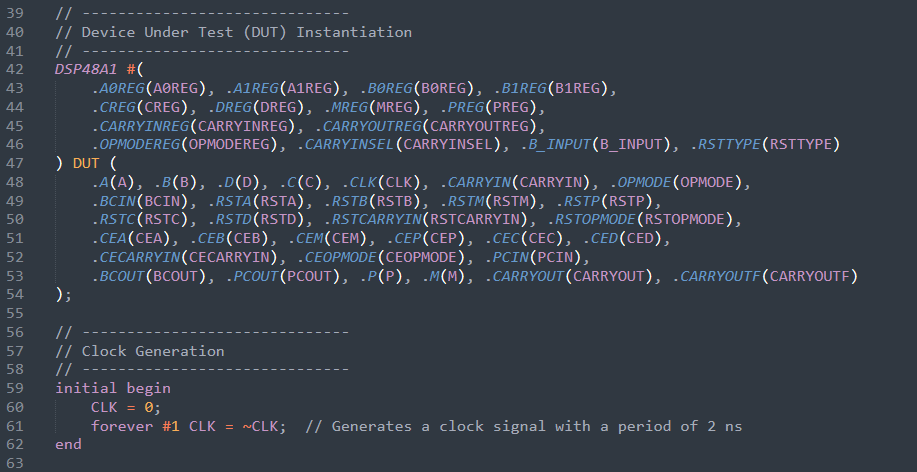
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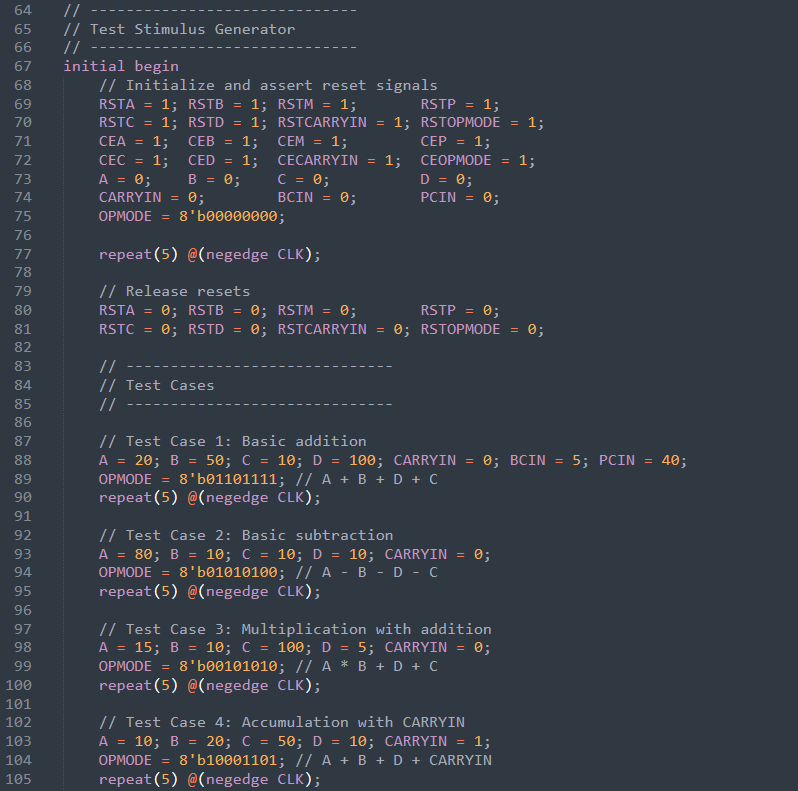
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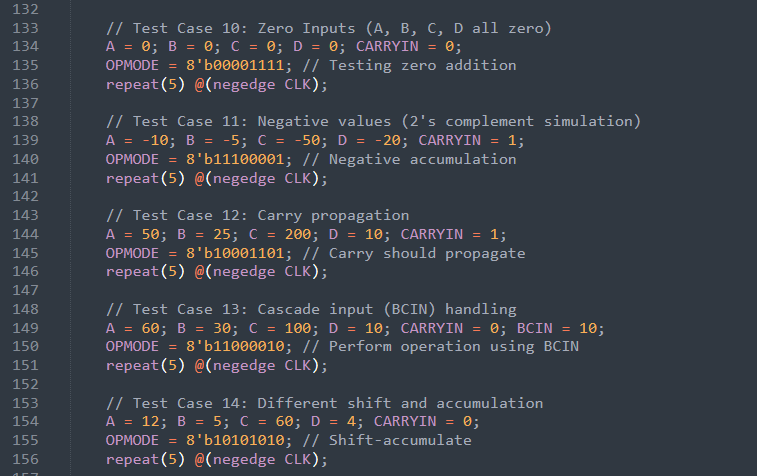
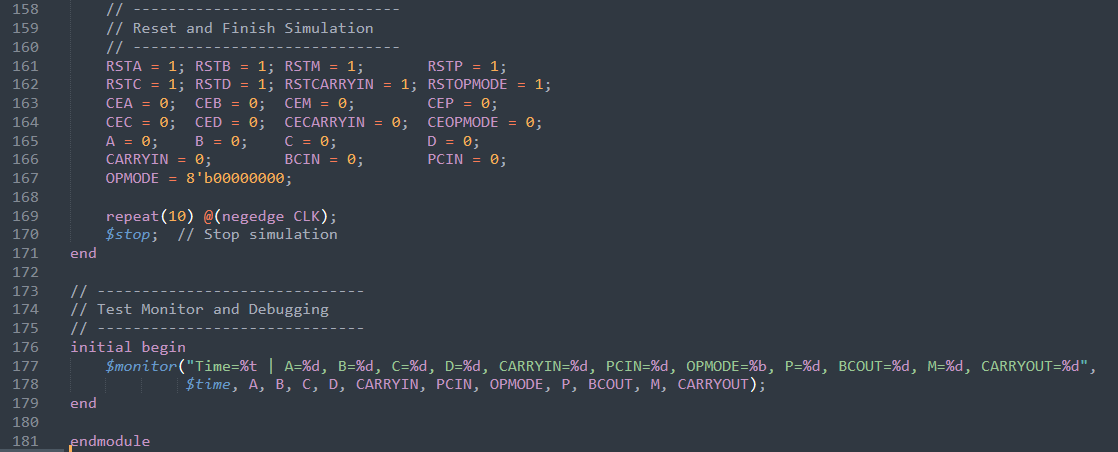
1. **RTL Code**

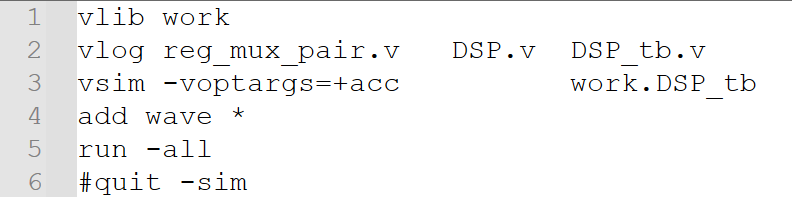
* BLOCK (REG + MUX)
* Design DSP

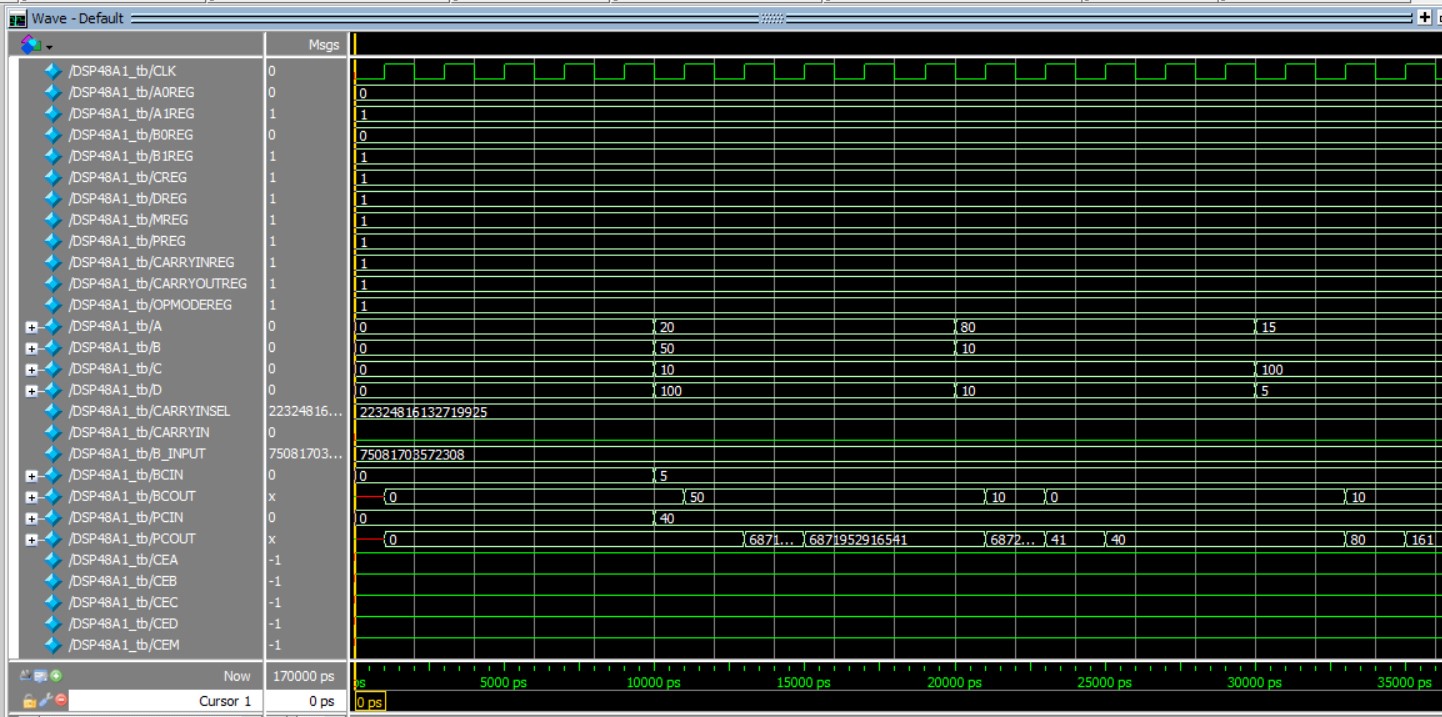
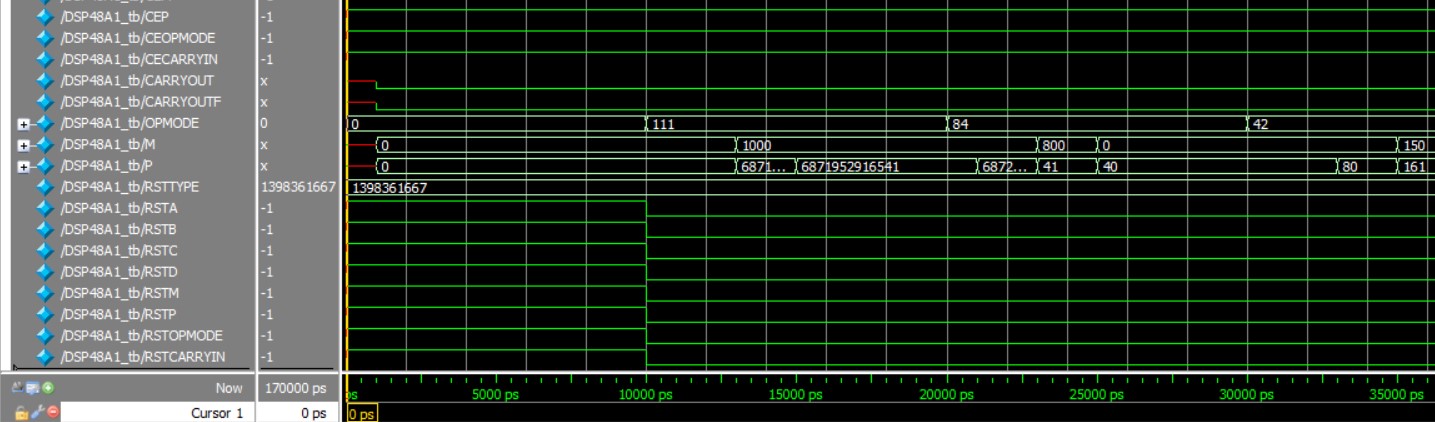
****

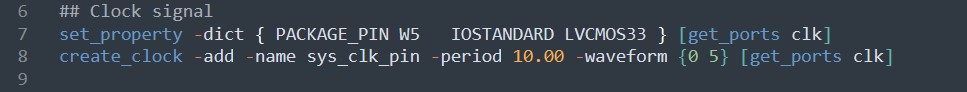
1. **Testbench Code**

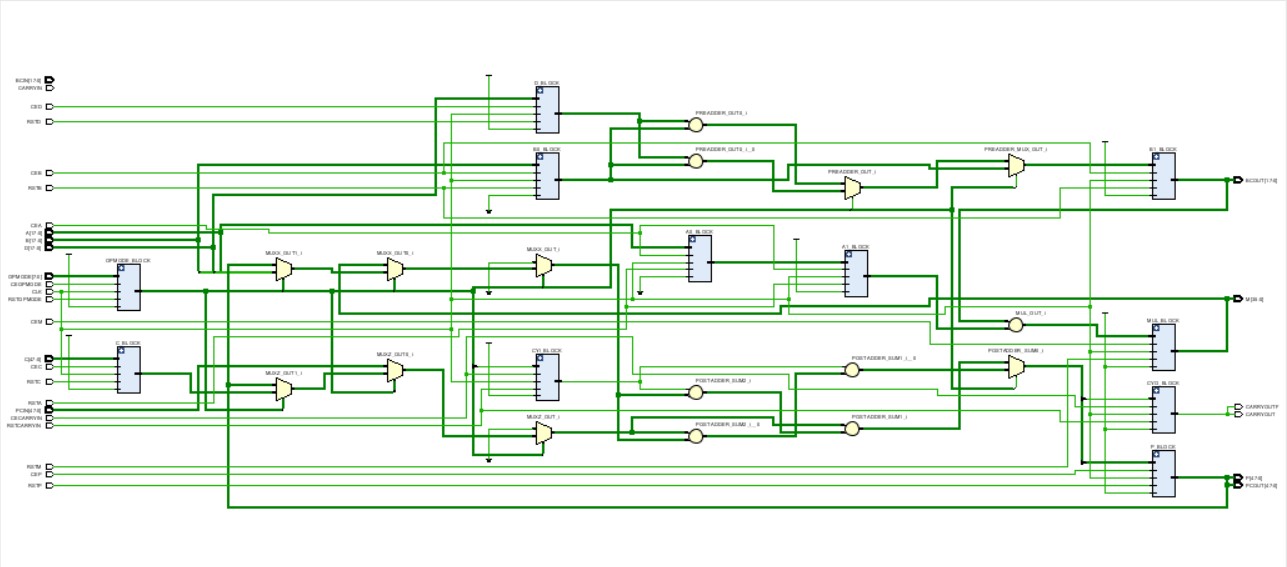
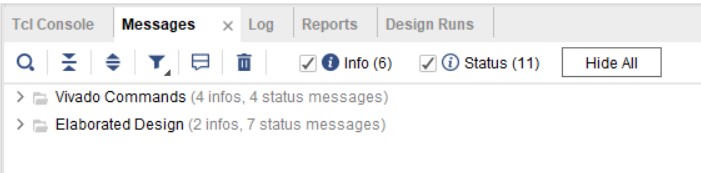


****

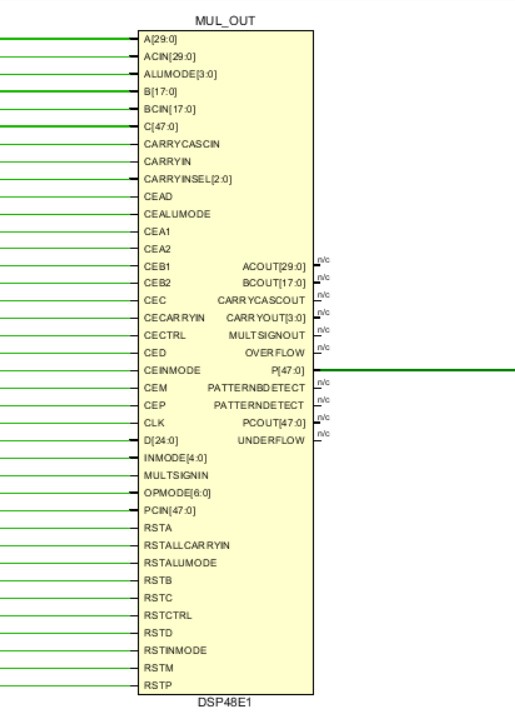
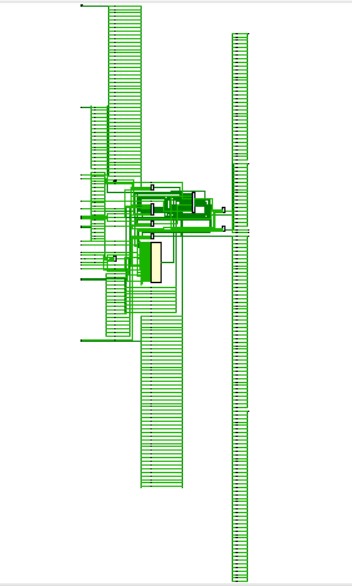
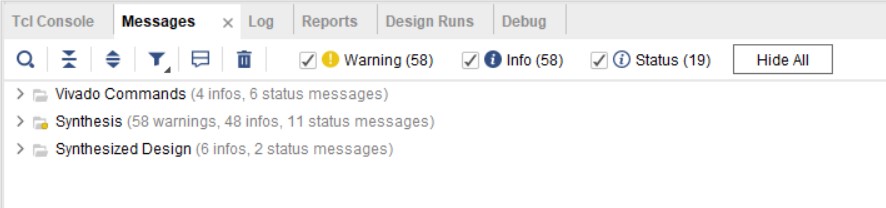
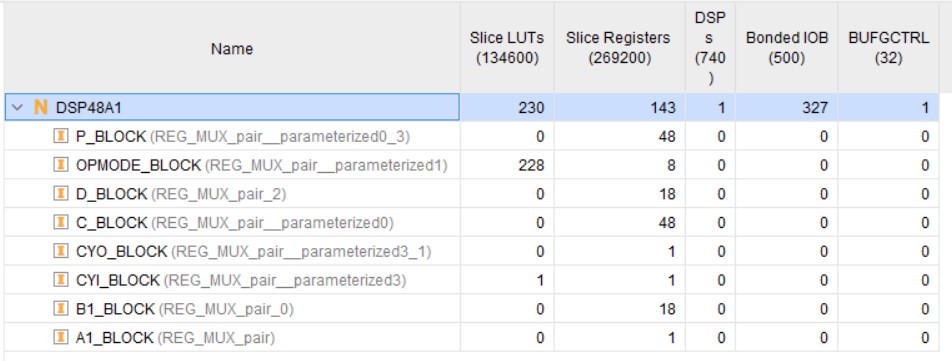
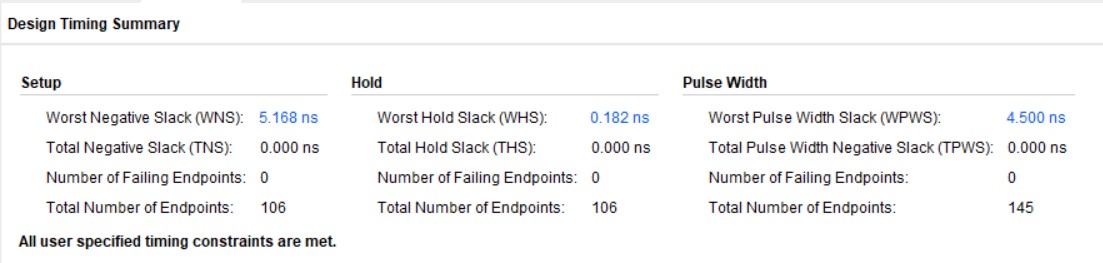
1. **Do File**
2. **QuestaSim Snippets**

* Waves

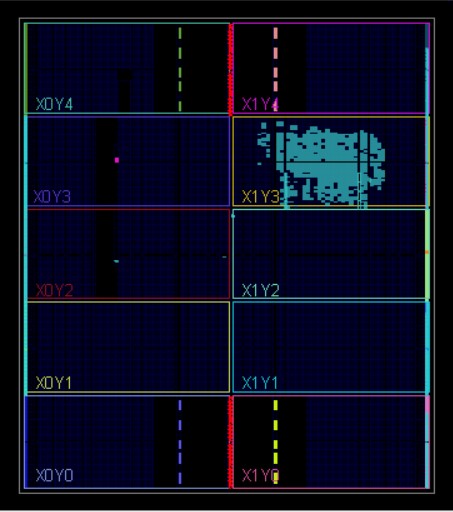
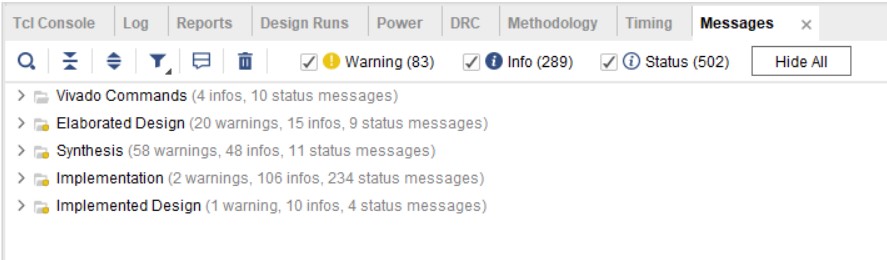
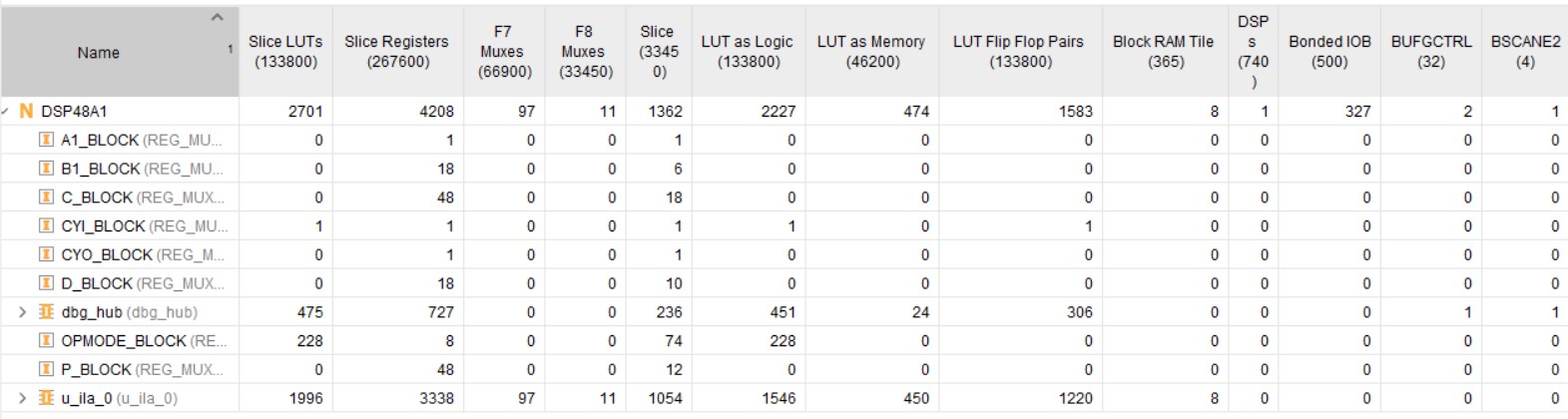
1. **Constraint File**
2. **Elaboration**

* Schematic Snippets
* Messages (No Errors or Warnings)

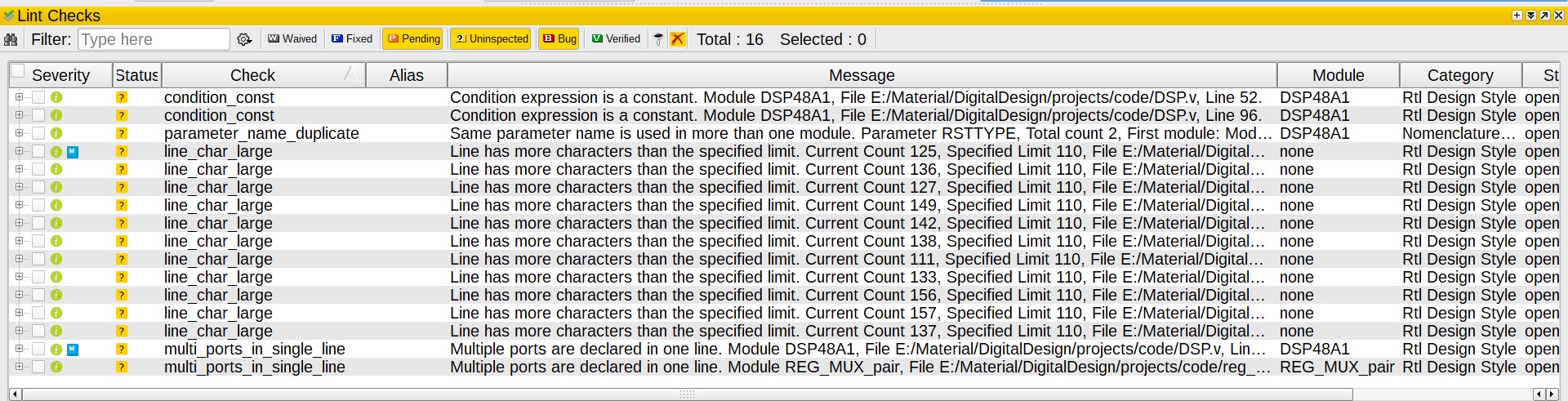
1. **Synthesis**

* Schematic Snippets
* Messages (No Errors)
* Utilization report
* Time report

1. **Implementation**

* Device Snippets
* Messages (No Errors)
* Utilization report
* Time report

1. **Linting**

* Lint Checks (No Errors or Warnings)