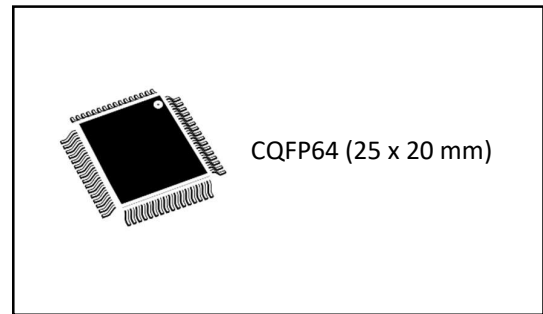# NTINY-E

RISCV -IMF 32b MCU+FPU, 1.56/1.95 Legal/Best-effort DMIPS, 3.28 CoreMark, 32KB Instruction RAM, 8KB Data RAM, 2 communication interfaces and industry standard PWM

Features

- Core: NTINY 32-bit Embedded CPU with FPU, 5-Stage pipeline, 0-wait state execution from RAM, 1.56 DMIPS/MHz, and 3.28 CoreMark, frequency up-to 50MHz
- Memory
  - 32 Kbyte SRAM Instruction Memory
  - 8 Kbyte SRAM Data Memory
- Clock, reset and supply
  - 1V Core Supply
  - 2.5V I/O Supply
  - Off-chip Configurable PLL
  - Asynchronous active high reset
- Timer
  - Single 32bit timer
  - Timer pre-scalar register
  - Output compare register
  - Interrupt
- GPIO
  - 16 GPIOs
  - Pull-up enabled
  - Interrupt
- UART
  - Single UART
  - Configurable baud rate
  - Interrupt

CQFP64 (25 x 20 mm)

- SPI Master with interrupt
- I2C Master with interrupt
- PWM
  - High side and low side operation
  - 16bit
  - Configurable frequency
- Debug
  - Standard JTAG Support
  - ICSP
  - Stepping, halt and resume options