

sign - magnitude

drawback: → 2 representation for 0

→ extrabit to represent sign if 2 no. are added.

larges

$$\text{sign-mag. } \left\{ \begin{array}{l} \phi \\ \end{array} \right\} = -2^{n-1} + 1 \text{ to } 2^{n-1} - 1$$

$\Rightarrow$  convert to 2's  
 write iff the sign-m  
 form, if -ve take  
 $2^k$  wrap, then MSB = 1

$$2^n \text{ comp} = -2^{n-1} \text{ to } 2^{n-1} - 1 \quad (\text{MSB} = 1 \Rightarrow \text{no is -ve})$$

○ ○ ○ ○ ○

0 0 0 1 - 1

DRAFT

~~3311~~

0 0 1 1 3

0 1 0 0 4

0 1 0 1 S

0 1 1 0 6

卷之三

5 111

$$\left\{ \begin{array}{r}
 -3 \\
 | \quad 0 \quad 0 \quad 0 \quad 0 \quad 0 = 8 \Rightarrow \\
 | \quad 0 \quad 0 \quad 0 \quad 1 \quad -7 \Rightarrow -8 + 1 \\
 | \quad 0 \quad 1 \quad 0 \quad -6 \quad -8 + 2 \\
 | \quad 0 \quad 1 \quad 1 \quad -5 \\
 | \quad 1 \quad 1 \quad 0 \quad 0 \quad -4 \\
 | \quad 1 \quad 1 \quad 0 \quad 1 \quad -3 \\
 | \quad 1 \quad 1 \quad 1 \quad 0 \quad -2 \\
 | \quad 1 \quad 1 \quad 1 \quad 1 \quad -1
 \end{array} \right.$$

sign extension : ~~most significant bit same as sign bit~~

Overflow

$$\begin{array}{r} 6+ \\ 7 \\ \hline \end{array} \Rightarrow \begin{array}{r} 0110+ \\ 0111 \\ \hline 1101 \end{array}$$

-ve not true  $\Rightarrow$  overflow.

$$6-7 \Rightarrow 0110$$

$$\begin{array}{r} 1001 \\ -1111 \\ \hline \end{array}$$

$$-6-7 \Rightarrow$$

$$\begin{array}{r} 1010+ \\ 1001 \\ \hline 0011 \end{array}$$

+ve  $\Rightarrow$  wrong

## Multiplication (Shift & add)

$32 \times 32$

Multiplicand (64 bit)  $\rightarrow$  shift-left & add if 1  
else shift-left

Multiplexor (62 bit)  $\rightarrow$  shift-right

product (64)  $\rightarrow 32 + 32 = 64$

ALU (64)

eg:  $2 \times 3$

| Iteration | Mr.          | Mc.      | product   |
|-----------|--------------|----------|-----------|
| 0         | 0011         | 00000010 | 00000000  |
| 1         | $P = P + Mc$ | 0001     | 000000100 |
|           | shl Mr.      |          |           |
| 2         | $P = P + Mc$ | 0000     | 000001000 |
|           | shl Mr.      |          |           |

3<sup>rd</sup>  
SLL MC 0000 00010000  
SRL MR

4<sup>th</sup>  
SLL MC  
SRL MR

Total iteration = 32.

max no. of steps =  $32 \times 3 = 96$ .

② To reduce size

instead of multiplicand left shift we  
do product right shift

e.g.:  $82 \times 32$  bit  $\rightarrow$  multiplicand = 32  
multiplicand = 32  
product = 64  
ALU = 32

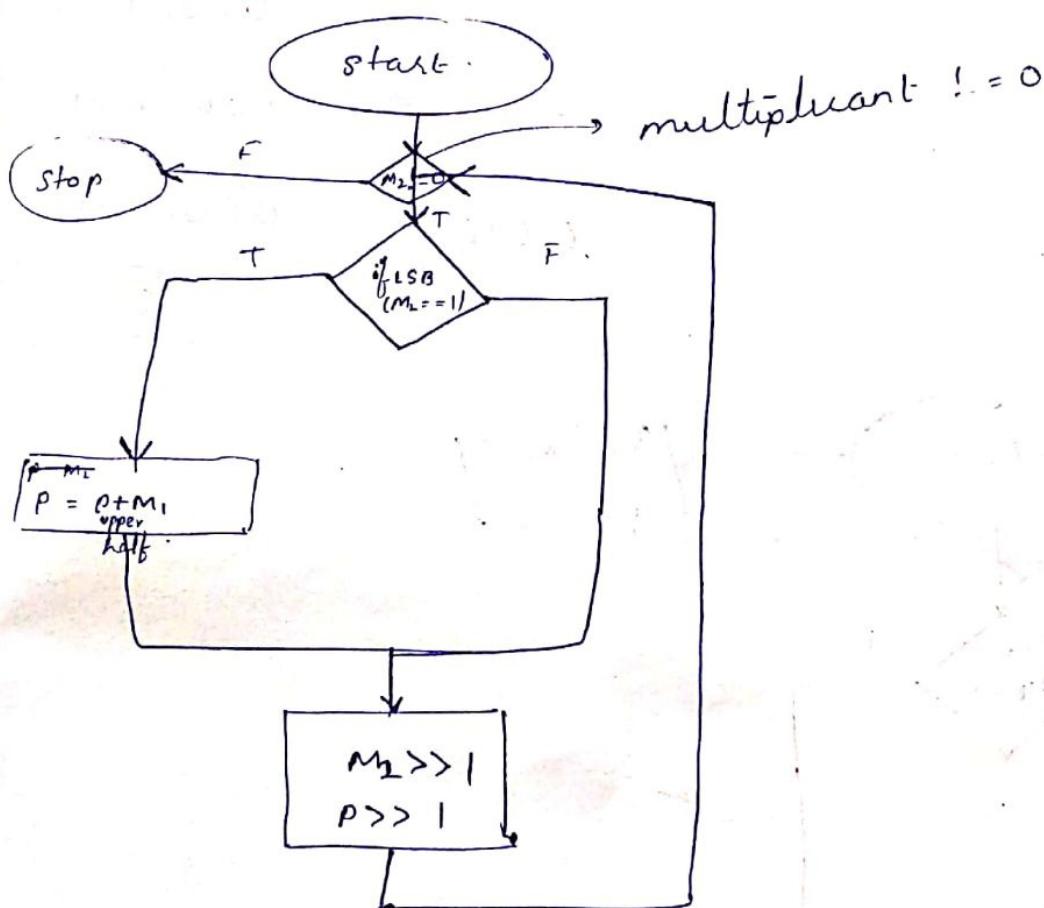
Ex: Iterations step multiplier multiplicand product  
0 initial 0011 0010 0000 0000  
1  $P+M_1$  0001 0010  $\rightarrow$  0001 0000  
 $M_2 >> 1$   
 $P >> 1$   
2.  $(P+M_1)$  higher order bit 0000 0010 00110000  
 $M_2 >> 1$   
 $P >> 1$

3.  $M_L \gg 1$       0000      0010      0000 1100  
 $P \gg 1$

4)  $M_L \gg 1$       0000      0010      0000 0110  
 $P \gg 1$

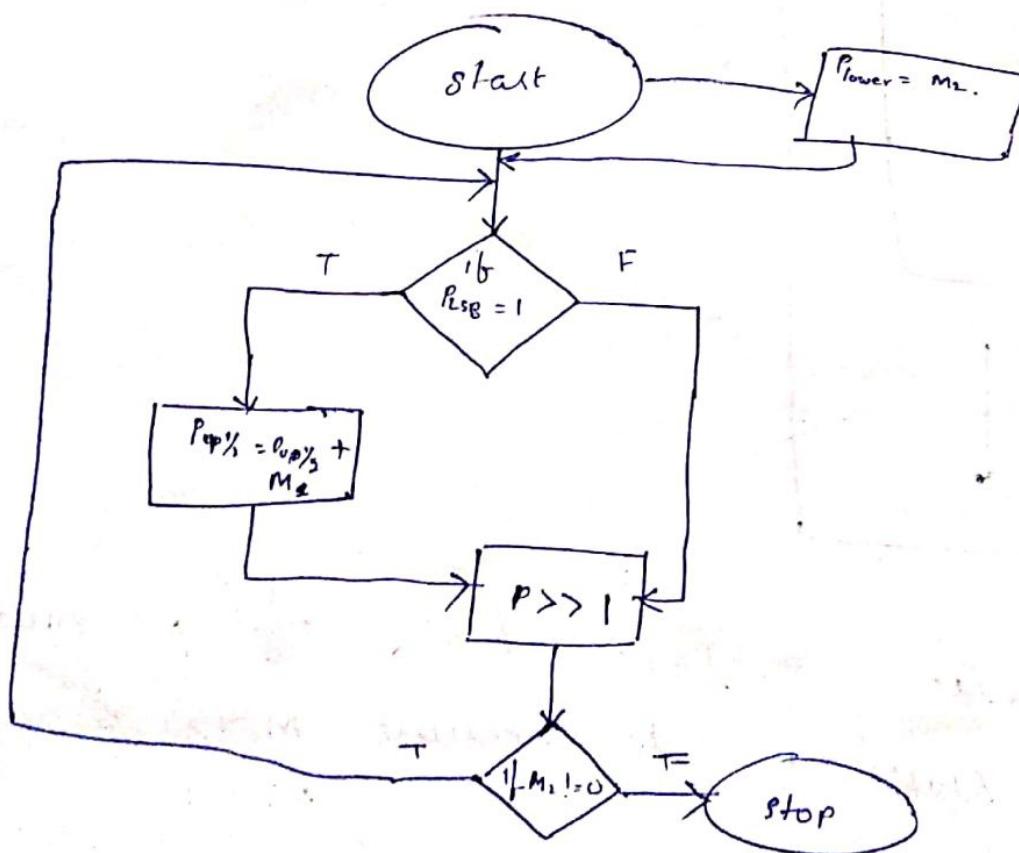
6

Draw its flow chart



- ③ store multiplicand in lower half of product  
& left shift P to combine  $M_L \gg 1$  &  
 $P \gg 1$

| Iteration step |                                       | Multiplicand | Multiplicant | product  |
|----------------|---------------------------------------|--------------|--------------|--|
| 0              | initial                               | $\alpha$     | 0 0 1 0      | <u>0 0 0 0 0 0 1 1</u>                                     |
| 1              | $P_{LSB} = 1 \therefore$              |              | 0 0 1 0      | <u>0 0 1 0 0 0 1 1</u><br>—————<br><u>M<sub>2221</sub></u> |
|                | $P_{up} = P_{up} + M_1$               |              | 0 0 1 0      | <u>0 0 0 1 0 0 0 1</u>                                     |
|                | $P \gg 1$                             |              |              |  |
| 2              | $P_{LSB} = 1 \therefore$              |              | 0 0 1 0      | <u>0 0 1 1 0 0 0 1 &gt;&gt;</u><br>0 0 0 1 1 0 0           |
|                | $P_{up} = P_{up} \cancel{\chi} + M_1$ |              | 0 0 1 0      | <u>0 0 0 1 1 0 0</u>                                       |
| 3.             | $P \gg 1$                             |              | 0 0 1 0      | <u>0 0 0 0 1 1 0 0</u>                                     |
| 4              | $P \gg 1$                             |              | 0.0 1 0      | <u>0 0 0 0 0 1 1 0</u><br>—————<br><u>6</u>                |



$$\text{Total no. of iterations} = 32 \times 2 = \underline{\underline{64}}$$

# Signed bit multiplication

$-2 \times -3$

$$0010 \times 110\textcircled{1}^{m_1}$$

11.20  
14  
18.75

| <u>Iteration</u> | <u>Step</u>               | <u>multiplicand</u><br>$(m_1)$ | <u>Product</u>           |
|------------------|---------------------------|--------------------------------|--------------------------|
| 0                | initial                   | 0010                           | 0000 1101                |
| 1                | "                         | "                              | 0010 110\textcircled{1}  |
|                  | $P_p = P_{p,p} + M_1$     | "                              | 0001 0110\textcircled{0} |
|                  | $P >> 1$                  | "                              | 0000 1011\textcircled{0} |
| 2                | $P >> 1$                  | "                              | 0010 1011                |
|                  | $P_{p,p} = P_{p,p} + M_1$ | "                              | 0001 010\textcircled{1}  |
|                  | $P >> 1$                  | "                              | 0011 0101                |
| 4                | $P_{p,p} = P_{p,p} + M_1$ | "                              | 0001 1010                |
|                  | $P >> 1$                  | "                              | 1110 0110                |

$\Rightarrow 0010$   
 $1110 \times 00101$

|    |                           |      |                         |
|----|---------------------------|------|-------------------------|
| 0. | initial                   | 1110 | 0000 001\textcircled{1} |
| 1  | $P_{p,p} = P_{p,p} + M_1$ | "    | 1110 0011               |
|    | $P >> 1$                  | "    | 1111 000\textcircled{1} |
| 2. | $P_{p,p} = P_{p,p} + M_1$ | "    | 10101 0001              |
|    | $P >> 1$                  | "    | 0010 1000               |

③  $P \gg 1$

~~1111~~ 0100

④  $P \gg 1$

~~1111~~ 1010  
ans = -6

$$M_1 \quad M_L \\ -2 \times -3.$$

1110 1101

multiplicand  
 $M_L$

0

initial

1110

00001101

1

$$P_{\text{up}} = P_{\text{up}} + M_1$$

1110

multiplier

$P \gg 1$

(sign-extended)

"

1111 0110

2.

$P \gg 1$

1111 1011

3.

$$P_{\text{up}} = P_{\text{up}} + M_1$$

"

1101 1011

$P \gg 1$

1110 1101

4)

$$P_{\text{up}} = P_{\text{up}} + M_1$$

"

1100 1101

$P \gg 1$

1110 0110

- ans

# Division

shift left & sub from 1<sup>st</sup> q bit

$\begin{array}{r} 101 \\ \times 101 \\ \hline 101 \end{array}$

$$\begin{array}{r}
 \overline{1000} \quad | \quad \overline{1001010} \\
 \overline{1000} \quad | \\
 \overline{00010} \\
 \overline{0000} \\
 \overline{0101} \\
 \overline{0000} \\
 \overline{1000} \\
 \overline{010}
 \end{array}$$

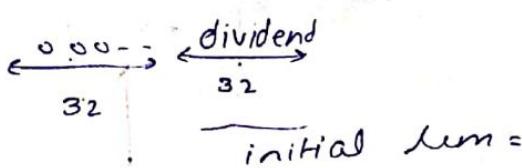
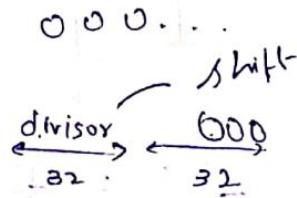
Rem.

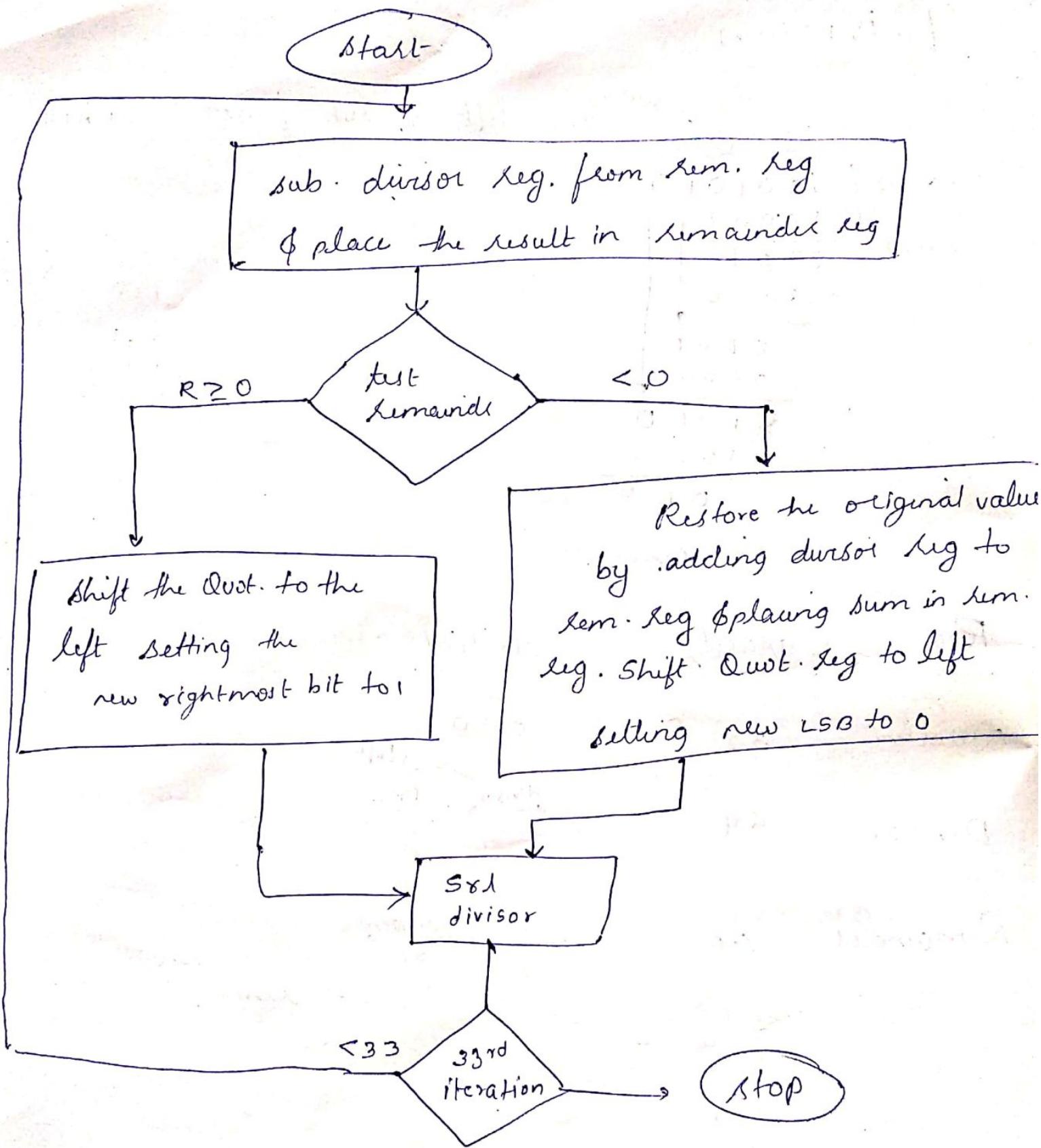
Reg. Capacity      initial

Quotient      32

Divisor      64

Remainder      64





$$10001 \div \underbrace{0101}_{\text{div.}}$$

| iteration | step        | div   | rem. (R)    |
|-----------|-------------|---|-------------|
| 0         | initial     | $\underbrace{0101}_{\text{div}} \underline{0000}$ | $00010001$  |
| 1         | $R = R - D$ | $101000(\text{D})$                                | $100010100$ |
|           | $R << 1$    |   |             |
| 2         | $R = R - D$ | $010001$  | $0000111$   |
|           | $R << 1$    |   |             |
| 3         | $R = R - D$ | $1000011$   | $0000010$   |
|           | $R << 1$    |   |             |

| 7/2. If. | Step              | div         | Quo. | Rem.       |
|----------|-------------------|-------------|------|------------|
| 0        | initial           | $0010 0000$ | 0000 | $00000111$ |
| 1.       | $R - \text{div}$  |             | 0000 | $11100111$ |
|          | $R << 1$          |             |      |            |
|          | $R + \text{div}$  |             |      |            |
|          | $\text{div} >> 1$ | $0001 0000$ |      | $00000111$ |
| 2.       | $R - \text{div}$  |             | 0000 | $11110111$ |
|          | $R << 1$          |             |      |            |
|          | $R + \text{div}$  |             |      |            |
|          | $\text{div} >> 1$ | $0000 1000$ |      | $00000111$ |
| 3        | $R - \text{div}$  |             | 0000 | $11111101$ |
|          | $R << 1$          |             |      |            |
|          | $R + \text{div}$  |             |      |            |
|          | $\text{div} >> 1$ | $00000100$  |      | $00000111$ |

4.  $R = \text{div}$

~~0000/0011~~

$a < 1$

~~0001~~

$\text{div} \gg 1$

~~0000/0010~~

(5)

$R = \text{div}$

$\text{div} \gg 1$

$a < 1$

~~0000/0001~~

~~0000/0001~~

~~/rem~~

5<sup>th</sup>  
( $4+1^*$ )

iteration  
 $r = \text{div}$

∴ stop

~~0011~~

~~0000/0000~~

111<sup>th</sup> to

0010 [ ~~0000/0111~~ ]

- Rem. 111  
→ div = possible  
 $a < 1$  set
- Rem. 110  
→ div = impossible  
 $a < 1$  bits

## Division 2

Instead of  $\text{div} \gg 1$  do  $\text{temp} \ll 1$

Divisor

32 bit

divisor

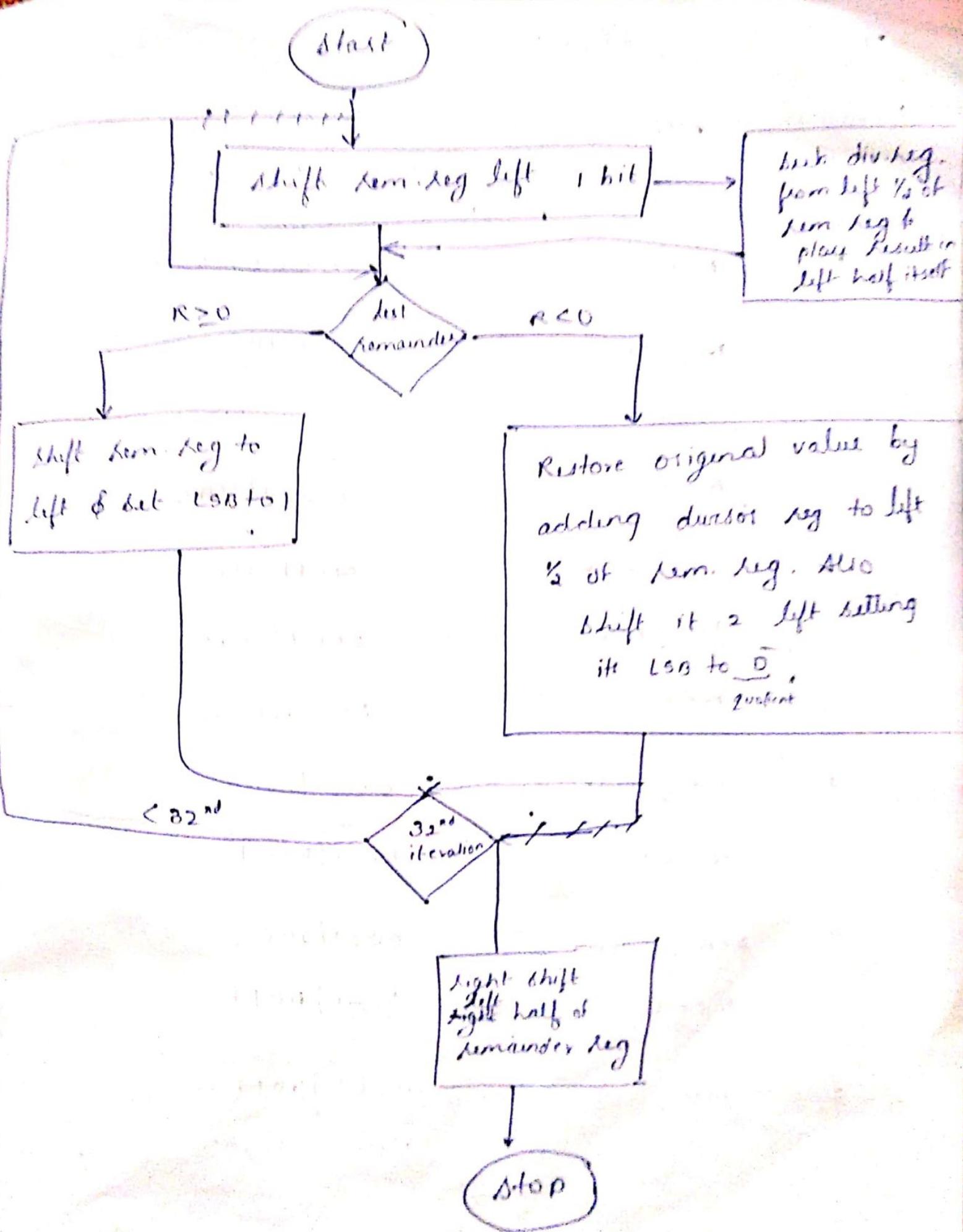
Rem

64 bit

~~0000~~ → dividend

ALU

32 bit



7/2

Iteration

Step.

div.

rem

0

initial

0010

0000 / 0111  
rem<sub>%</sub>

1

R << 1

0000 / 1110

1 - 10 / 1110

rem<sub>%</sub> - div

rem<sub>%</sub> + div

R << 1

0 0 0 0 / 1110  
0010

0 0 0 1 / 1100

2

rem<sub>%</sub> - div

0111 / 1100

rem<sub>%</sub> + div

0001 / 1100

R << 1

0011 / 1000

3

rem<sub>%</sub> - div

0001 / 1000

rem<sub>%</sub>  
0011 / 0001

R << 1

0001 / 000 ]

0010 ) 0011

4

rem<sub>%</sub> - div

R << 1

0001 / 0011

rem      quotient

5

rem<sub>%</sub> > 1

## Scientific & normal form

$$\text{eg} \quad \text{normal} \quad \left\{ \begin{array}{l} 1.0 \times 10^9 \\ 0.1 \times 10^{-8} \end{array} \right\} \xrightarrow{\text{scientific}} \left\{ \begin{array}{l} 1.0 \times 10^{-10} \end{array} \right\} \text{normal}$$

## Representations

→ single precision

|      |                |   |   |                 |   |
|------|----------------|---|---|-----------------|---|
| 31   | 30             | . | . | 23 22           | 0 |
| Sign | 8 bit exponent |   |   | 23 bit fraction |   |

overflow : +ve exponent is too large

underflow : -ve " "

To represent a no:

① convert to normal form

② add bias value to exponent =  $2^{n-1} - 1$   
 $n = \text{no. of bits}$

eg: @hyp.  $(0.1)_k = 1.0 \times 2^{-1}$  in 8 bit  $\therefore$  exponent = 3 bit

③ bias =  $2^{3-1} - 1 = 3 \therefore -1 + 3 = 2$

|    |     |       |   |   |
|----|-----|-------|---|---|
| 7  | 6   |       | 3 |   |
| 10 | 010 | -0000 | F | 0 |

0.0625  $2^{-9}$ , 0.0

$$\textcircled{2} \quad 0.5625 = 2^{-1} + 2^{-4} \quad (8 \text{ bit})$$

$$= 0.1 +$$

$$0.0001 =$$

$$\underline{0.1001} = 1.001 \times 2^{-1} \Rightarrow \text{normal form}$$

$$\text{base } = 2^{3-1}-1 = 3$$

$$\Rightarrow -1 + 3 = 2$$

|   |     |      |   |
|---|-----|------|---|
| 7 | 6   | 3    | 0 |
| 0 | 010 | 0010 | F |

$$\textcircled{3} \quad -0.75 \quad \text{single precision} \rightarrow 32 \text{ bit} \Rightarrow n=8$$

$$= -0.1 +$$

$$-0.01$$

$$\underline{-0.11} = -1.1 \times 2^{-1}$$

$$\therefore n=8$$

$$b = 2^{n-1}-1 = 127$$

$$\therefore \Rightarrow -1 + 127 = 126$$

|    |         |            |   |
|----|---------|------------|---|
| 31 | 30      | 23-22      |   |
| 1  | 0111110 | 1000000... | 0 |

convert back

$$b = 2^{3+1} - 1 = 93 \quad e = 2 + 3 = -1$$

$$\boxed{10|01010010} \times (1 + 0.25) \times 2^{-1} = 0.5625$$

$$= \underline{\underline{0.5625}}$$

to store exp  
add bias

②  $\begin{array}{ccccccccccccc} 31 & 30 & & 23 & & & & & & & & & & & \\ 111100\ldots011.0100\ldots01 & & & & & & & & & & & & & & \end{array}$

8 bit

$$\text{bias} = 2^{8-1} - 1 = 127$$

$$\therefore \text{exp} = (2^7 + 1) - 127 \\ = 128.0 - 127 = +3.02$$

$$\therefore -1.01 \times 2^{\frac{2}{+3.02}} = -(1 + 0.25) \times 2^{\frac{+2.02}{+3.02}} \\ = -1.25 \times 2^{\frac{+2.02}{+3.02}}$$

$$= \underline{\underline{-5}}$$

single precision

double precision

obj

|                    | E       | F        | E       | F         |                |
|--------------------|---------|----------|---------|-----------|----------------|
| $\downarrow (E+F)$ | 0       | 0        | 0       | 0         | 0              |
|                    | 0       | non-zero | 0       | non-zero  | $\pm$ denormal |
| 1-254              | Any     |          | 1-2046  | any       | $\pm$ float    |
| 255                | 0       | 2047     | 0       | 0         | $\pm \infty$   |
| 255                | Nonzero | 2047     | Nonzero | Not a no. |                |

In denormalised form

$$\text{value} = (-1)^s (0+f)^{1-b}$$

Self study: floating point multiplication

⇒ Floating point addition

Assumption: We have storing of 4 decimal digits of significance & 2 digits of exponent in fraction

- ① Align the decimal point of number with smaller exponent so that it matches the larger one (After normalising & truncating)

e.g.:  $A = 9.990 \times 10^1$   
 $B = 161.0 \times 10^{-1} \Rightarrow B = \underbrace{1.610}_{4} \times 10^1$

- ② addition of significance

$$\begin{array}{r} 0.990 + \\ 1.610 \\ \hline 11.609 \end{array} \times 10^1$$

- ③ Normalisation

$$\underbrace{1.1609}_{5 \text{ but 4 allowed}} \times 10^2$$

$$\therefore \text{round} \Rightarrow 1.161 \times 10^2$$

(final result must be normalized)

$$0.1 \cdot 0.5 - 0.4375$$

$$\frac{0.1 \times 2^0}{10 \times 2^{-1}} - 0.0111 \times 2^0 \Rightarrow 1.000 \times 2^{-1} - 1.11 \times 2^{-2}$$

$$\begin{array}{r} 0.1 \\ \cancel{0.0111} \\ \cancel{0.001110} \times 2^0 \\ \Rightarrow 1.000 \times 2^{-3} = 0.0625 \end{array}$$

$$\begin{array}{r} 0.125 \\ \cancel{5} \\ \cancel{0.0625} \\ 0.0625 \end{array}$$

$$\begin{array}{r} 0.25 \\ 0.125 \\ 0.0625 \\ \hline 0.4375 \end{array}$$

$$\begin{array}{r} 1.000 \times 2^{-1} \\ 1.110 \times 2^{-2} \\ \hline 0.001 \times 2^{-1} = 0.0625 \\ \hline 1.000 \times 2^{-4} \end{array}$$

$$\begin{array}{r} 0.5000 \\ 0.4375 \\ \hline 0.0625 \end{array}$$

Min value of exp in single precision = -128  
+ bias = 127

$$\text{Max value} = \underline{127} + 127 = \boxed{254} \checkmark$$

Parallelism & computer arithmetic

Associativity

Integers follow associativity but

floating point numbers not so

e.g.:  $x = -1.5 \times 10^{-38}$

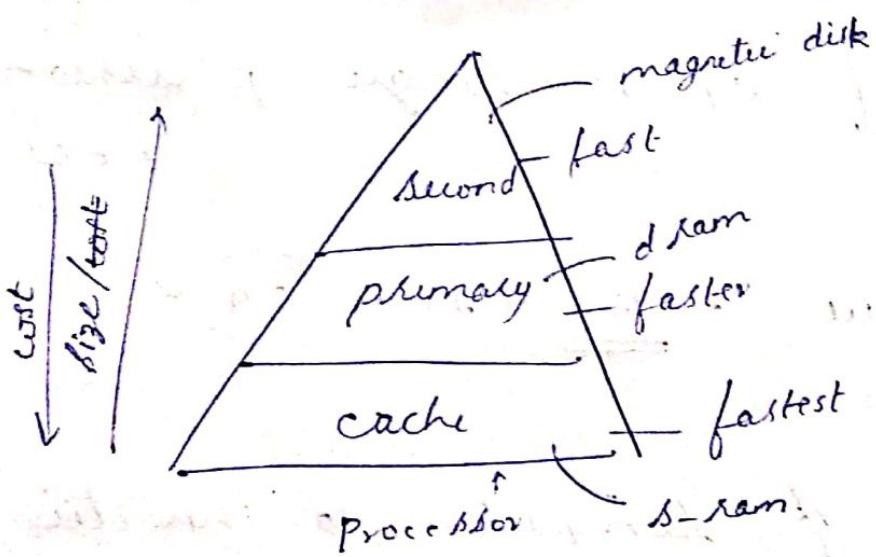
$y = 1.5 \times 10^{-38}$

$z = 1.0$

I]  $x + (y+z) = x + 1.5 \times 10^{-38}$   
= 0

II]  $(x+y)+z = 0 \times 10^{-38} + z$   
= 0 + 1  
= 1

### Memory hierarchy:



processor access data from cache

data copied from  $2^{\circ} \rightarrow 1^{\circ} \rightarrow \text{cache} \rightarrow \text{processor}$

Now, processor at once copies all the data items needed for a given piece of code rather than one at a time.

Now it is based on principle of locality

### ① temporal locality

if we access  $x$ , then there is higher probability of accessing  $x$  in near future (eg: loops) ( $\therefore$  copy to cache)

### ② spatial locality

if we access  $x$ , then there is higher prob. of accessing data near to  $x$  in near future

### iii. Arrays

(If we copy items / subset of data so that if we need a data we need to search only cache (smaller) than  $1^{\circ}$  or  $2^{\circ}$  (larger))

block / line: Minimum unit of info.

that is either present or not present in memory.

hit: A hit is occurred if a data item given is found in a level else its a miss.

If a hit is occurred it copies it to next level closer to processor

else matches in next level above it

hit ratio: % of memory access satisfied by that level

$$\text{hit ratio} = \frac{\text{no. of hits in level}}{\text{no. of access in level}} \times 100$$

$$\text{miss ratio} = 100 - \text{hit ratio}$$

hit-time: time taken to provide a data to CPU including the time to

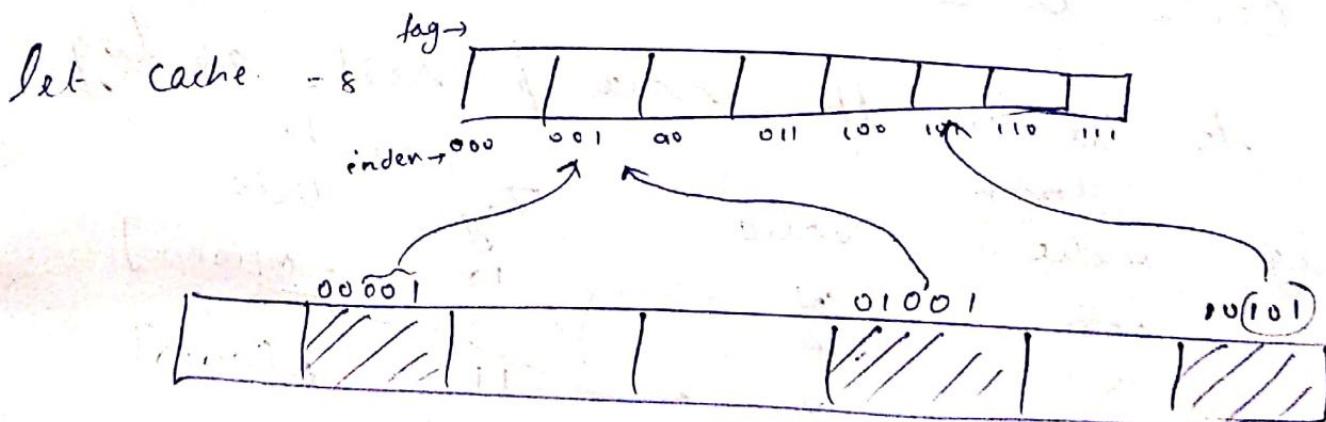
check if its a miss / hit

mispairality: If its a miss time to copy lower to upper mem + time to deliver data to CPU

Now, as cache limited space is there

so how do we copy all the elements needed for a program in there (duplication or overwrite may occur)

if how the processor can identify the data from cache?



is lower 3 bit as index of upper 5 bit tag  
here by using modulo on address  
/8

if add data = 00001 we do  
modulo 8 to find index 001

Now to know if its lower bit of 0100101  
00001 place upper bit in tag associated with  
that position in cache.

This is called Direct mapped cache

Now to see if its valid data or not we  
have a valid bit also associated with each  
cell.

If an address of a data is

$x_n x_{n-1} \dots x_0$  then if size of  
cache is  $2^n$ , then lower n bits is used  
to decide the index of rest as tag.

| Index | valid | Tag | Data     |
|-------|-------|-----|----------|
| 000   | Y     | 10  | M[10000] |
| 001   | N     | 11  | M[11010] |
| 010   | Y     | 00  | M[00011] |
| 011   | Y     | 01  | M[00010] |
| 100   | Y     | 10  | M[10110] |
| 101   | Y     | 11  | M[11100] |
| 110   | Y     | 00  | M[00100] |
| 111   | N     | 01  | M[00111] |

If 10110 is needed by processor, it checks

index : 110 (cache =  $2^3$ )

first it check 110; since  $v \neq N$  & tag & index  
& not same  $\therefore$  its a miss.  $\therefore$  copy data;  
& store address in corresponding index

Then we need 11010  
tag index  $\rightarrow$  miss  $\therefore$  store.

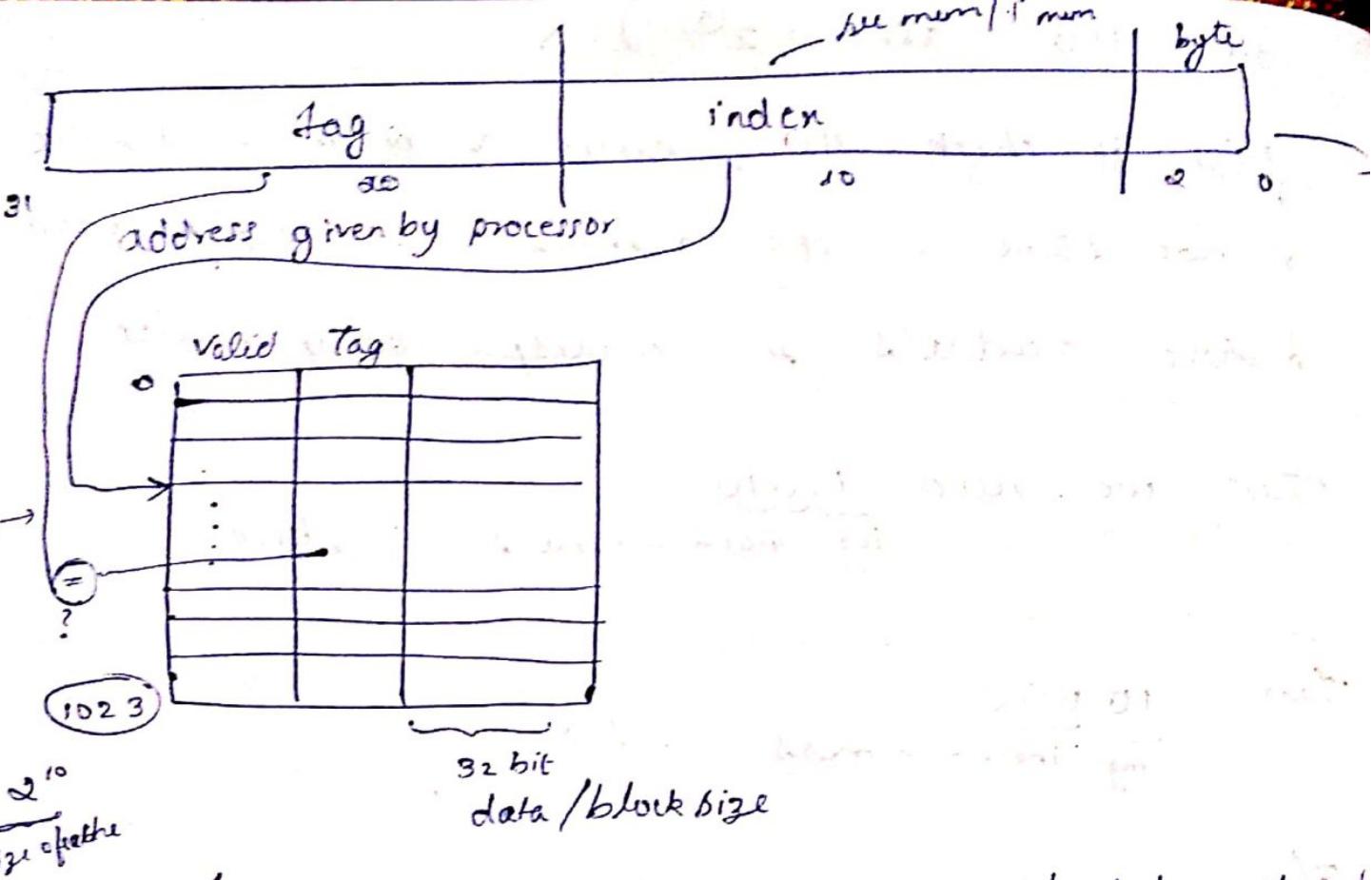
Then 10 000  
tag index  $\rightarrow$  miss  $\therefore$  store.

Then 00011  
tag index  $\rightarrow$  miss  $\therefore$  store.

Then 10 010  
tag index  $\rightarrow$  miss [index same but tag not equal]

$\therefore$  store update tag

$\therefore$  if info is changed in sec. memory  
change valid bit of corresponding address as  
"N" in cache.



here we need only part of data stored in cache (earlier we accessed all the bits) stored in cache). Here if we need 1st byte of data, what to do?

Here we use 10 bits for index

Now how to access byte by byte.

we need 2 bits to know which portion of word you need to access (words has 4 bytes)

∴ index - 10      tag - 20 bits.  
byte portion - 2

If processor gives a 32 bit address, we use its index part to find its location in cache, tag part to check if its the requested data that is stored in that location & valid bit to check validity. If its a hit, then we use the byte part to know which byte portion of the word stored in that cache location is required by processor.

Q] 32 bit <sup>access byte by byte</sup> byte addressed directed mapped cache  
of size  $2^n$  blocks, & block size  $2^m$  words.  
find size of tag field, total no. of bits  
in directed mapped cache

A: total =  $2^m \cdot 2^n \times 2^k$

$$\text{tag field} = 32 - \underbrace{n - 2 - m}_{\text{index + byte}} = 32 - [n + m + 2]$$

<sup>index to identify from  $2^m$  words</sup>

<sup>to find the byte portion from each word</sup>

total =  $2^n \left[ \underbrace{\text{Blocksize} + \text{tagsize} + \text{valid}}_{\text{Store data}} \right]$

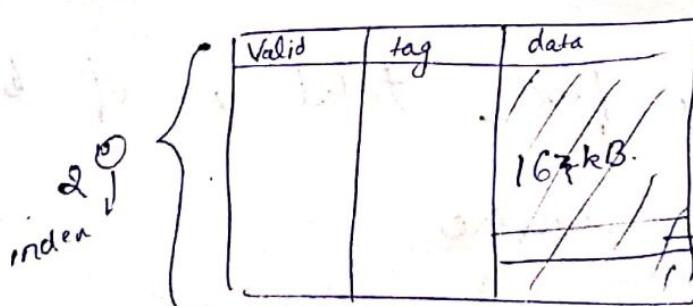
$$= 2^n \left[ \underbrace{2^m \times 32}_{\text{bits.}} + 32 - [n+m+2] + 1 \right]$$

Q) how many total bits & tag for di. mapped cache with 16 kB of data & 4 word block assuming 32 bit address

A: tot-block-size = 16 kB  
 $\Rightarrow$  size of 1 block  $\times$  total no. of block lines  
 $4 \times 4 \text{ byte}$

$$\Rightarrow \text{total no. of block lines} = \frac{16 \text{ kB}}{16 \text{ byte}}$$

$$K = 1024 \\ 2^{10}$$



$$\therefore \text{tag} = 32 - [10 + 2 + 2] \quad [\text{for word} \quad \text{for byte}]$$

$$= 32 - [14] = 18$$

$$\therefore 1 \text{ line} = 1 + 18 + 4 \times 32 = 19 + 128$$

$$= 147 \text{ bits}$$

$$\text{total} = 2^{10} \times 147 \text{ bits} // = \underline{\underline{147 \text{ kB}}}$$