

grading policy (relative)

Midterm - 30%

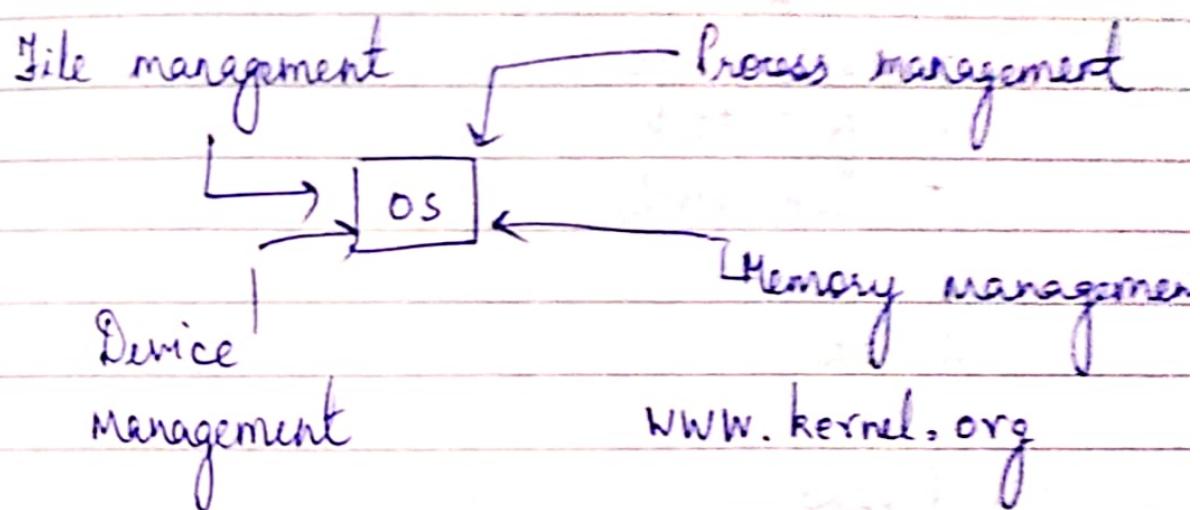
Final - 40%

Project - 20%

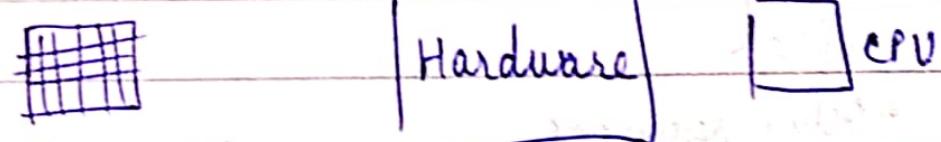
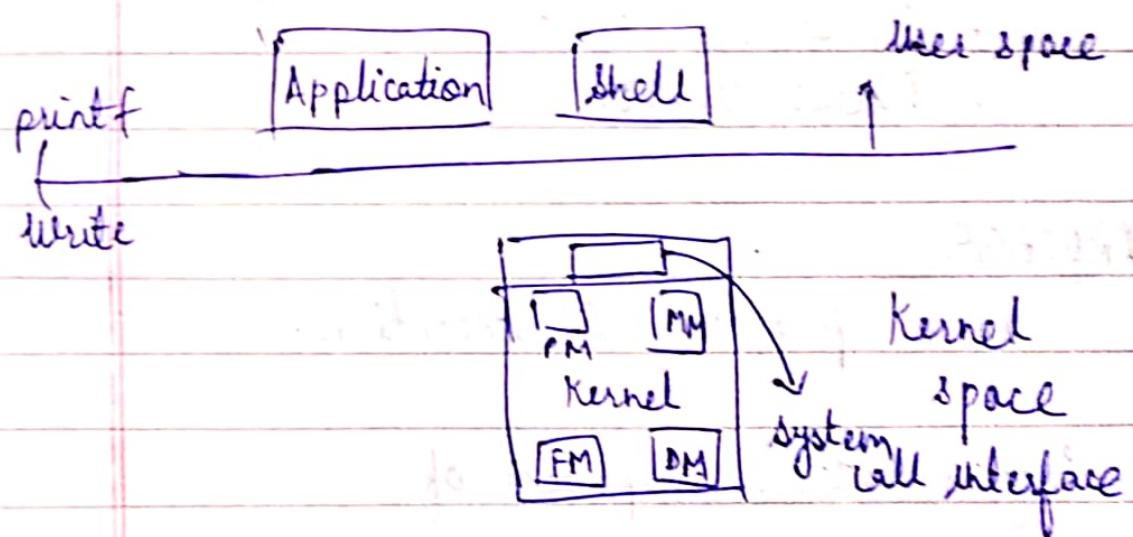
Ques 2 - 10%



OS is a program which acts as an interface b/w hardware & user of the computer.



www.kernel.org



Linux OS
architecture

- Linux booting
- Linux OS Stack

- GRUB
- Boot Image
- INIT task
- Run levels

Agenda

- Process
 - Process context
 - Process life cycle
 - Process creation

PROCESS

is a program in execution.

- Process context comprised of
 - Text
 - Data
 - Stack
 - Other resources

- Preemptive SJN
- Preemptive priority scheduler
- Round Robin
- Multilevel queue
- Multilevel feedback queue

Preemptive SJN

| P_r | Service Time | Arrival time |
|-------|--------------|--------------|
| P_0 | 36 | 00:00 |
| P_1 | 16 | 00:10 |
| P_2 | 15 | 00:15 |

| | | | |
|-------|-------|-------|-------|
| 0 | 10 | 20 | |
| P_0 | P_1 | P_2 | P_0 |
| 35 | | | 55 |

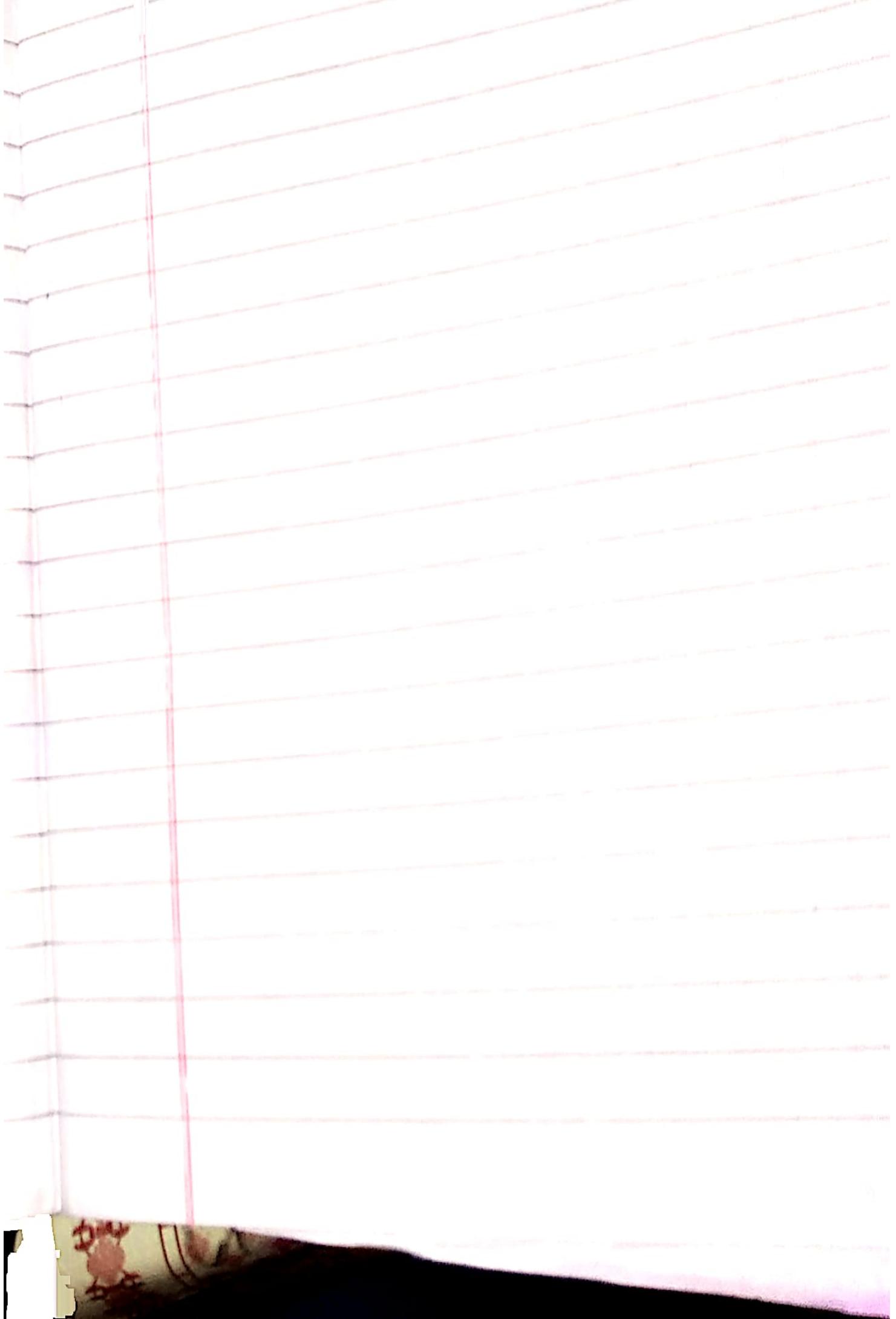
Preemptive priority scheduler

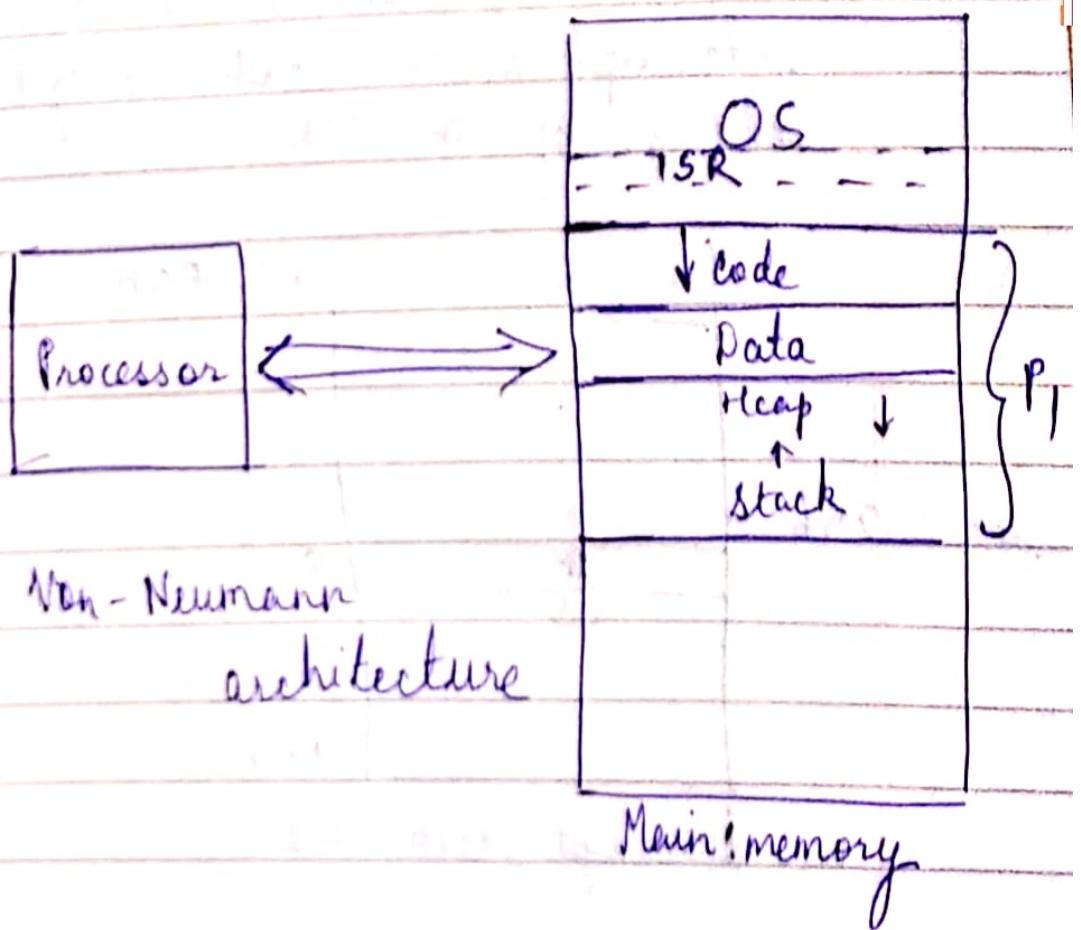
| P_r | Service time | Priority | Arrival time |
|-------|--------------|----------|--------------|
| P_0 | 30 | 2 | 00:00 |
| P_1 | 10 | 1 | 00:10 |
| P_2 | 20 | 3 | 00:15 |

0 10 20 40 60

| | | | |
|-------|-------|-------|-------|
| P_0 | P_1 | P_0 | P_2 |
|-------|-------|-------|-------|

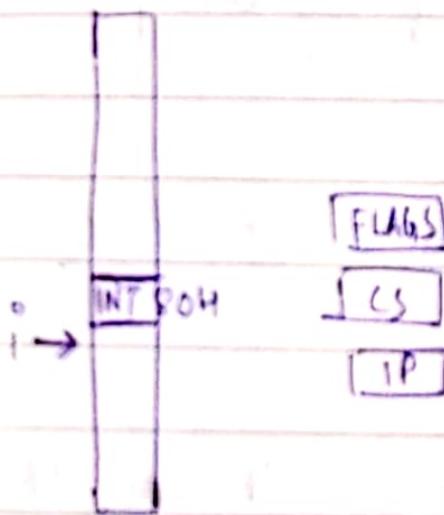
110 mm





Von-Neumann
architecture

Main memory

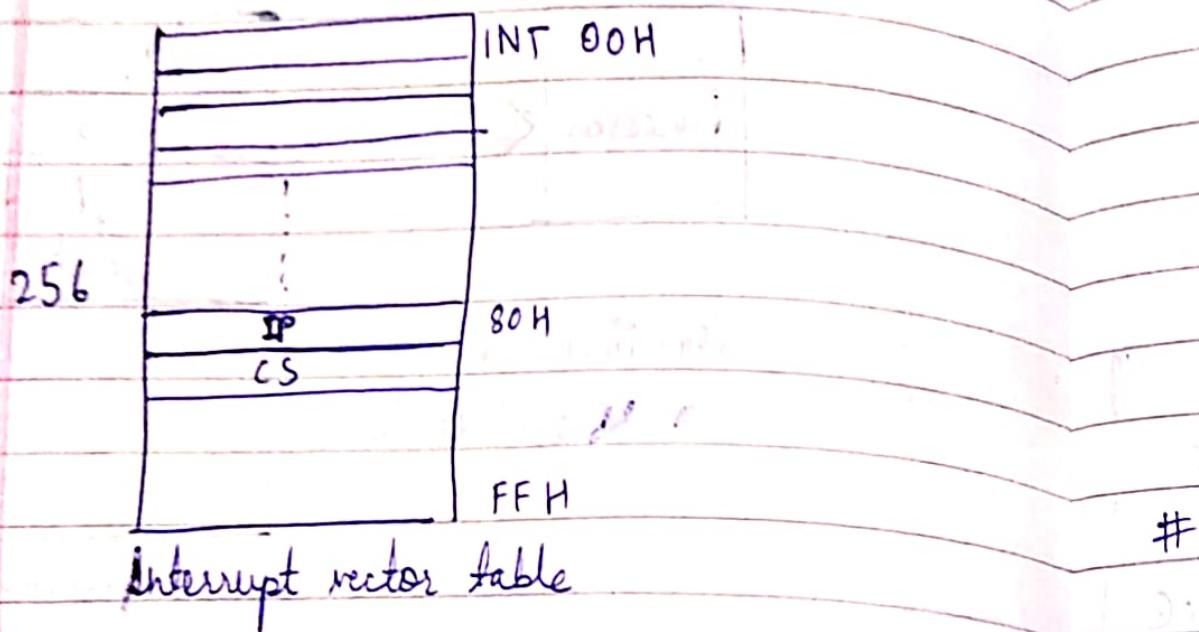


Code

FLAGS, CS &

When INT occurs, IP is stored on the stack.
(done by the machine)
(os)

When interrupt occurs, the OS refers the interrupt vector table to get the starting address of the ISR.



First 4 bytes are for interrupt vector no. 00
next 4 " " 01H H

Total 1 kB of data is stored in interrupt vector table.

→ The last instruction in ISR is: IRET.

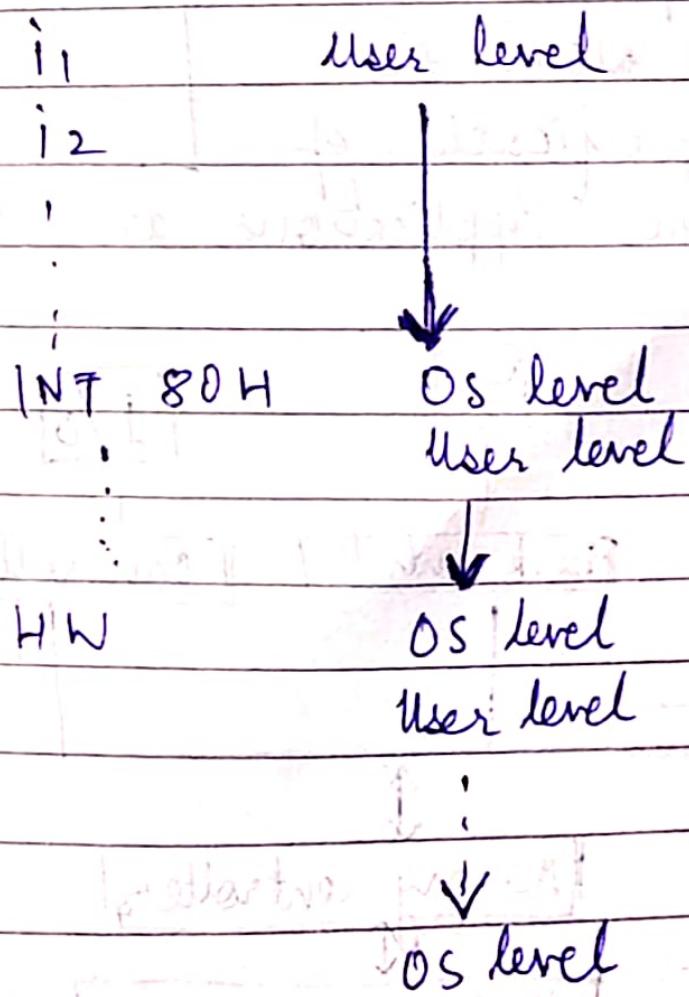
IP, CS & FLAGS are restored from the stack.

We also need to restore the register values.
This can be done in the program by **PUSHA**
before the interrupt instruction.

IDTR contains the starting location of the
interrupt vector table.

The OS's designer can decide where the
interrupt vector table should be stored in
the OS.

PROCESS



80X86 - 4

Ring level

00 ← os (Highest priority)

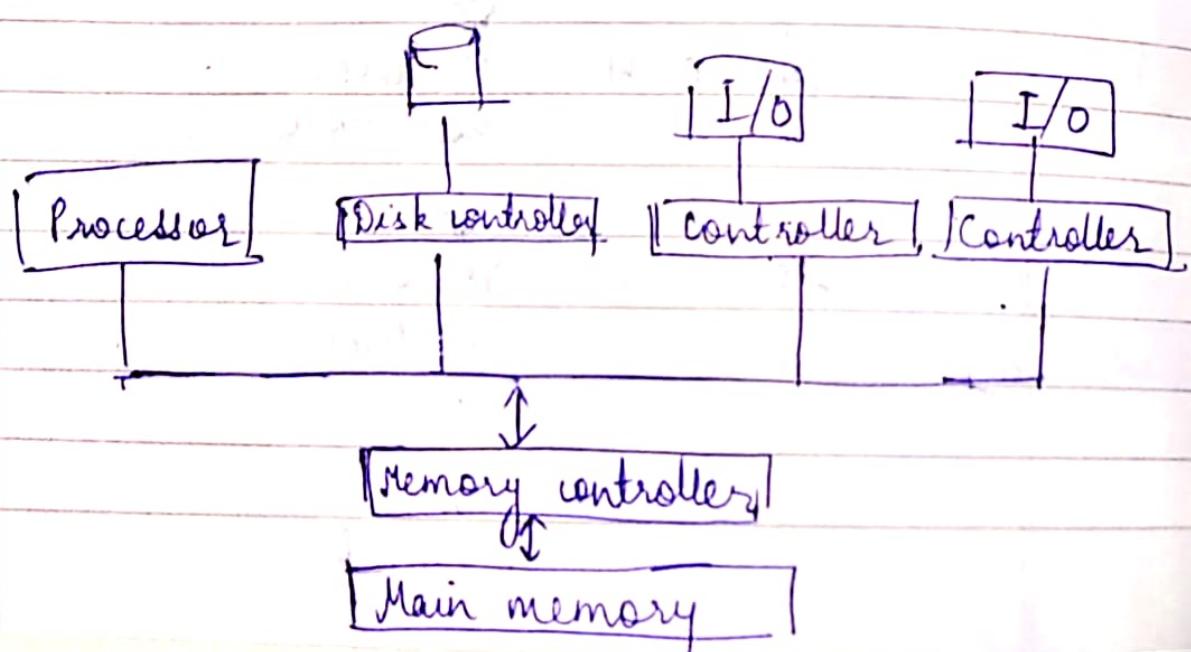
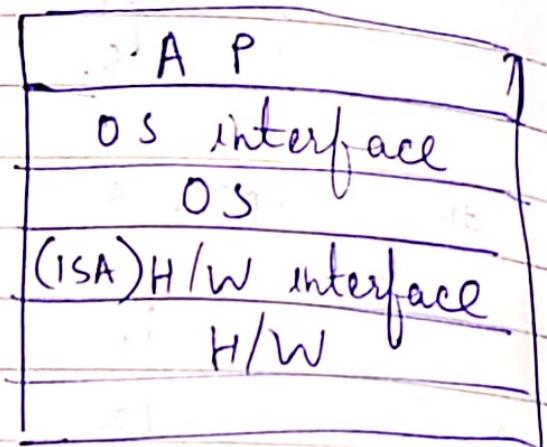
01

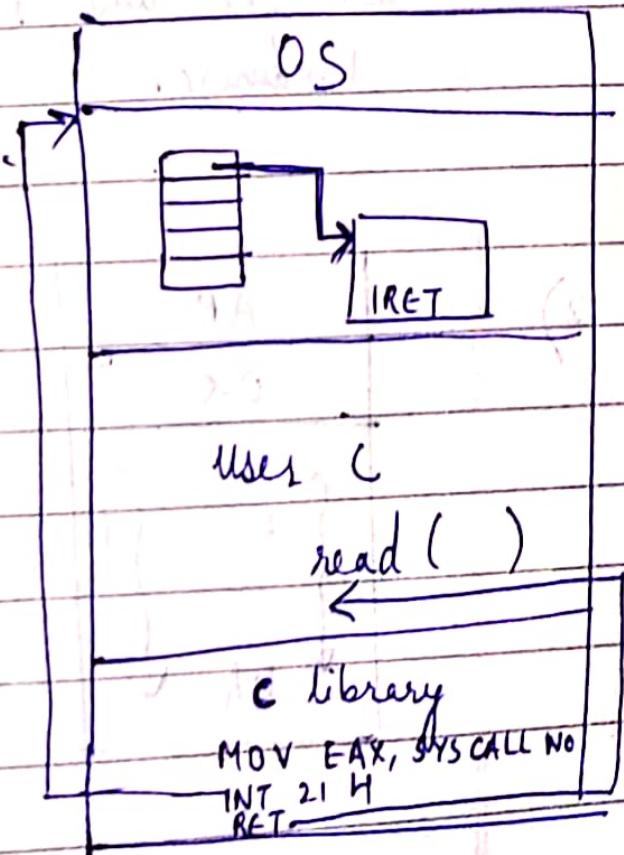
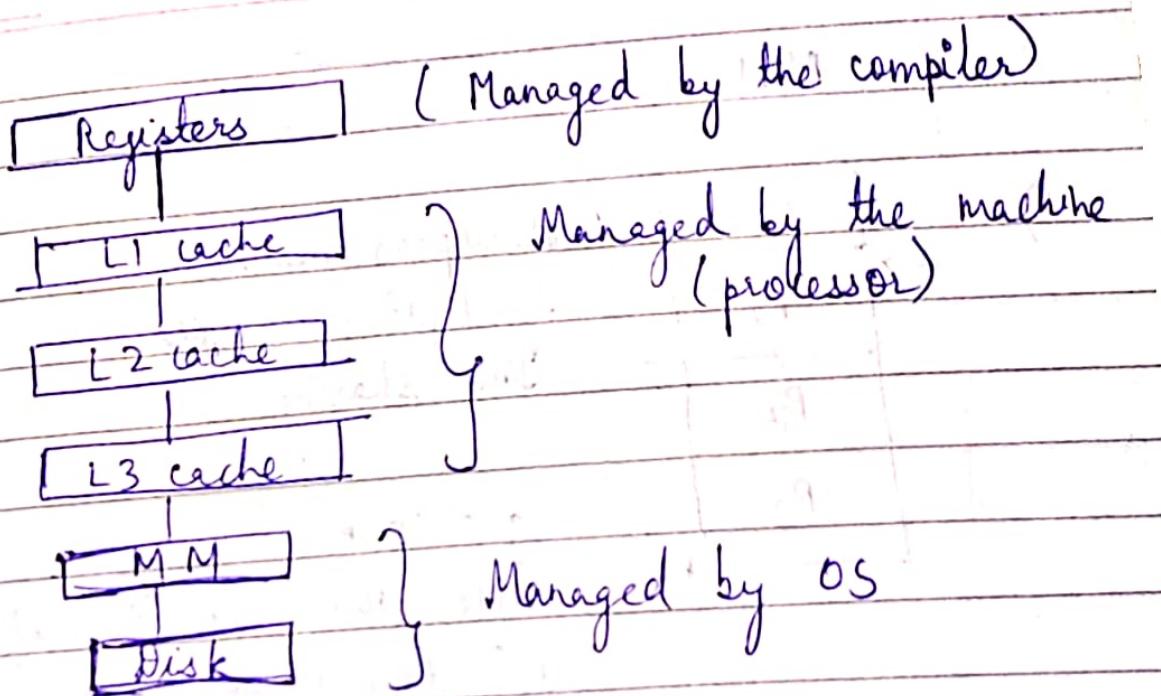
10

11

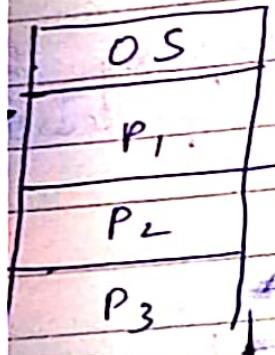
(System calls)

application programs need
not know about the
hardware specification of
the machine. Applications are OS dependent.





MM

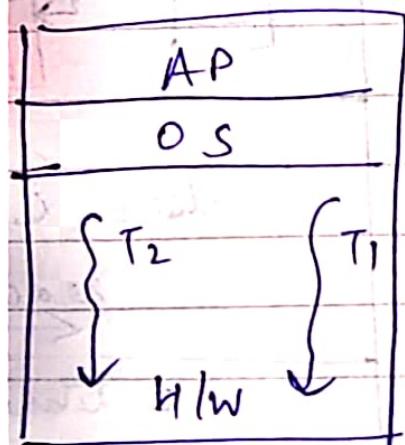


Multiprogramming
Multiple processes
Time sharing

$$q = 10 \text{ ms}$$

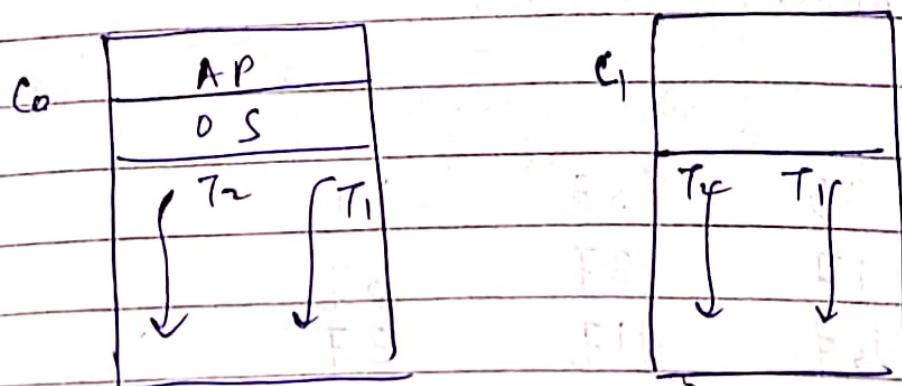
After 10 ms execution of process is stopped & execution of next process is started.

The time is measured by a timer in hardware.



Two threads \Rightarrow 2 PC values, 2 registers
in the same core
Two processes run at a time.

3) Multi-core architecture



4 processes at a time.

`fork()` :- This system call creates a new process.

Returns the process id.

```
int main()  
{
```

```
    pid_t pid1, pid2;  
    n = 10;
```

```
    n = n + 5;
```

```
    pid1 = fork();
```

```
    if (pid1 == 0)
```

```
    {  
        n = n + 20;  
        printf("%d", n);
```

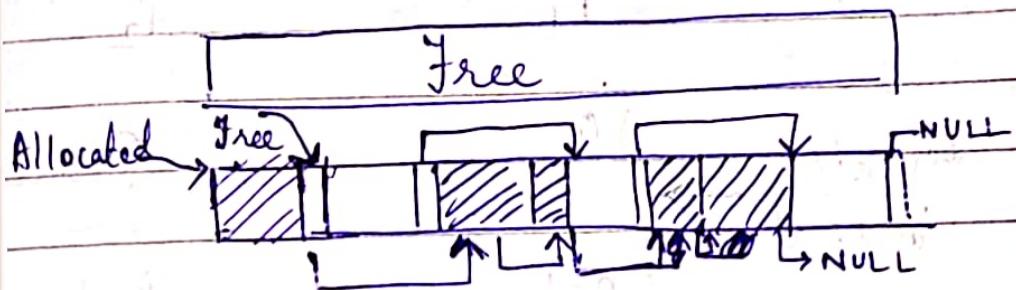
3

```
n = n + 2;  
printf("%d", n);  
}
```

| | | |
|----|----|----|
| 35 | 35 | 17 |
| 17 | 37 | 35 |
| 37 | 17 | 37 |

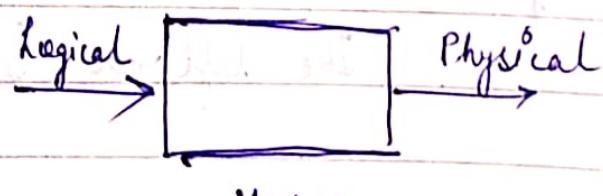
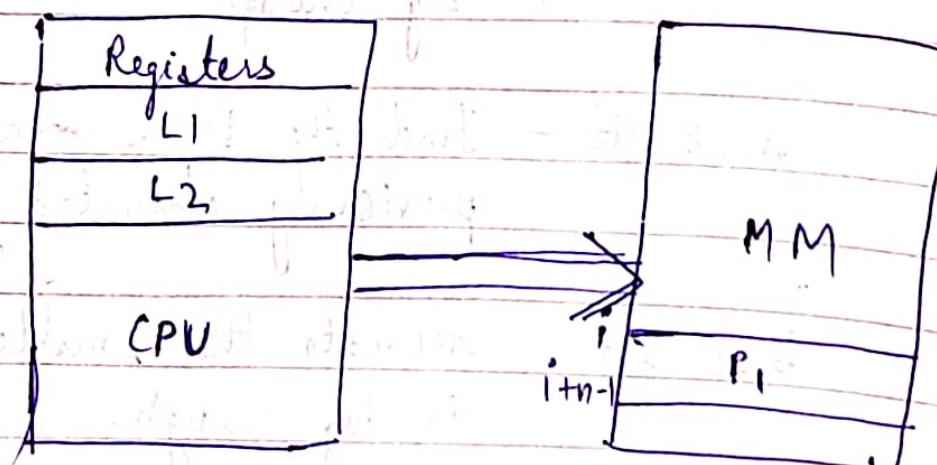
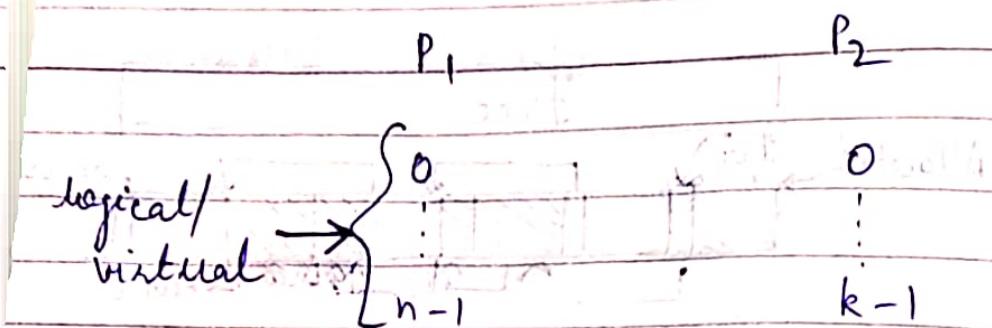
MEMORY MANAGEMENT

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- 1) First fit - Find the first block which is big enough
- 2) Next fit - Find the block after the previously allocated block.
- 3) Best fit - Allocate the smallest block which is big enough
- 4) Worst fit - Allocate the largest block, The leftover block is still big.

(Circular linked list)



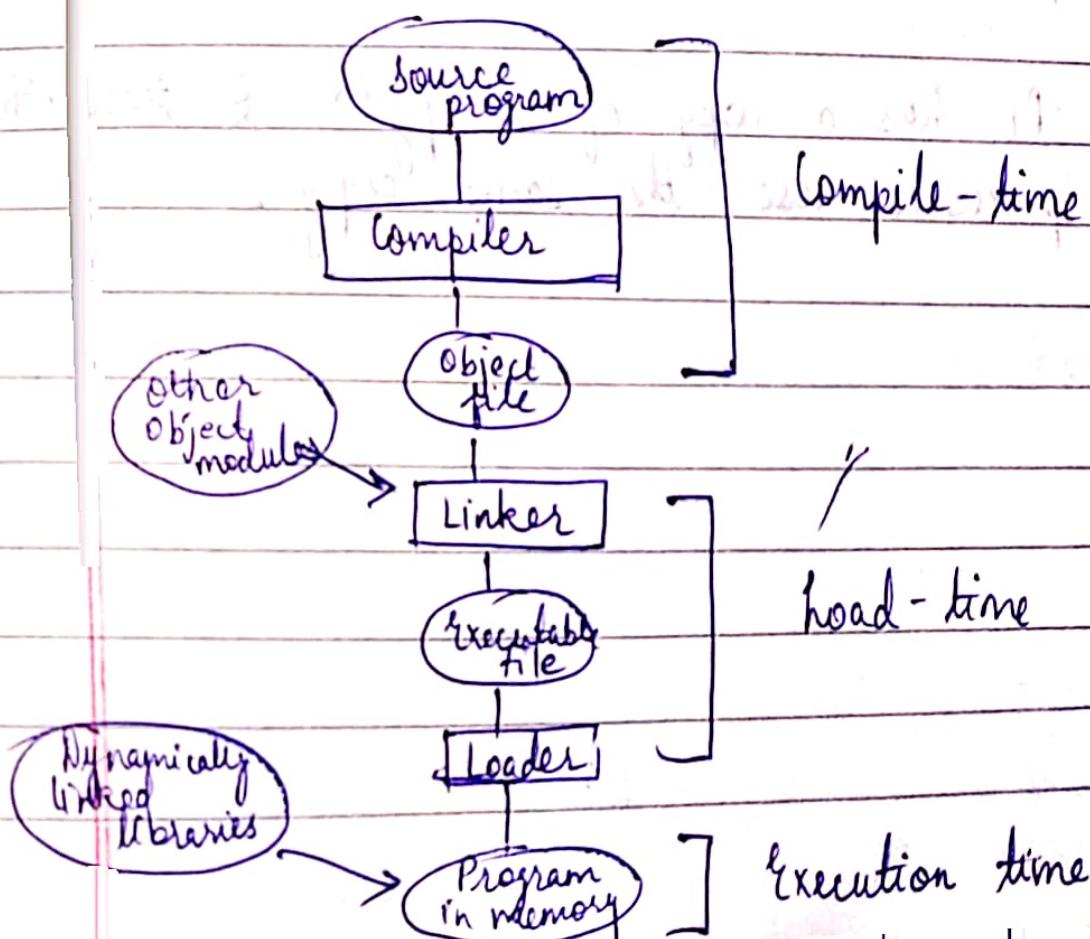
MMU
(Memory management unit)

Compile-time binding - The physical addresses of the processes are fixed at compile-time so it is not possible to load it anywhere else in the MM afterwards.

Load-time binding - Loader loads the program into the MM at load-time.

It is not possible to move to another location at execution time.

Execution-time binding - At run-time, the translation of addresses takes place (logical \rightarrow physical).



required. The module have to be dynamically linked.

#

DYNAMIC LINKING

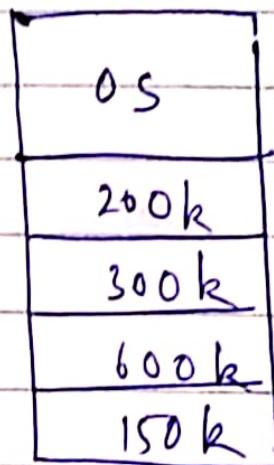
10 programs → strcpy()

In dynamic linking, only one copy of strcpy() is present & can be used by all the processes.

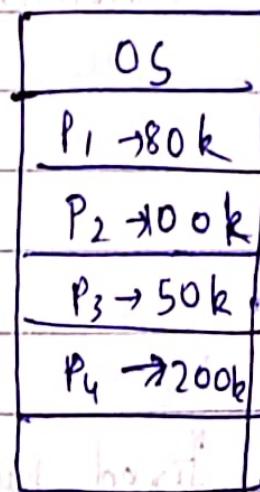
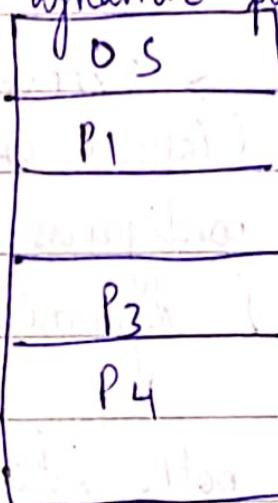
P₁ has a copy of strcpy() & then other processes use the same copy.

PARTITIONED MEMORY ALLOCATION (CONTIGUOUS ALLOCATION)

→ Fixed partition - Cannot change size during run-time.

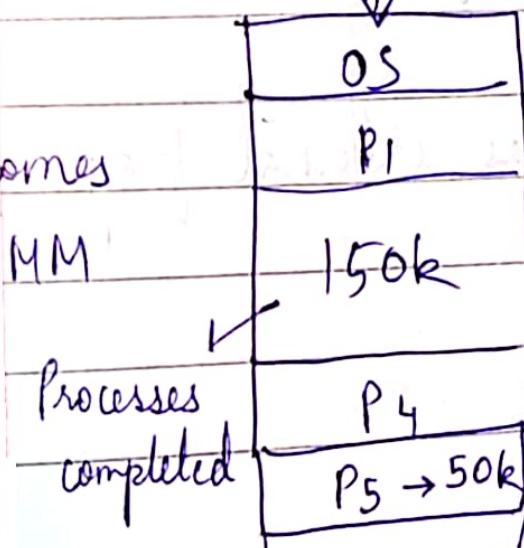


→ Variable / dynamic partition

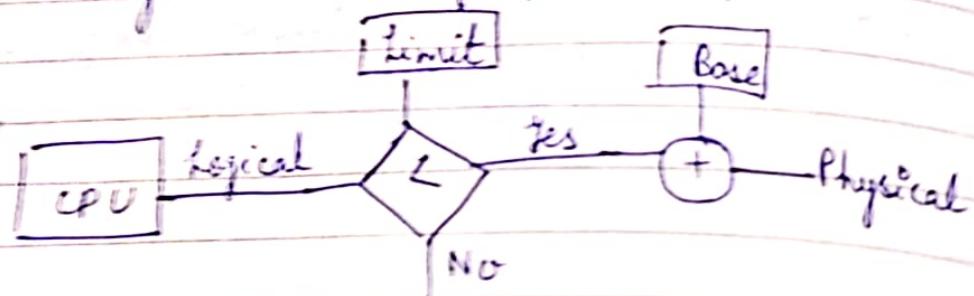


P₅ comes

into MM



Base register - Starting location of process
 Limit register - Size of process.



Trap (Violation)
 Exception illegal memory access

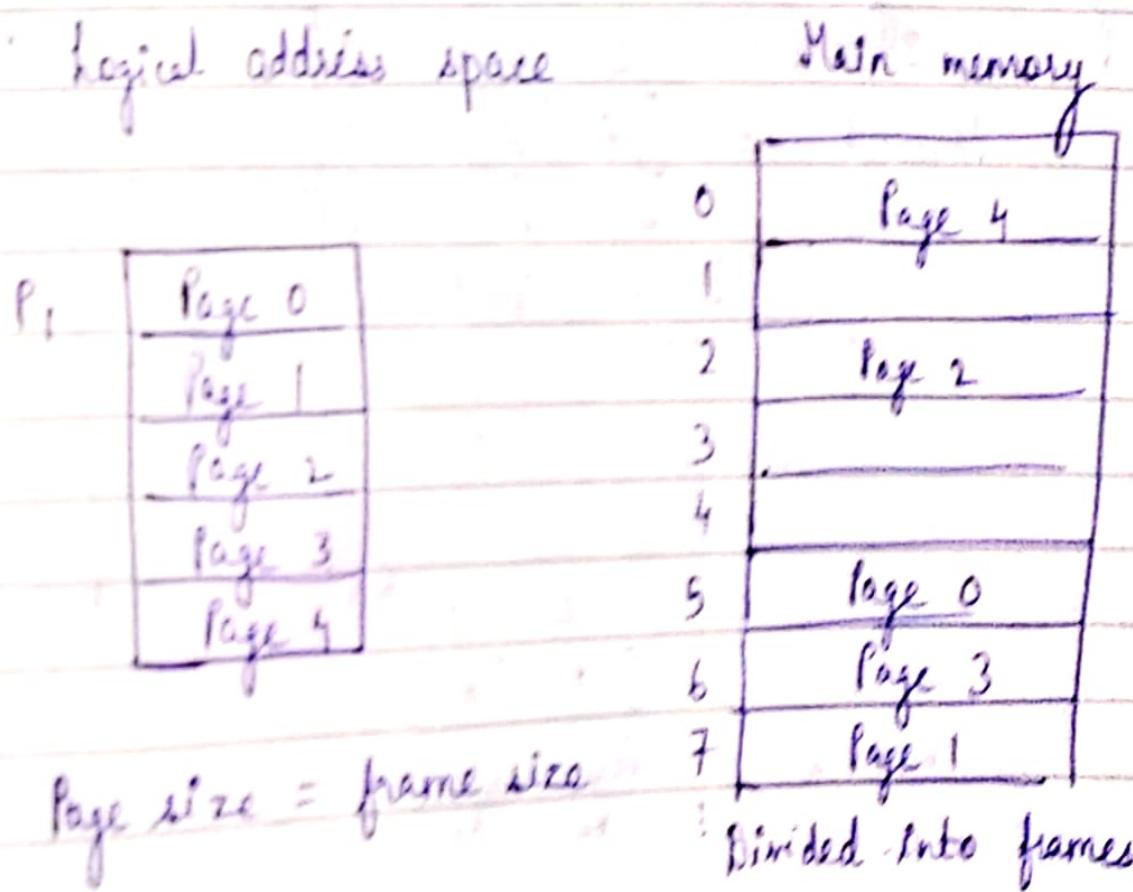
FRAGMENTATION → Internal (due to fixed partitions)
 → External
 (Caused due to when sufficient contiguous memory is not present in dynamic allocation)

Fixed partition has both internal as well as external fragmentation.

Dynamic partition has internal frag.

- External fragmentation can be resolved by compaction. Compaction is a costly operation. The processes have to be reallocated into one used area & remaining area becomes free.
- Avoid contiguous allocation to resolve external fragmentation.
This can be done by paging.

PAGED MEMORY (Modern computers use this)



Page table stores where the pages are loaded in the ^{main} memory. Each process has a page table.

Page Table

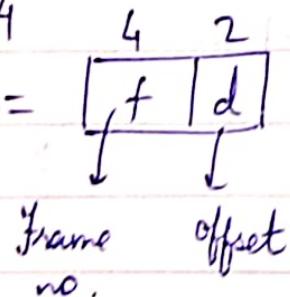
| | | |
|------|---|---|
| PTBR | → | 5 |
| 1 | | 7 |
| 2 | | 2 |
| 3 | | 6 |
| 4 | | 0 |

Index of page table
is the page no.

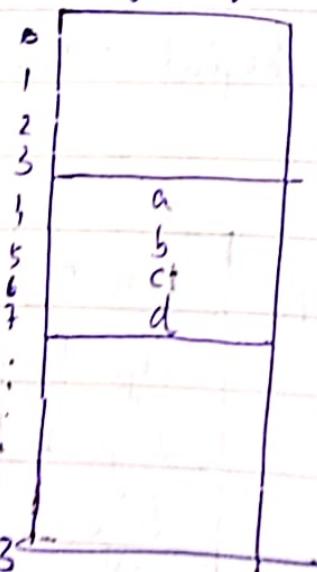
e.g

$$2^6 = 64$$

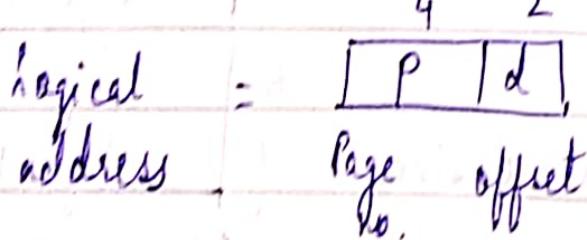
Physical address

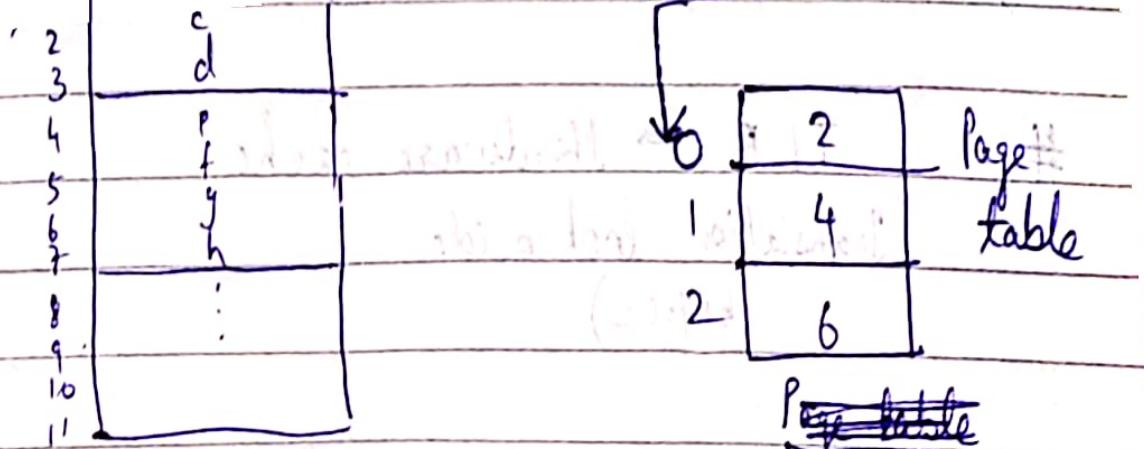


64 bytes of data



Memory frame





$$\text{Physical address} = \boxed{0010|01}$$

- There can be internal fragmentation in case of paged memory.
If process requires 10 kB & page size is 4 kB, 2 kB is wasted.
- If page size = 4 kB
 - Max. internal frag = 4 kB - 1
 - Min. " " = 0
 - Avg. " " = 2 kB

There is no external fragmentation as any free frame can be allocated.

Code pages can be shared among processes.

TLB → Hardware cache
(Translation look aside
buffer)

TLB is fully associative cache.
The key of all of them can be compared
together.

Key | Value → Frame
no.

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