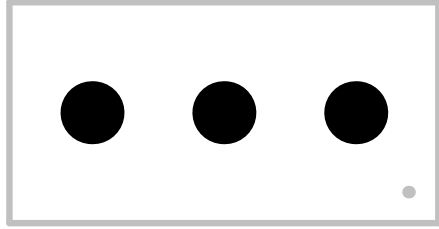


● FD2

INPUT PINS



C1



C3



C2



CON1

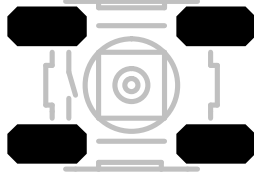
U1



C5



SW1



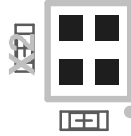
RESET BTN

P3

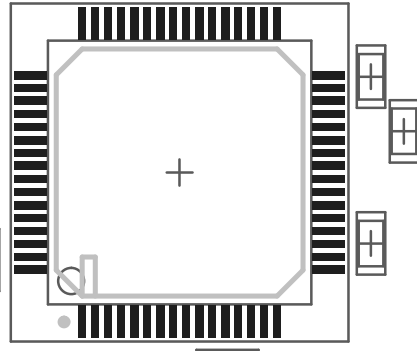


DI PINS

X1

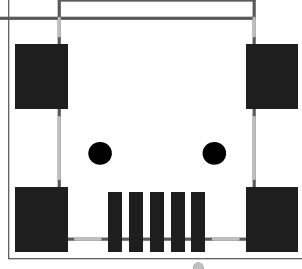


U3



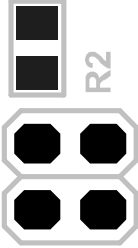
Zero to + X
MCU project

USB CONNECTOR



J1

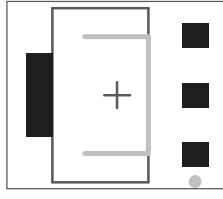
P6



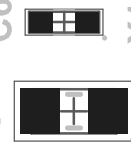
R1



U2



C4



C6



P4



R2



BOOT PINS

GENAREL I/O PINS



MAKER : Shaibal Mandal

MCUProject

Test : 0.1

● FD1

P1 USART PINS

P2

SPI PINS

P5

P7

TIMER PINS

P8

FD3



