

Shakeeb Arsalan Zacky Contribution List

- Prescale
 - Wrote the PreScale component and the PreScale testing vhdl wrapper
- SchedulerTesting
 - Wrote the wrapper for the Scheduler Testing
- ControlUnit
 - Wrote the ControlUnit component and the ControlUnit Testing vhdl wrapper
- Edited SMDB component
 - Made changes for proper instantiation in the SMDB component
- ToneGenerator
 - Attempted to create the ToneGenerator component, unfortunately due to running out of time I removed it from the project since it was not working properly.
- ToneGenerator Testbench
 - Wrote the ToneGenerator testbench to verify functionality (correct functionality was not achieved so the component is not used in our final project)
- Testing
 - Performed all code tests on the DE2-115 FPGA Board
- ASIP
 - Created the ASIP component and instantiated the components within
 - Created the top level ASIP structural diagram
- Crying
 - Equally split between Andrew and I. (I am pictured on the left working on ToneGenerator, and Andrew is on the right rewriting Scheduler)



- I believe all work was split equally and fairly between myself and Andrew Ha.