

## NATIONAL INSTITUTE OF TECHNOLOGY

## WARANGAL

## WARANGAL TELANGANA INDIA-506004

**International Conference on** 

Communications, Signal Processing and VLSI(IC2SV2019)

23<sup>rd</sup> - 24<sup>th</sup> October 2019



Day -1 (2	y -1 (23 <sup>rd</sup> Oct 2019, Wednesday)						Technical sessions(oral)					Poster session	
Registration 8:30-10:00	guration 3-10:45	th Tea 5-11:00	11:00 - 11:45	11:45 - 12:30	12:30 - 13:15	cyard Seminar omplex - 14:30	14:30 - 16:00		16:15 – 17:45		17:45	14:30 - 17:45	
			Key note - 1 Venue-Homi Bhabha Hall	Key note - 2 Venue-Homi Bhabha Hall	Key note - 3 Venue-Homi Bhabha Hall		Track 1 Homi Bhabha Hall	Track 2 Bose Hall	.00-16:15	Track 3 Homi Bhabha Hall	Track 4 Bose Hall	Track P1 Bose Hall Foyer	
			Communications, Signal	Sensor system approach -	Smart By Design -		Paper ID		16.	Paper	Paper ID Paper ID		
			Processing and VLSI - Defense	Sri Hanuman Sai V , Director,	Charudhattan Nagarajan, AE	ckyard compl 5 - 14:	015	014	ks	037	010	006	
egi :30	Inaugu 10:00-	High 0:45-]	Applications Perspective - Sri B	Austria Micro Systems	Director, Cadence Design	Back hall co 13:15	029	018	nac	067	019	056	
∞ ∞	In 10	Ξ	H V S Narayana Murthy, RCI		Systems	E page 113	032	027	g p	074	040	060	
			Director			lch	073	071	an	080	053	077	
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						, ,		088					
17:45 - 1	17:45 - 18:00		Tea										
18:45 - 20:00			Social cultural program Homi Bhabha Hall										
20:00 - 21:30			Banquet Seminar hall back yard										

Day -2 (24th Oct 2019, Thursday)							Poster session				
	09:30 - 10:30		11:00 - 11:30	11:30-12:00	12:00 - 13:30			14:30	- 16:00	14:30 - 17:00	
	Key note-4		Key note-5	Key note - 6	Track 5	Tack 6		Track 7	Track 8		
	Venue-Homi Bhabha hall		Venue	Venue-Homi	Homi	Bose Hall	×	Homi	Bose Hall	Track P2	
		¥	Homi Bha-	Bhabha hall	Bhabha hall		ard nple 0	Bhabha		Bose Hall Foyer	
st 00		Break 11.00	Bha Hall				complex 4:30	Hall			
Breakfast 7:30 – 9:00	Fusing radar and	Br 11.	Integrated Circuit	Dr. L Sateesh,	Paper ID		Bac all c - 14	Paper ID		Paper ID	
rea] 0 -	telecommunication- Prof Md	30-	Design for IoT	Senior Manager,	007	025	h 0	045	028	022	
B <sub>1</sub>	Zafar Ali khan,	Coffee 10.30-	Applications By Sri R.	AMD, Hyderabad	035	068	nch inar [3:3	048	050	038	
	IIT Hyderabad	0	K. Mudimela, Analog		036	070	m	051	084	043	
			Design Manager, Texas		046	082	Se	059	090	057	
			Instruments		079	091		076		083	
					087					081	
17:00 - 17:30		Valedictory function/Announcement of best papers and High Tea									
17:30 onwards		Local Tour (visit of Kakatiya dynasty monuments): Optional									