

National Institute of Technology, Warangal



International Conference on Communications, Signal Processing and VLSI (IC²SV 2019)

CONFERENCE THEME

Emerging Trends, Innovations, and Challenges in Communications, Signal Processing and VLSI

VENUE

Department of ECE National Institute of Technology Warangal, Warangal - 506004

PATRON

Prof. N. V. Ramana Rao, Director, NIT Warangal. India

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DATE

28th - 29th Sept.

2019

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ABOUT AND SCOPE OF THE CONFERENCE

The 2019 First International Conference on Communications, Signal Processing and VLSI (IC²SV 2019) will be held at National Institute of Technology Warangal, Warangal, Telangana, India. The Conference creates a network of various key players in the field of advances in computers and electronics around the globe. Speakers from various industries, institutions, and research laboratories will give the technical impact of their expertise. The conference also bring to meet new colleagues, collect new ideas and establish new cooperation between research groups and provide a platform for researchers from academic and industry to present their original work and exchange ideas, information, techniques and applications in the field of advances in computers and electronics. All selected papers will be published in Springer volume.

ABOUT THE INSTITUTE AND DEPARTMENT

National Institute of Technology, Warangal (Deemed University), formerly known as Regional Engineering College, was established *in 1959*. The Institute currently has thirteen academic departments and a few advanced research centers in various disciplines of engineering, pure sciences and management.

The Department of Electronics and Communication Engineering is one of the country's largest **ECE** departments among all NITs in India and one of the largest departments of the National Institute of Technology, Warangal (NITW). The department of ECE offers broad range of programs that include undergraduate (B.Tech) and post graduate (M.Tech) in Electronics Instrumentation, VLSI System design, Communication Systems and research (Ph.D) programs.

PRELIMINARY CALL FOR PAPERS

Original contributions from researchers describing their original, unpublished, research contribution which is not currently under review by another conference or journal and addressing state-of-the-art research are invited to share their work in all areas of Advances in Electronics and Communications. Topics of interest include, but are not limited to, the following areas:

Analog Communication
Cellular Communication
Digital Communication
Optical Communication
Satellite Communication
RF & Microwave
Photonics

Speech Processing
Video Processing
Signal processing
Image Processing
Information Processing

Embedded Systems
FPGA Based Systems
Internet of Things
Real Time Systems
Machine Learning
Robotics
Single Board Computer Systems

DSP Architecture
FPGA Architecture
Low Power VLSI Circuits
Device Compact Modeling
Device-Circuit co-design
Novel Memory elements
VLSI Interconnects

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Tutorial Co-chairs: Dr. P. Muralidhar, Dr. S. Anuradha,

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REGISTRATION DETAILS*

	Author	Participant
Student Members	INR 3000	INR 1500
International Student Members	USD 100	USD 50
Faculty Members	INR 4000	INR 2000
International Faculty Members	USD 200	USD 100
Industry Members	INR 6000	INR 3000
International Industry Members	USD 300	USD 150

(* Papers will be available in Springer volume online proceedings only if any one of the authors presents the paper)

CONTACT DETAILS

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URL

http://www.cms.nitw.ac.in/conference/ic2sv/

IMPORTANT DATES

Submission Deadline	30 th July 2019
Notification of Acceptance of Papers	15 th Aug 2019
Camera Ready Paper Submission	31 th Aug 2019
Author Registration	31 th Aug 2019