```
AREA LARGEST , CODE, READONLY
ENTRY
START
     MOV R5.#6
     LDR R1,=VALUE1
     LDR R2,[R1],#4
    LDR R4.[R1].#4
     BHI LOOP1 (For smallest BLI)
     MOV R2,R4
LOOP1
     CMP R5,#0
     BNE LOOP
     LDR R4,=RESULT
     STR R2,[R4]
B1 B B1
     DCD 0X4444444
     DCD 22,55,33,AA,CC,99
     AREA DATA2, DATA, READWRITE
RESULT DCD 0X0 END
```

```
ABEA DESCENDING, CODE, READONLY
ENTRY
START
MOV R8,44
LDR R2,=CVALUE
LDR R3,=CVALUE
LDR R3, EVALUE
LDR R3, EVALUE
LOR R3, EVALUE
STAR R1, [R2],44
STAR R1, [R2],44
STAR R1, [R2],44
STAR R1, [R2],44
STAR R1, [R3],44
LDR R2, R3,01
MOV R7,83
MOV R7,83
MOV R7,83
MOV R7,83
MOV R7,83
BGT LOOP2
START,11,144
LDR R2, [R1],144
LDR R3,[R1]
CMP R2,83
BGT LOOP2
STAR R2,[R1]
MOV R7,81
STAR R3,[R1]
MOV R7,81
STAR R3,[R1]
MOV R7,81
DOP2
STAR R3,[R1]
MOV R7,81
DOP3
STAR R3,[R1]
STAR R3,
```

```
DC motor

illinclude-cpc214.k.h-
void cbcl, vinelyoid;
void and _include-cpc214.k.h-
void cbcl, vinelyoid;
void and _include, vinelyoid;
void and _include,
vinelyoid;
(include _include,
vinelit] {
    clock_winel;
    for(inc)=4000000;+};
    ant_clock_winel;
    for(inc)=4000000;+};
    void cbcl_winelyoid;
    void cbcl_winelyoid;
    include_vinelyoid;
    inclu
```

```
SQUARE wave filinclude -dipC21xx.h>
void delay(void);
Int main (i
```

```
7 segment led

#include <1PC210X.h>
unsigned int delay;
unsigned int Switchcount-0;
unsigned int Switchcount-0;
unsigned int Switchcount-0;
unsigned int Delg [id=]el0x0300000,06.58,4F.66, 60, 70, 07, 7F, 6F,
777.C39,2F.797.1;
### Common Com
```