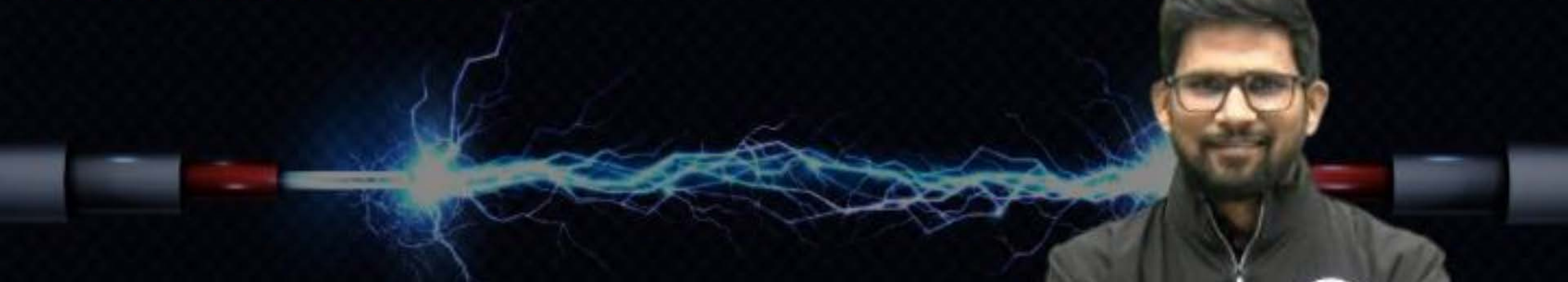


# COMPUTER SCIENCE & IT

## DIGITAL LOGIC




Lecture No. 07

Combinational Circuit



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# Recap of Previous Lecture

K-Map & Comparator ckt





# Topics to be Covered

Comparator ckt cont.

MUX.

A → 2 bit

0

1

2

3

$y_1(A > B)$

B → 2 bit

0

1

2

3

→ Total bits = 4 → Total Comb. =  $2^4 = 16$  Comb.

$$\checkmark N_1(A > B) = 0 + 1 + 2 + 3 = 6$$

$$N_2(A < B) = 0 + 1 + 2 + 3 = 6$$

$$N_3(A = B) = 4 = 2^2$$

$$(N_1 + N_2 + N_3) = 16$$

$$1 + 2 + 3 + \dots + m \\ = m(m+1)/2$$

A  
n-bit

0

1

⋮

$(2^n - 1)$

B  
n-bit

0

1

⋮

$(2^n - 1)$

→ Total bits =  $2n$  → Total Comb. =  $2^{2n}$

$$N_1(A > B) = N_2(A < B) = 0 + 1 + 2 + \dots + (2^n - 1)$$

$$N_3(A = B) = 2^n$$

$$\checkmark N_1 + N_2 + N_3 = 2^{2n}$$

•	A (2 bit)	B (3 bit)	→ Total = 5 bits → Total comb. = $2^5 = 32$
	0	0	$N_1(A > B) = 0 + 1 + 2 + 3 = 6$
	1	1	
	2	2	$N_2(A < B) = 4 + 5 + 6 + 7 = 22$
	3	3	$N_3(A = B) = 4 = 2^2$
		4	
		5	$(N_1 + N_2 + N_3) = 32$
		6	
		7	



A ( $n_1$  bit)    B ( $n_2$  bit)  $\rightarrow (n_1 + n_2)$  bits  
 Total comb. =  $2^{n_1 + n_2}$

Case-I if ( $n_1 < n_2$ )

A	B
$n_1$ bit	$n_2$ bit
0	0
1	1
⋮	⋮
$(2^{n_1} - 1)$	⋮
	⋮
	$(2^{n_2} - 1)$

$$N_1(A > B) = 0 + 1 + \dots + (2^{n_1} - 1)$$

$$N_3(A = B) = 2^{n_1}$$

$$N_2(A < B) = \text{Total comb} - (N_1 + N_3)$$

Case-II  $\rightarrow$  if ( $n_1 > n_2$ )

$$N_2(A < B) = 0 + 1 + 2 + \dots + (2^{n_2} - 1)$$

$$N_3(A = B) = 2^{n_2}$$

$$N_1(A > B) = \text{Total comb} - (N_2 + N_3)$$

$$= 2^{n_1 + n_2} - (N_2 + N_3)$$



## [ Question ]

We have two number A and B both A and B are 2-bit numbers then in how many combination  $A > B$ .

(a) 5

☒ (b) 6

(c) 7

(d) 8

$$N_1(A > B) = 0 + 1 + 2 + 3 = 6$$



# [ Question ]

We have two 4-bit number A and B then number of combinations in which  $A < B$  \_\_\_\_\_

Number of combinations in which  $A = B$  16.

$$N_2(A < B) = 0 + 1 + 2 + \dots + 15 = \frac{15(16)}{2} = 120$$

$$N_3(A = B) = 2^4 = 16$$

$$\text{Total comb} = 2^{4+4} = 2^8 = 256$$

- If  $Y_1$  represents logical 0/1 for  $A > B$  then no. of terms in its POS expression will be 136.

$$N_1(A > B) = 0 + 1 + 2 + 3 + \dots + 15 = 120$$

$$Y_1(A > B) \Rightarrow \text{SOP terms} = 120$$

$$\text{POS terms} = 136$$



$A \rightarrow 3 \text{ bit}$

$B \rightarrow 6 \text{ bit}$

No. of Comb. where  $B > A$  476.

$$\text{Total bits} = 9 \rightarrow \text{Total Comb} = 2^9 = 512$$

$$N_1(A > B) = 0 + 1 + 2 + \dots + 7 = \frac{7(8)}{2} = 28$$

$$N_3(A = B) = 2^3 = 8$$

$$\begin{aligned} N_2(A < B) &= \text{Total Comb.} - (N_1 + N_3) \\ &= 512 - 28 - 8 = 476 \end{aligned}$$

•  $A \rightarrow 4 \text{ bit}$

$B \rightarrow 7 \text{ bit}$

$$\longrightarrow \text{Total Comb.} = 2^{11} = 2048 \text{ Comb.}$$

$$N_1(A > B) = \underline{120}$$

$$N_1(A > B) = 0 + 1 + 2 + 3 + \dots + 15 = 120$$

$$N_2(A < B) = \underline{1912}$$

$$N_3(A = B) = 2^4 = 16$$

$$N_2(A < B) = 2048 - 120 - 16 = 1912$$



- $A \rightarrow 6 \text{ bits}$

$B \rightarrow 2 \text{ bits}$

$$N_1(A > B) = \underline{246}.$$

$$N_2(A < B) = 0 + 1 + 2 + 3 = 6$$

$$N_3(A = B) = 2^2 = 4$$

$$N_1(A > B) = 2^8 - 6 - 4 = 246$$

## [ Question ]

Design a combinational circuit having 3-input and 1-output line.  
The output is high when majority of input lines are at logic '0'.

H.W.



## [ Question ]

Design a combinational circuit where input is 3-bit input and output is square of input number. ✓

H.W.

## [ Question ]

Design a combinational circuit where input is BCD code and output is '1' when input is divisible by 2.

H.W.



# Question

A logic circuit implements the Boolean function :

$$f(A, B, C) = A\bar{B} + \bar{A}\bar{B}\bar{C} = \sum(0, 4, 5) + d\sum(2, 6)$$

It is found that the input combination  $B = 1$  and  $C = 0$  can never occur. Then the simplified output  $f(A, B, C)$  will be

(a)  $\bar{B}\bar{C} + \bar{A}\bar{C}$

(b)  $(A + \bar{C})(\bar{B} + \bar{C}) = \bar{C} + A\bar{B}$

(c)  $\bar{A}\bar{B} + \bar{B}\bar{C}$

(d) None of these

			B	C
			0	1
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# [ MUX ]

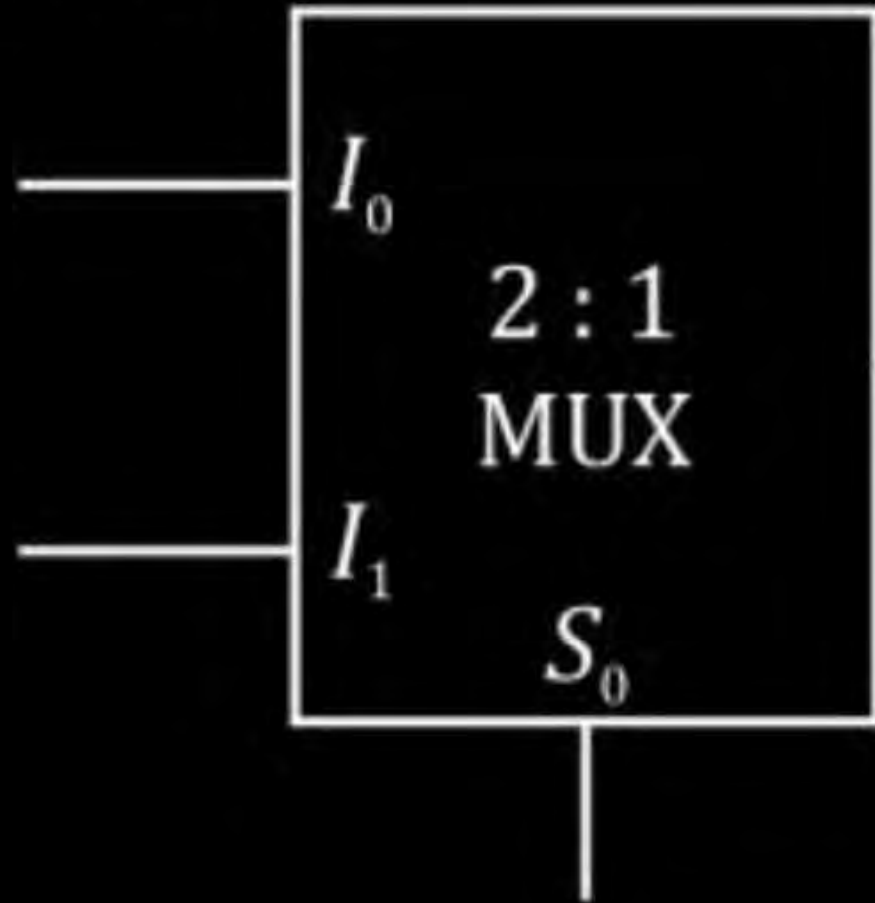


- What is MUX?

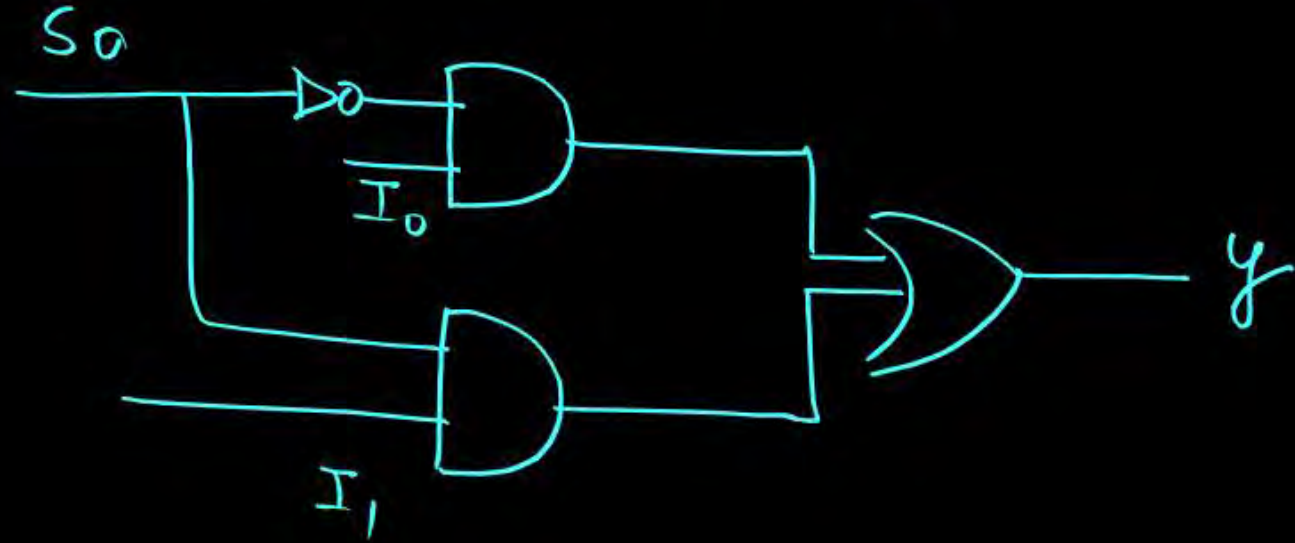
MUX is a comb. CKt having many i/p<sub>s</sub> and one o/p line and on the basis of select i/p line, one of the i/p line is transferred to the o/p line.



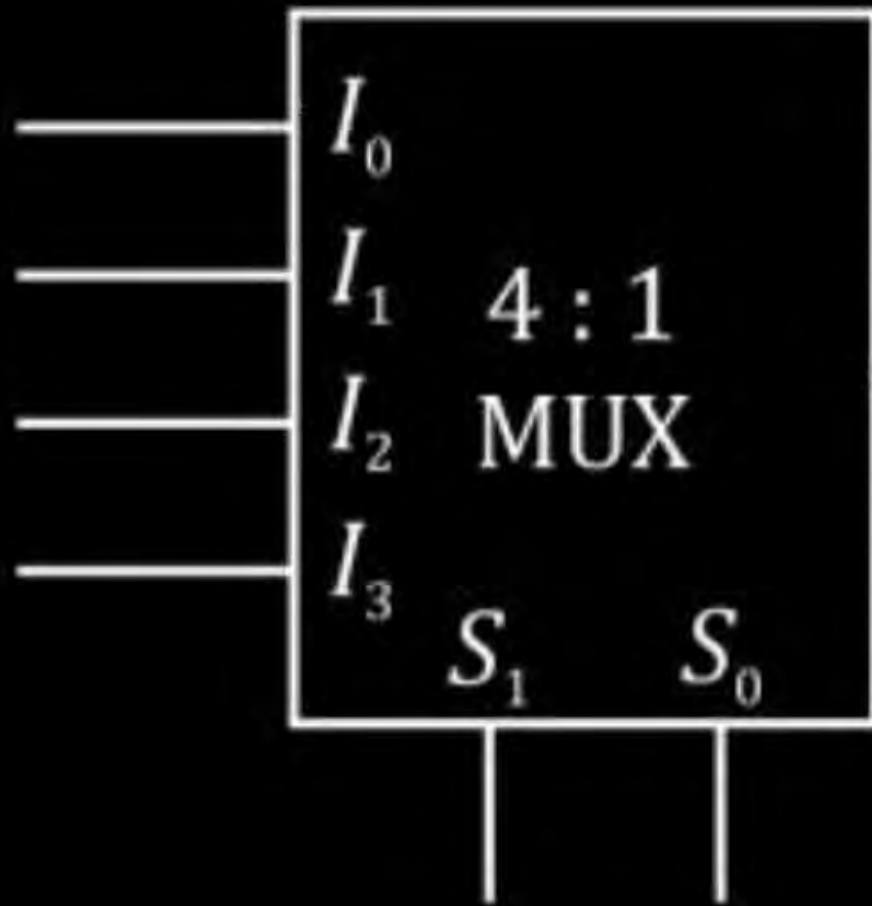
- 2 : 1 MUX



$$y = \overline{S_0} \cdot I_0 + S_0 \cdot I_1$$



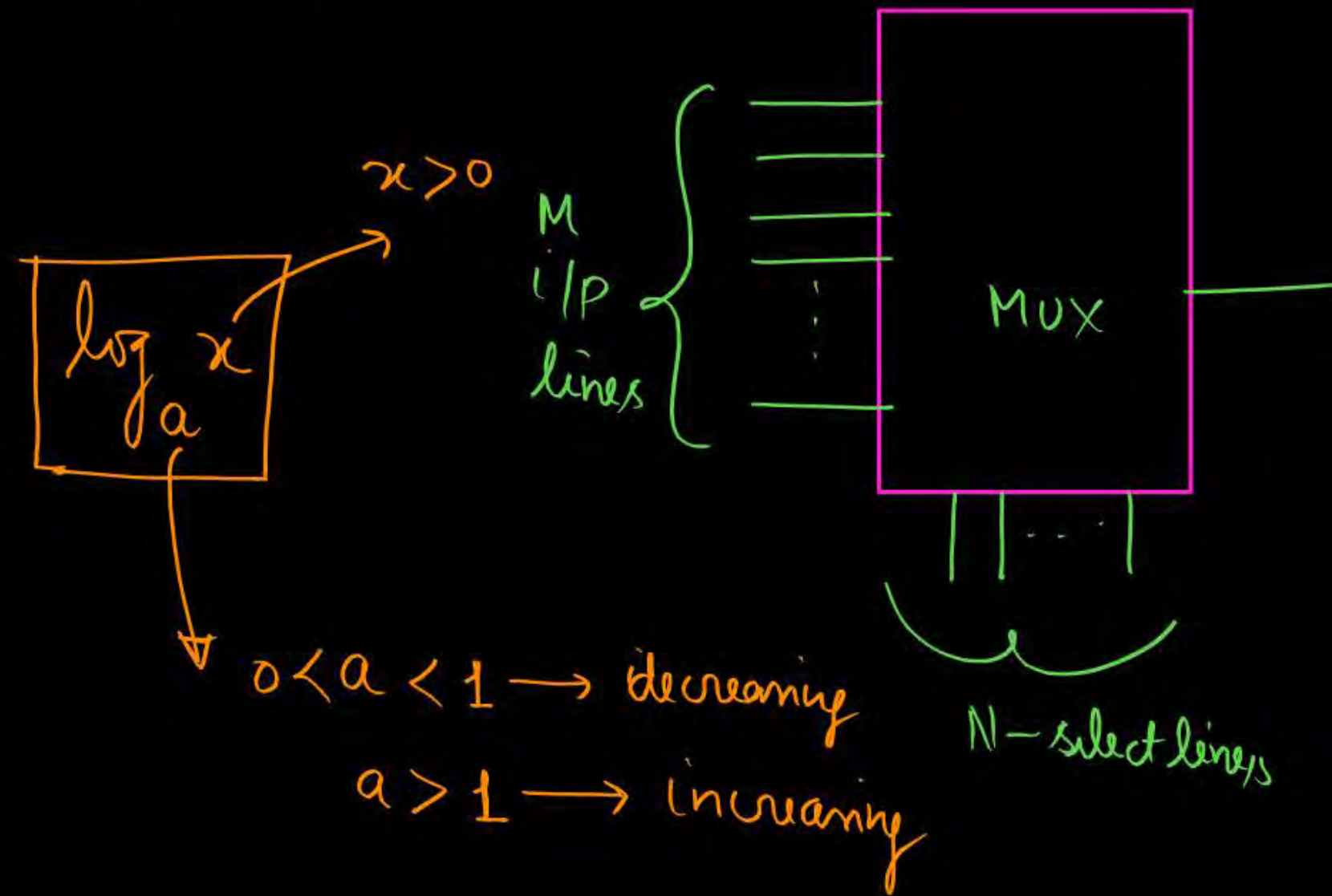
- 4 : 1 MUX



$$y = \overline{S_1} \cdot \overline{S_0} \cdot I_0 + \overline{S_1} \cdot S_0 \cdot I_1 + S_1 \cdot \overline{S_0} \cdot I_2 + S_1 \cdot S_0 \cdot I_3$$

$$S_1 = 1, S_0 = 0 \longrightarrow y = I_2$$

- Relation between number of input lines  $M$  and number of select lines  $N$ :



For proper operation

$$2^N \geq M$$

$$(\log_2 2^N \geq \log_2 M)$$

$$N \geq \log_2 M$$

$$\log_{0.5} 2^N \leq \log_{0.5} M$$





## 2 Minute Summary

→ Comparator CKt  
→ MUX.

**Thank you**

**GW**  
*Soldiers !*

