

COMPUTER SCIENCE & IT

DIGITAL LOGIC



Lecture No. 07

Combinational Circuit



By- Chandan Gupta Sir



Recap of Previous Lecture

K-Map & Comparator ckt





Topics to be Covered

Comparator ckt cont.

MUX

$A \rightarrow 2\text{ bit}$

0	0
1	1
2	2
3	3

$y_1(A > B)$

$B \rightarrow 2\text{ bit} \longrightarrow \text{Total bits} = 4 \longrightarrow \text{Total comb.} = 2^4 = 16 \text{ comb.}$

$$\checkmark N_1(A > B) = 0 + 1 + 2 + 3 = 6$$

$$N_2(A < B) = 0 + 1 + 2 + 3 = 6$$

$$N_3(A = B) = 4 = 2^2$$

$$(N_1 + N_2 + N_3) = 16 \quad | + 2 + 3 + \dots + m \\ = m(m+1)/2$$

$A \quad B \longrightarrow \text{Total bits} = 2n \rightarrow \text{Total comb.} = 2^{2n}$

$n\text{-bit}$	$n\text{-bit}$
0	0
1	1
:	:
$(2^n - 1)$	$(2^n - 1)$

$$N_1(A > B) = N_2(A < B) = 0 + 1 + 2 + \dots + (2^n - 1)$$

$$N_3(A = B) = 2^n$$

$$\checkmark N_1 + N_2 + N_3 = 2^{2n}$$

A (2 bit)	B (3 bit)	\rightarrow Total = 5 bits \rightarrow Total comb. = $2^5 = 32$
0	0	$N_1(A > B) = 0 + 1 + 2 + 3 = 6$
1	1	
2	2	$N_2(A < B) = 4 + 5 + 6 + 7 = 22$
3	3	$N_3(A = B) = 4 = 2^2$
4		$(N_1 + N_2 + N_3) = 32$
5		
6		
7		



$A(n_1 \text{ bit}) \quad B(n_2 \text{ bit}) \rightarrow (n_1 + n_2) \text{ bits}$

$$\text{Total comb} = 2^{n_1+n_2}$$

Case-I if ($n_1 < n_2$)

A $n_1 \text{ bit}$	B $n_2 \text{ bit}$
0	0
1	1
⋮	⋮
$(2^{n_1}-1)$	$(2^{n_2}-1)$

$$N_1(A > B) = 0 + 1 + \dots + (2^{n_1} - 1)$$

$$N_3(A = B) = 2^{n_1}$$

$$N_2(A < B) = \text{Total comb} - (N_1 + N_3)$$

$$(2^{n_2}-1)$$

Case-II → if ($n_1 > n_2$)

$$N_2(A < B) = 0 + 1 + 2 + \dots + (2^{n_2} - 1)$$

$$N_3(A = B) = 2^{n_2}$$

$$N_1(A > B) = \text{Total comb} - (N_2 + N_3)$$
$$= 2^{n_1+n_2} - (N_2 + N_3)$$



[Question]

We have two number A and B both A and B are 2-bit numbers then in how many combination $A > B$.

- (a) 5
- (b) 6
- (c) 7
- (d) 8

$$N_1(A > B) = 0 + 1 + 2 + 3 = 6$$

[Question]

We have two 4-bit number A and B then number of combinations in which $A < B$ _____

Number of combinations in which $A = B$ 16.

$$N_2(A < B) = 0 + 1 + 2 + \dots + 15 = \frac{15(16)}{2} = 120,$$

$$N_3(A = B) = 2^4 = 16$$

- If y_1 represents logical op for $A > B$ then no. of terms in its POS expression will be 136.

$$N_1(A > B) = 0 + 1 + 2 + 3 + \dots + 15 = 120$$

$$y_1(A > B) \Rightarrow \text{SOP terms} = 120$$

$$\text{POS terms} = 136$$

$$\begin{aligned} \text{Total comb} &= 2^{4+4} = 2^8 \\ &= 256 \end{aligned}$$

A → 3 bit

B → 6 bit

No. of Comb. where $B > A \xrightarrow{476}$.

Total bits = 9 → Total Comb = $2^9 = 512$

$$N_1(A > B) = 0 + 1 + 2 + \dots + 7 = \frac{7(8)}{2} = 28$$

$$N_3(A = B) = 2^3 = 8$$

$$\begin{aligned} N_2(A < B) &= \text{Total Comb.} - (N_1 + N_3) \\ &= 512 - 28 - 8 = 476 \end{aligned}$$

A → 4 bit
B → 7 bit

→ Total Comb. = $2^{11} = 2048$ Comb.

$$N_1(A > B) \xrightarrow{120}$$

$$N_1(A > B) = 0 + 1 + 2 + 3 + \dots + 15 = 120$$

$$N_2(A < B) \xrightarrow{1912}$$

$$N_3(A = B) = 2^4 = 16$$

$$N_2(A < B) = 2048 - 120 - 16 = 1912$$



• $A \rightarrow 6$ bits

$B \rightarrow 2$ bits

$$N_1(A > B) = \underline{2^4 6}.$$

$$N_2(A < B) = 0 + 1 + 2 + 3 = 6$$

$$N_3(A = B) = 2^2 = 4$$

$$N_1(A > B) = 2^8 - 6 - 4 = 246$$

[Question]

Design a combinational circuit having 3-input and 1-output line.
The output is high when majority of input lines are at logic '0'.

H.W.

[Question]

Design a combinational circuit where input is 3-bit input and output is square of input number. ✓

A yellow circle containing the letters "H.W.".

[Question]

Design a combinational circuit where input is BCD code and output is '1' when input is divisible by 2.



[Question]

A logic circuit implements the Boolean function :

$$f(A, B, C) = \underset{100}{AB} + \underset{000}{\bar{A}\bar{B}\bar{C}} = \sum(0, 4, 5) + d\sum(2, 6)$$

It is found that the input combination $B = 1$ and $C = 0$ can never occur. Then the simplified output $f(A, B, C)$ will be

(a) $\bar{B}\bar{C} + \bar{A}\bar{C}$

(b) $\checkmark (A + \bar{C})(\bar{B} + \bar{C}) = \bar{C} + A\bar{B}$

(c) $\bar{A}\bar{B} + \bar{B}\bar{C}$

(d) None of these

	B	C	
	0	1	$0 \rightarrow 2$
	1	1	$0 \rightarrow 6$
\bar{A}	1		
A	1	1	X

$= \bar{C} + A\bar{B} \checkmark$

don't care conditions

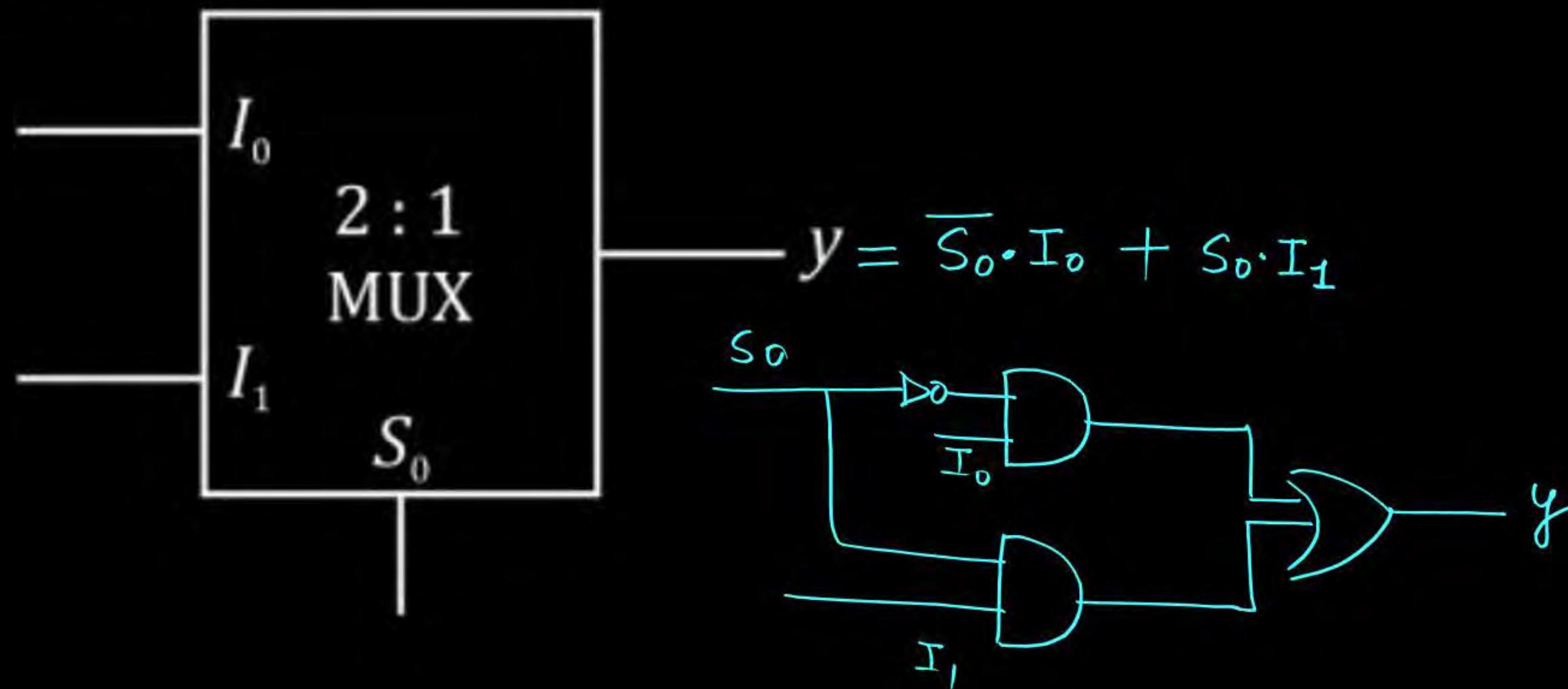
[MUX]



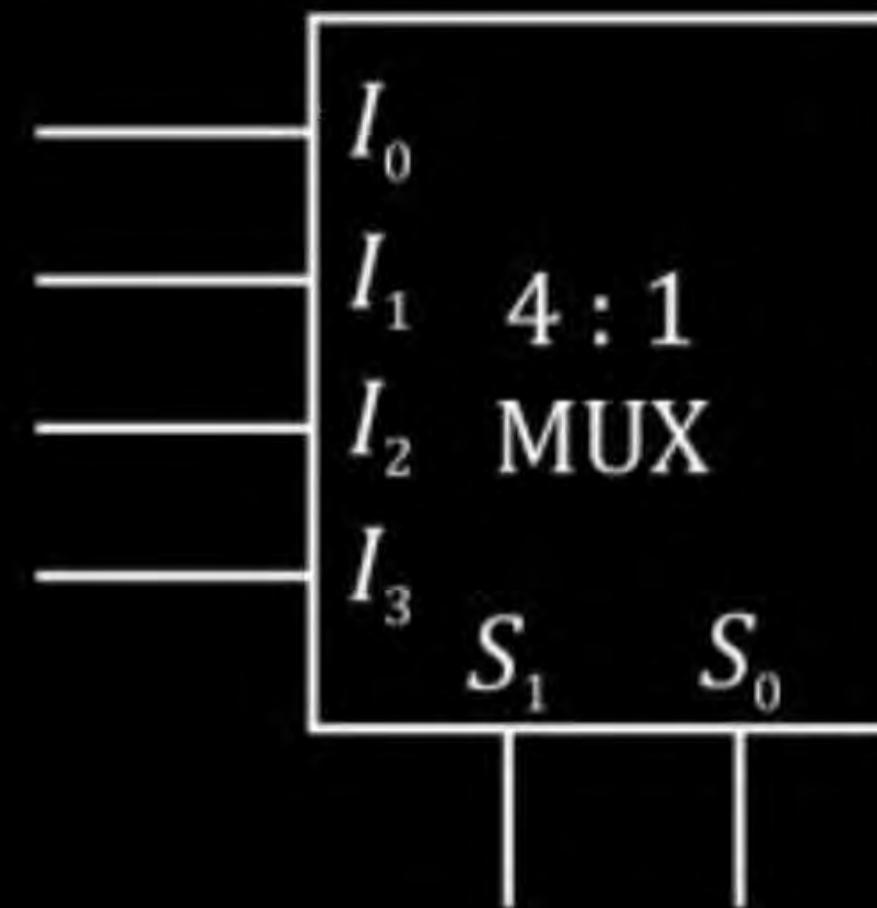
- What is MUX ?

MUX is a comb. ckt having many i/Ps and one o/P line and on the basis of select i/P line , one of the i/P line is transferred to the o/P line .

- 2 : 1 MUX

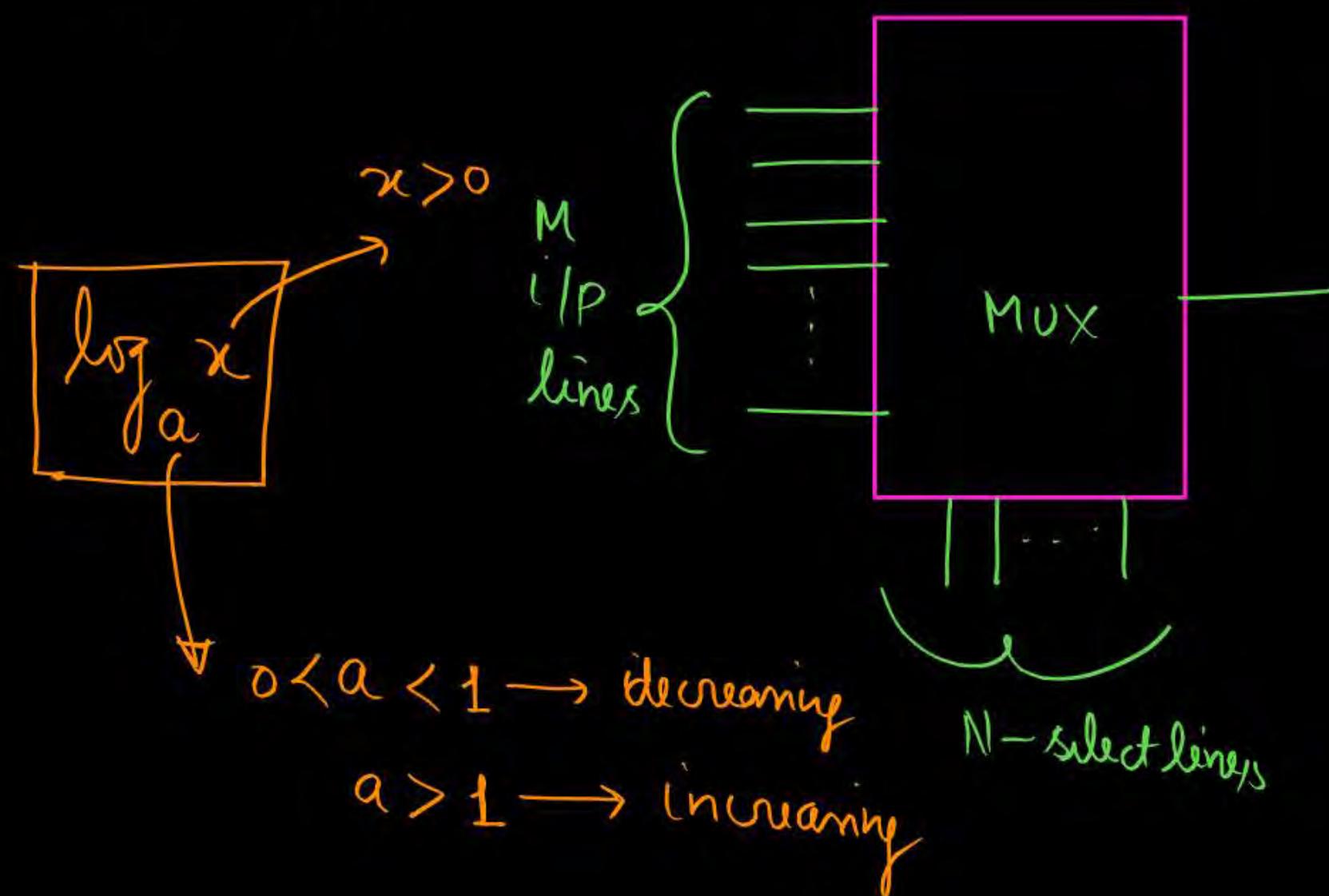


- 4 : 1 MUX



$$y = \overline{S_1} \cdot \overline{S_0} \cdot I_0 + \overline{S_1} \cdot S_0 \cdot I_1 + S_1 \cdot \overline{S_0} \cdot I_2 + S_1 \cdot S_0 \cdot I_3$$
$$S_1 = 1, S_0 = 0 \rightarrow y = I_2$$

- Relation between number of input lines M and number of select lines N :



For proper operation

$$\cancel{2^N} \geq \cancel{M}$$

$$(\log_2 2^N \geq \log_2 M)$$

$$N \geq \log_2 M$$

$$\log_{0.5} 2^N \leq \log_{0.5} M$$

2 Minute Summary

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→ Comparator CKt
→ MUX

Thank you
GW
Soldiers !

