

Shanmukha Poorna Chand Adapaka

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🐙 Github 📞 +91 9398746477 🔗 Professional Profile

SUMMMARY

Seeking a challenging position as a Software Developer to leverage my expertise in Java, web technologies, and full-stack development. Aiming to contribute to innovative projects at the intersection of software engineering and practical problem-solving in areas such as application development, system optimization, and scalable web solutions.

PROFESSIONAL EXPERIENCE

Java Full Stack Trainee

Jan 2025 – present

Vijayawada, India

- Developed a responsive Amazon clone using front-end technologies, ensuring an intuitive and user-friendly shopping experience across devices.
- Optimized workflows and automated routine tasks, reducing administrative workload by 35%.
- Utilized Java, SQL, HTML, CSS, JavaScript, and Bootstrap to build a scalable and efficient solution.
- Gained expertise in SDLC methodologies and full-stack development, improving system efficiency and user experience.

EDUCATION

Vijetha English Medium High School

SSC

Jun 2017 – Apr 2018

Vijayawada, India

DVR & Dr. HS MIC College of Technology

Diploma in Electronics and Communication Engineering

Jun 2018 – Sep 2021

Kanchikacherla, India

Potti Sriramulu Chalavadi Mallikarjunarao College of Engineering & Technology

Btech in Electronics and Communication Engineering

Nov 2021 – May 2024

Vijawada, India

SKILLS

- | | | | |
|-------------|------------|--------|---------------|
| • Java | • HTML | • CSS | • Java Script |
| • Bootstrap | • Node.js | • JDBC | • SQL |
| • GitHub | • MS Excel | | |

LANGUAGES

Telugu • Hindi • English

PROJECTS

Amazon Clone [🔗](#)

Tools: VS code | Skills: HTML, CSS, Javascript

Developed a responsive Amazon Clone with key e-commerce features, interactive UI, and advanced CSS and JavaScript for a seamless user experience.

To-Do List Application

Tools: VS code | Skills: HTML, CSS, Javascript

Developed a web-based to-do list application using HTML, CSS, and JavaScript, incorporating features for task addition, editing, deletion, and completion tracking. Integrated local storage to ensure data persistence, allowing users to retain tasks even after closing the browser. Designed a responsive and user-friendly interface to enhance task management efficiency and improve user experience.

Efficient Approximate Adders with Minimal Error for FPGA Implementation

Tools: Xilinx Vivado



Engineered FPGA-optimized approximate adders (LEADx and APEX) to enhance efficiency, minimizing power, area, and error while optimizing hardware utilization.

Configuring EIGRP



Tools: Cisco Packet Tracer

Designed and implemented an EIGRP topology for 13 routers, optimizing reliability and convergence. Configured routing tables, simulated performance in Cisco Packet Tracer, and applied troubleshooting to enhance network efficiency.

CERTIFICATES

Web Development  • Problem solving(Basic)  • CSS(Basic)  • Introduction to Java  •

Java(Basic)  • CCNAv7: Enterprise Networking, Security, and Automation  •

CCNAv7: Switching, Routing, and Wireless Essentials  • CCNAv7: Introduction to Networks  •

IT Essentials 

DECLARATION

I hereby declare that all the above mentioned information is true and genuine to the best of my knowledge and belief.

Shanmukha Poorna Chand Adapaka