Sessional II Total points	18/20	?
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An arithmetic circuit that subtracts one binary digit from another considering a borrow is called a	-	0/2
Full adder		
Full Subtractor		
Half-Adder		×
Half-Subtractor		
Correct answer		
Half-Subtractor		

✓	An arithmetic circuit that adds only two binary digits is called a	2/2
0	half subtractor	
•	half adder	/
0	adder	
0	parallel adder	
~	Which of the following logic circuit accepts two binary digits on inputs, and produces two binary digits, a sum bit and a carry bit on its outputs?	2/2
0	Serial adder	
0	Parallel adder.	
0	Full Adder	
•	Half-adder .	<u>'</u>
~	A full adder can be realized using	2/2
•	two half-adder, one OR gate	/
0	two half-adder , two OR gates	
0	two half-adder , one AND gates	
0	one half-adder , two OR gates	

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✓ In digital systems subtraction is perfor	med 2/	2
Using half-adders		
Using half-subtractors		
Using adder with 1's complement represent	tation of negative numbers	
By None of the above.		
To secure a higher speed of addition, v preferred solution?	which of the following is the 2/3	2
Serial adder		
Parallel Adder		
Full-adder		
Adder with a look-ahead-carry	✓	
✓ How many inputs and outputs does a f	full-adder have? 2/	2
Two inputs, Three outputs		
Two inputs, two outputs		
Three inputs, two outputs	✓	
Two inputs, one output		

 A full-adder and a full-subtractor can be realized by using at lea NAND gates. 	st 2/2
O 7	
5	
O 10	
9	✓
✓ The difference output in a full – subtractor is the same as the	2/2
Difference output of a half-subtractor	
carry output of a full adder	
sum output of a full-adder	✓
sum output of a half-adder	
✓ How many full-adders are required to construct an m-bit paralle	el adder? 2/2
m/2	
m-1	
m	✓
m+1	

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