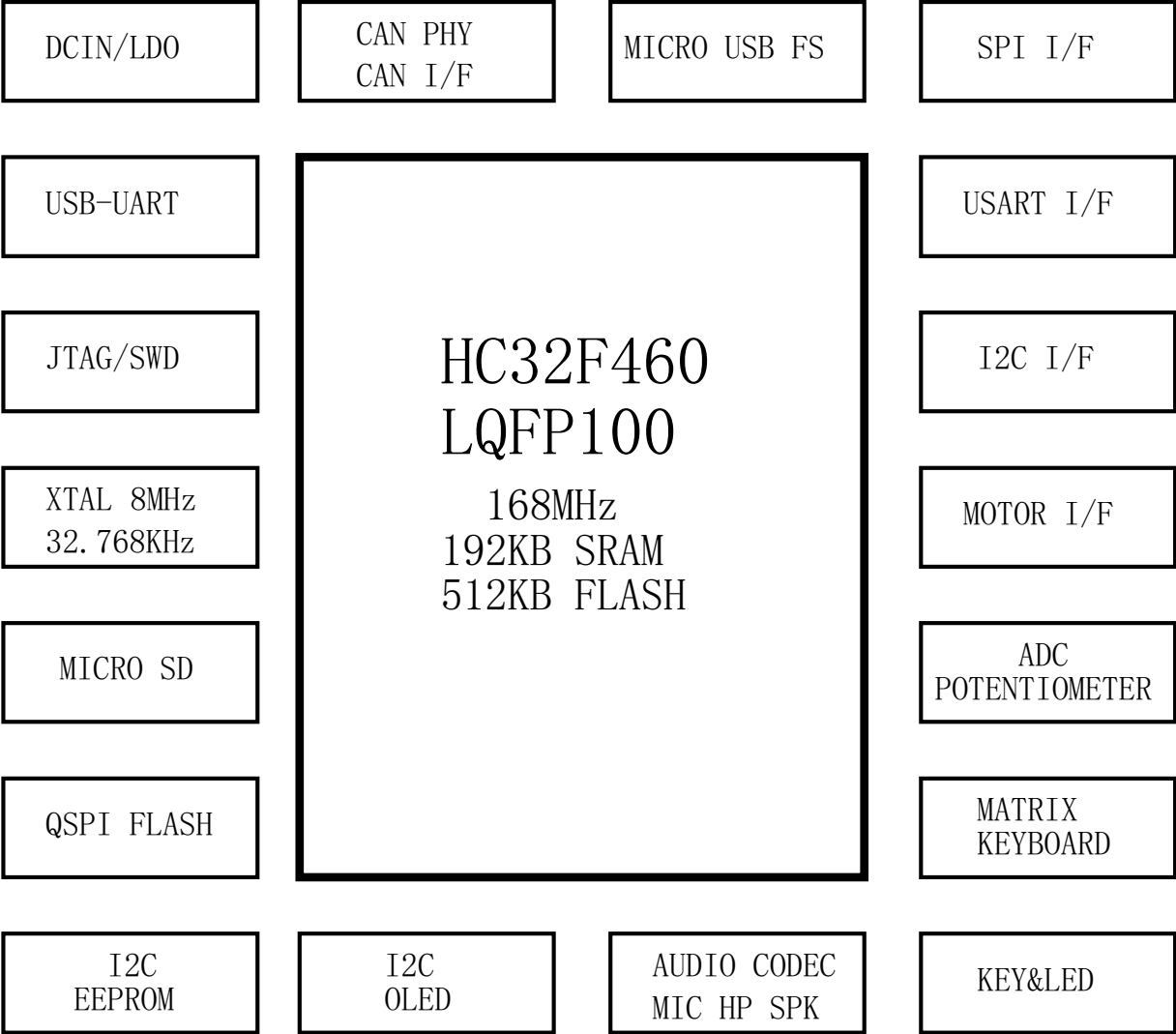


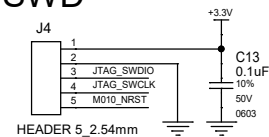
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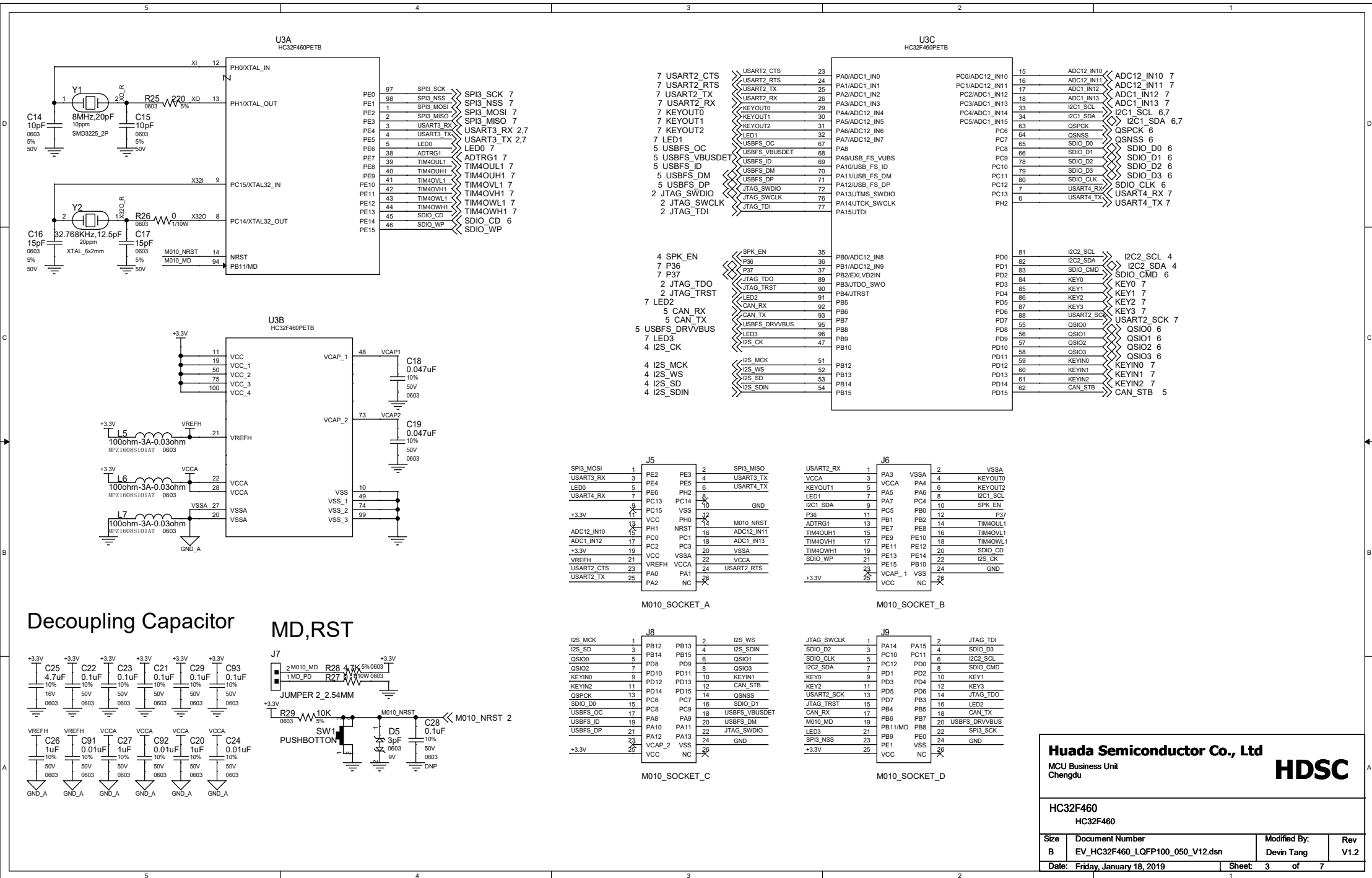
Revision History

Rev	Date	Description
V1.0	08/21/2018	- New design.
V1.1	12/12/2018	- Changed UART connector to match SmartCard. - Added R143~R147 to I2S signals. - Added R143 as pull-up resistor to SPI NSS. - Changed matrix keyboard output pin,uart2_0k pin,and LED1 control pin. - Changed CAN and I2C IO pin.
V1.2	01/11/2019	- Update HC32F460 pin definition. - C28 is now DNP.



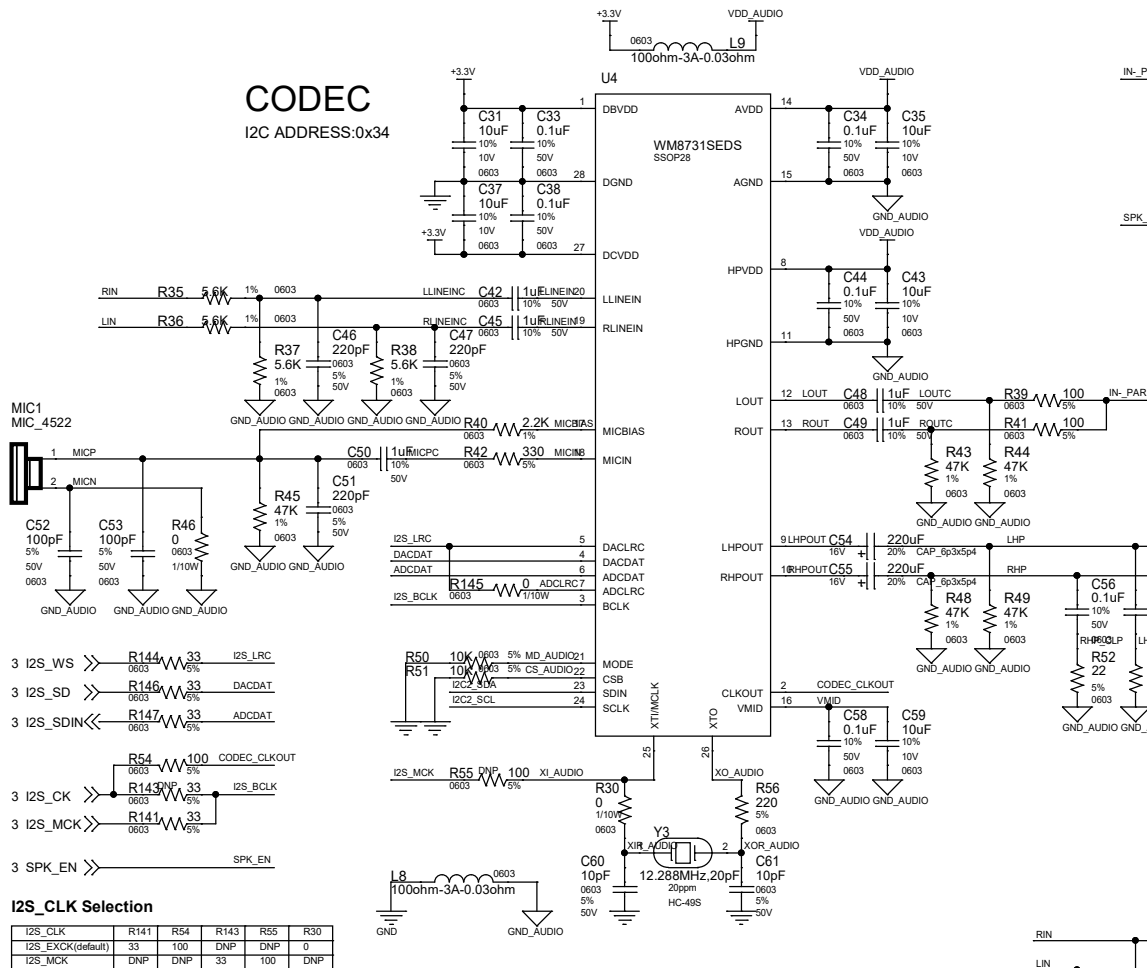


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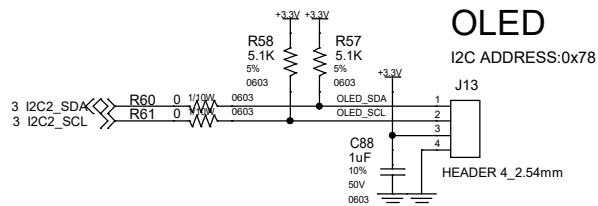
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OLED

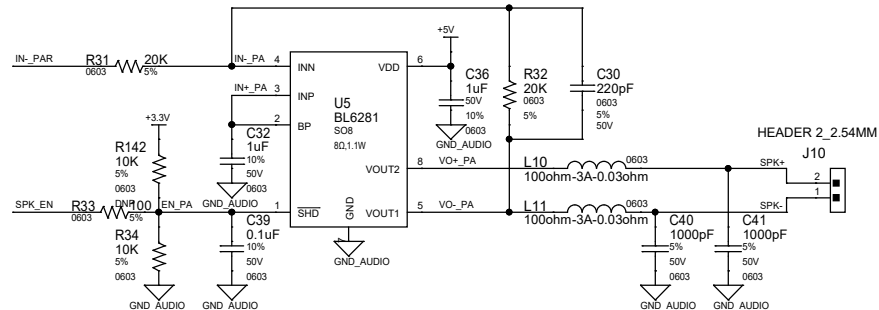
I2C ADDRESS:0x78



HEADPHONE

JACK_AUDIO

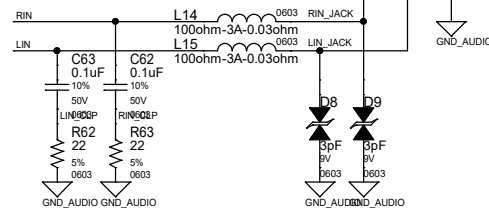
J11



LINEIN

JACK_AUDIO

J12



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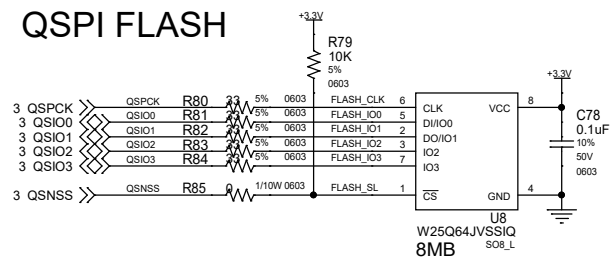
HDSC

HC32F460

AUDIO,OLED

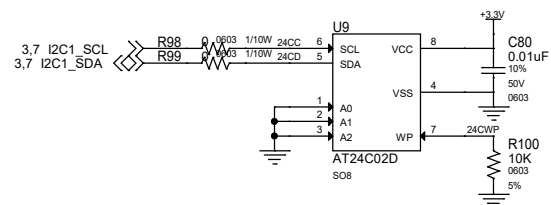
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QSPI FLASH

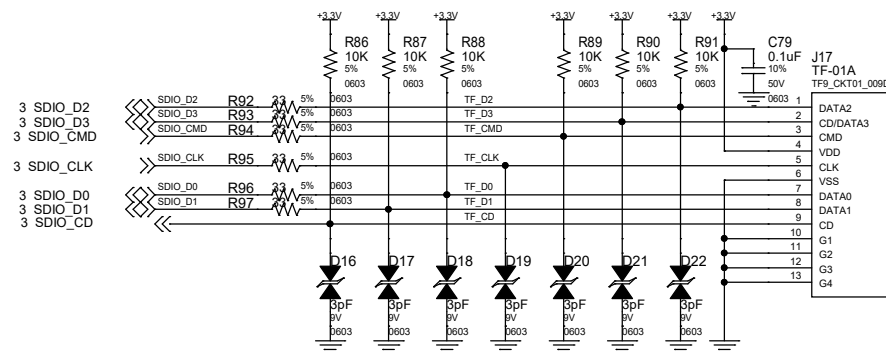


EEPROM

I2C ADDRESS:0xA0



TF CARD



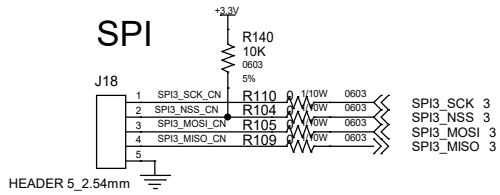
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HDSC

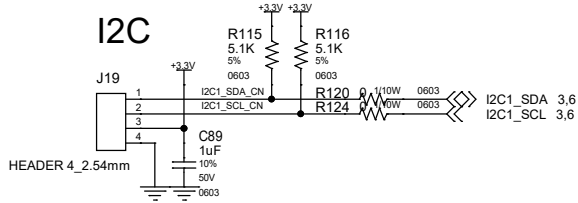
HC32F460
QSPI FLASH,MICRO SD,EEPROM

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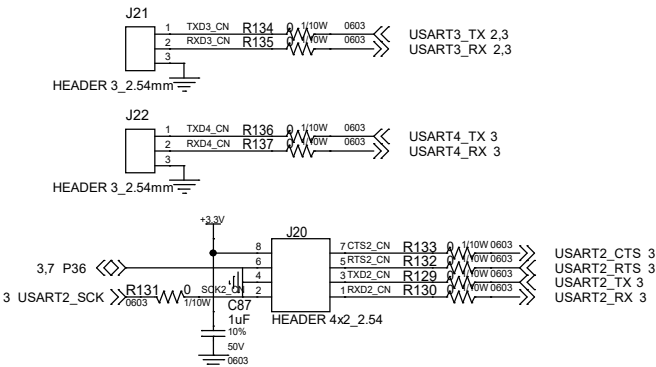
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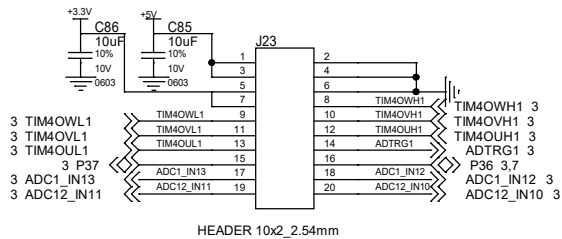
I2C



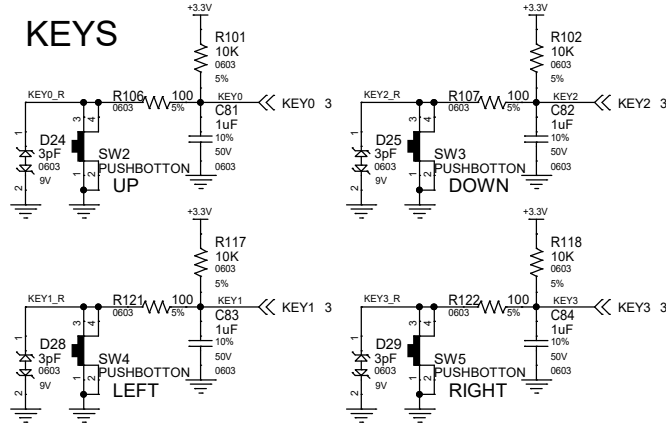
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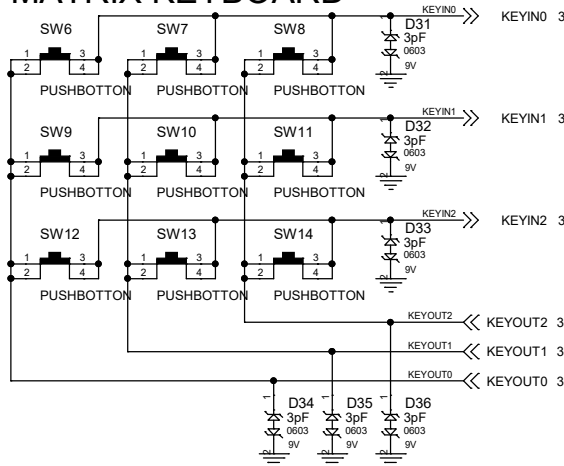
MOTOR CONTROL/ADC



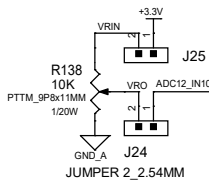
KEYS



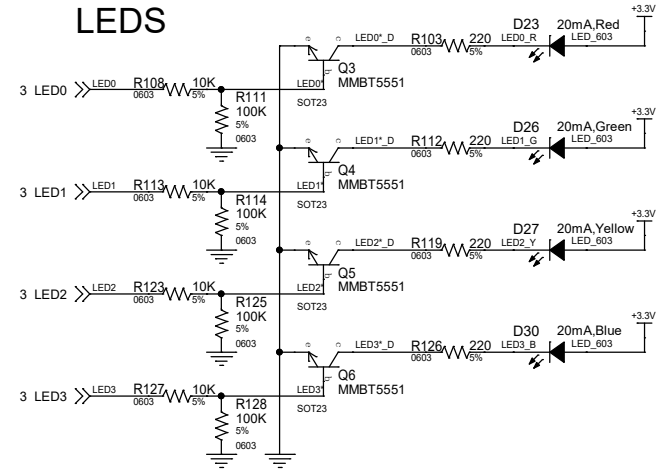
MATRIX KEYBOARD



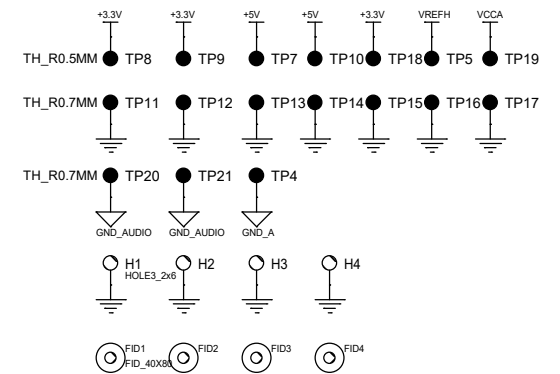
POTENTIOMETER



LEDS



TP/FID/FAB HOLES



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HDSC

HC32F460
USART,SPI,I2C,KEY,MOTOR,GPIO

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