National University of Singapore

CS2106 Operating System

Second Half Summary Notes

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1 I/O System

Definition 1.1. I/O Devices

- Communication devices: Input only (mouse, keyboard); output only (display); Input/output (network card)
- Storage devices: Input/output (disk, tape); Input only (CD-ROM)

Definition 1.2. Main tasks of I/O System

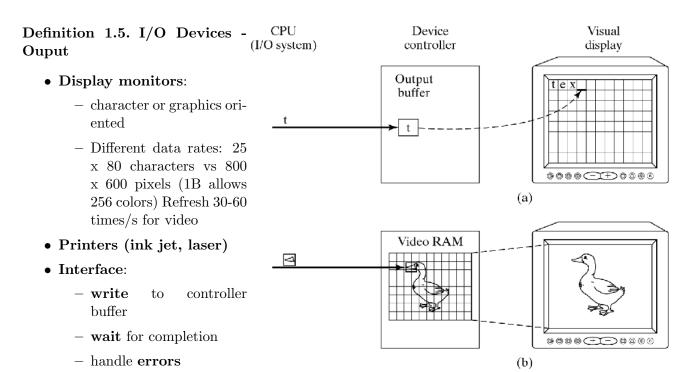
- Present **logical** (abstract) view of devices (**hide**: details of hardware interface and error handling)
- Facilitate efficient use: overlap CPU and I/O
- Support **sharing** of devices: protection when device is shared (disk), scheduling when exclusive access needed (printer)

Definition 1.3. Block-Oriented Device Interface

- Description: direct access, contiguous blocks, usually fixed block size
- Operation:
 - **Open**: verify device is ready, prepare it for access
 - Read: Copy a block into main memory
 - Write: Copy a portion of main memory to a block
 - Close: Release the device
 - *Note: these are lower level than those of the FS
- **Application**: Used by File System and Virtual Memory System; Applications typically go through the File System

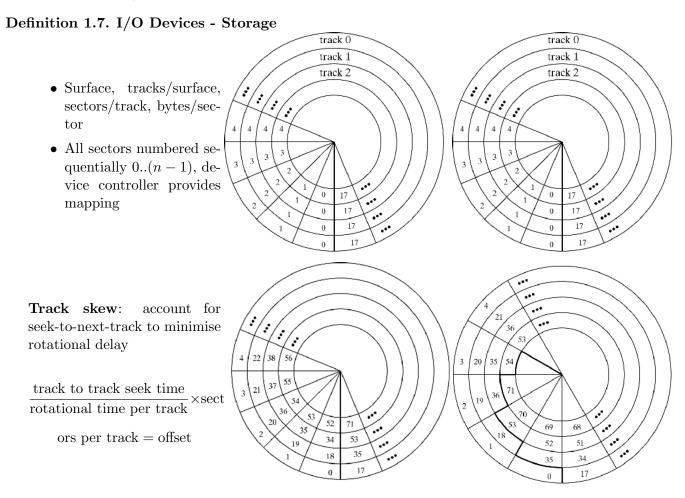
Definition 1.4. Stream-Oriented Device Interface

- Description: character-oriented, sequential access
- Operation:
 - Open: reserve exclusive access
 - Get: return next character of input stream
 - **Put**: append character to output stream
 - Close: release exclusive access
 - *Note: these too are different from those of the FS but some systems try to present a uniform view of files and devices



Definition 1.6. I/O Devices - Input Keyboards, pointing devices (mouse, trackball, joystick), scanners. **Interface**:

- device generates interrupt when data is ready
- read data from controller buffer
- low data rates, not time-critical



J Double-sided or multiple surfaces Read/write heads • Tracks with same diameter = cylinder37(55)• Sectors are numbered within cylinder consecu-22(140) 1₂₃₍₁₄₁₎ tively to minimise seek 88(106) 87(105) time 52(70)

Critical issue: data transfer rates of disks

- Sustained rate: continuous data delivery
- Peek rate: : transfer once read/write head is in place; depends on rotation speed and data density

Example 1.1. Transfer rate calculation: 7200 rpm, 100 sectors/track, 512 bytes/sector

• What is the **peak** transfer rate?

$$\frac{7200}{60} \times 100 \times 512$$
byte/s

• What is the **sustained** transfer rate? - Depends on file organization

Definition 1.8. I/O programming - access the I/O devices

- Polling (You with a broken ringer)
 - Consider a process that prints ABCDE-FGH on the printer: The OS then copies character by character onto the printers latch, and the printer prints it out.
 - 1. Copy the first character and advance the buffers pointer.
 - 2. Check that the printer is ready for the next character. If not, wait. This is called "busy-waiting" or "polling".
 - 3. Copy the next character. Repeat until buffer is empty.

copy_from_user(buffer, p, count);
for (i = 0; i < count; i++) {
 while (*printer_status_reg!= READY);
 *printer_data_register = p[i];
}
return_to_user();

Verinted Printed Printed Page

ABCD FEGH

Next ABCD FEGH

A

Issue: It takes perhaps 10ms to print a character. During this time, the CPU will be busy-waiting until the printer is done printing. On a 3.2 GHz processor this is equivalent to wasting 320,000,000 instructions!

- Interrupt-driven I/O (You with a fully functioning phone)
 - After the string is copied, the OS will send a character to the printer, then switch to a task.
 - When the printer is done, it will interrupt the CPU by asserting one of the interrupt request (IRQ) lines on the CPU.

Main Routine

copy_from_user(buffer, p, count);
enable_interrupts();
while (*printer_status_reg != READY);
*printer_data_register = p[0];
scheduler();

Interrupt Service Routine (ISR)

17(35)

if (count == 0) {
 unblock_user();
} else {
 *printer_data_register = p[i];
 count = count - 1;
 i = i + 1;
}
acknowledge_interrupt();
return_from_interrupt();

(a)

- Direct memory access (You have an answering machine)
 - Driver (CPU) operation to input sequence of bytes:

```
write_reg(mm_buf, m); // give parameters
write_reg(count, n);
write_reg(opcode, read); // start op
block to wait for interrupt;
```

- * Writing opcode triggers DMA controller
- * DMA controller issues interrupt after n chars in memory
- Cycle Stealing:
 - * DMA controller competes with CPU for memory access
 - * generally not a problem because: 1. Memory reference would have occurred anyway;
 - 2. CPU is frequently referencing data in registers or cache, bypassing main memory.

Definition 1.9. Device Management

- Disk Scheduling: Requests for different blocks arrive concurrently from different processes
- Minimize rotational delay: re-order requests to blocks on each track to access in one rotation
- Minimize seek time: Conflicting goals: Minimize total travel distance; Guarantee fairness

Algorithm 1.1. Device Management

- **FIFO**: requests are processed in the order of arrival: simple, fair, but inefficient
- **SSTF** (Shortest Seek Time First): most efficient but prone to starvation
 - always go to the track thats nearest to the current positions
- Scan (Elevator): fair, acceptable performance
 - maintain a direction of travel
 - always proceed to the nearest track in the current direction of travel
 - if there is no request in the current direction, reverse direction

