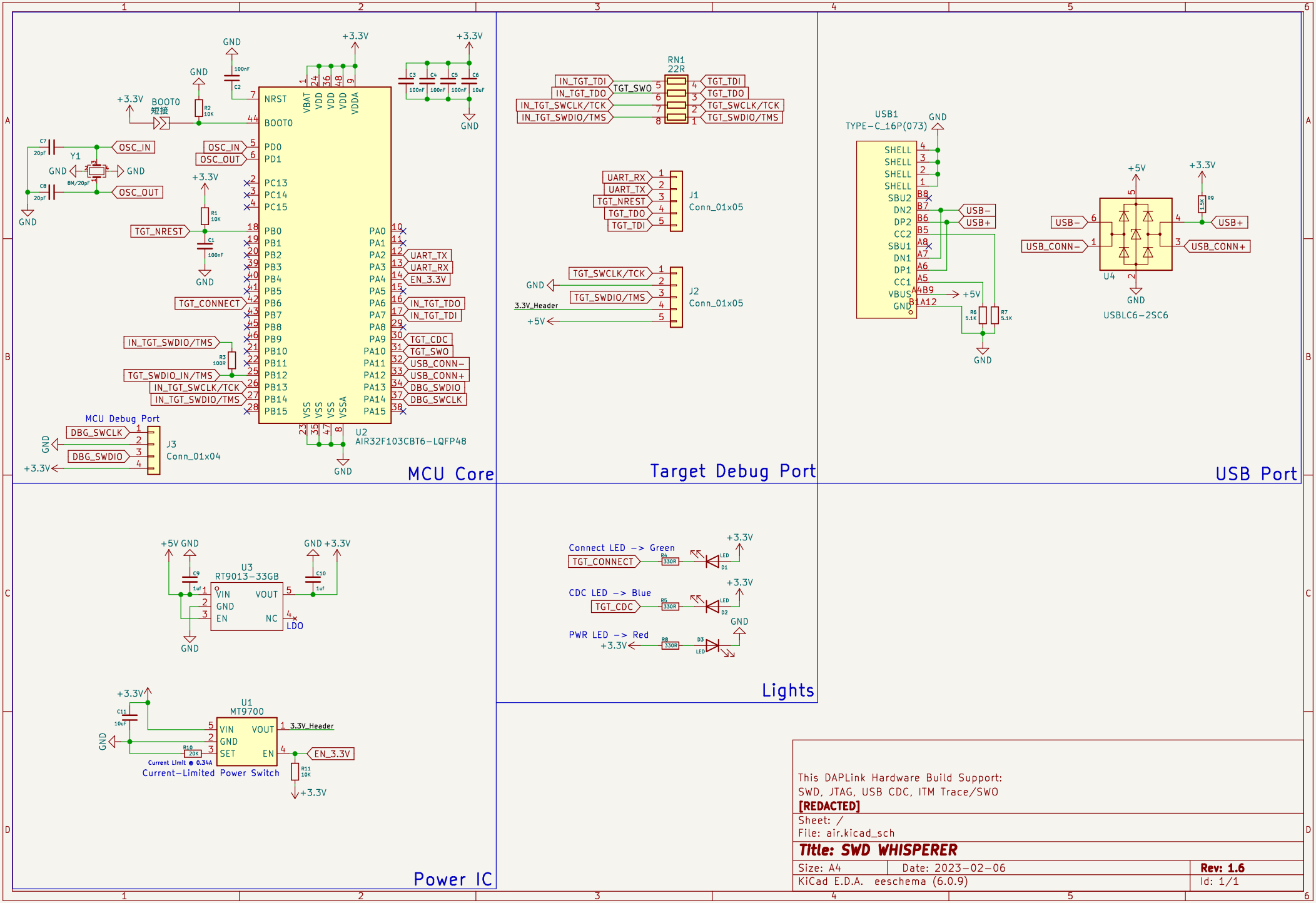
MCU Core
U2
AIR32F103CBT6-LQFP48
NRST
BOOT0
PD0
PD1
PC13
PC14
PC15
PB0
PB1
PB2
PB3
PB4
PB5
PB6
PB7
PB8
PB9
PB10
PB11
PB12
PB13
PB14
PB15
PA0
PA1
PA2
PA3
PA4
PA5
PA6
PA7
PA8
PA9
PA10
PA11
PA12
PA13
PA14
PA15
VBAT
VDD
VDDA
VSS
VSSA
OSC_IN
OSC_OUT
TGT_NREST
TGT_CONNECT
UART_TX
UART_RX
EN_3.3V
IN_TGT_TDO
IN_TGT_TDI
TGT_CDC
TGT_SWO
USB_CONN-
USB_CONN+
DBG_SWDIO
DBG_SWCLK
IN_TGT_SWDIO/TMS
TGT_SWDIO_IN/TMS
IN_TGT_SWCLK/TCK
BOOT0
短接
Y1
8M/20pF
MCU Debug Port
J3
Conn_01x04
Target Debug Port
RN1
22R
IN_TGT_TDI
IN_TGT_TDO
IN_TGT_SWCLK/TCK
IN_TGT_SWDIO/TMS
TGT_SWO
TGT_TDI
TGT_TDO
TGT_SWCLK/TCK
TGT_SWDIO/TMS
J1
Conn_01x05
UART_RX
UART_TX
TGT_NREST
TGT_TDO
TGT_TDI
J2
Conn_01x05
3.3V_Header
USB Port
USB1
TYPE-C_16P(073)
SHELL
SBU2
DN2
DP2
CC2
SBU1
DN1
DP1
CC1
VBUS
GND
USB-
USB+
R6
5.1K
R7
5.1K
U4
USBLC6-2SC6
USB_CONN-
USB_CONN+
R9
1.5K
Lights
Connect LED -> Green
TGT_CONNECT
R4
330R
D1
CDC LED -> Blue
TGT_CDC
R5
330R
D2
PWR LED -> Red
R8
330R
D3
Power IC
U3
RT9013-33GB
VIN
GND
EN
VOUT
NC
LDO
U1
MT9700
VIN
GND
SET
VOUT
EN
3.3V_Header
EN_3.3V
R10
20K
Current Limit @ 0.34A
Current-Limited Power Switch
R11
10K
This DAPLink Hardware Build Support:
SWD, JTAG, USB CDC, ITM Trace/SWO
[REDACTED]
Sheet: /
File: air.kicad_sch
Title: SWD WHISPERER
Size: A4
Date: 2023-02-06
Rev: 1.6
KiCad E.D.A. eeschema (6.0.9)
Id: 1/1