

Design Rules Verification Report

Filename : C:\development\shaololc-usb_breakout\miniUSB\shaololc-usb_breakout_miniusb.

Warnings 0
Rule Violations 6

Warnings	
Total	0

Rule Violations	
Clearance Constraint (Gap=0.2mm) (All),(All)	0
Short-Circuit Constraint (Allowed=No) (All),(All)	0
Un-Routed Net Constraint ((All))	0
Modified Polygon (Allow modified: No), (Allow shelved: No)	0
Width Constraint (Min=0.152mm) (Max=12.7mm) (Preferred=0.203mm) (All)	0
Routing Layers(All)	0
Routing Via (MinHoleWidth=0.25mm) (MaxHoleWidth=0.711mm) (PreferredHoleWidth=0.25mm) (MinWidth=0.25mm)	0
Routing Via (MinHoleWidth=0.254mm) (MaxHoleWidth=6.731mm) (PreferredHoleWidth=0.254mm)	0
Differential Pairs Uncoupled Length using the Gap Constraints (Min=0.254mm) (Max=0.254mm) (Preferred=0.254mm)	0
Power Plane Connect Rule(Relief Connect)(Expansion=0.508mm) (Conductor Width=0.508mm) (Air Gap=0.254mm)	0
Minimum Annular Ring (Minimum=0.127mm) (All)	0
Hole Size Constraint (Min=0.254mm) (Max=3.556mm) (All)	2
Hole To Hole Clearance (Gap=0.254mm) (All),(All)	0
Minimum Solder Mask Sliver (Gap=0.102mm) (All),(All)	0
Silk To Solder Mask (Clearance=0mm) (IsPad),(All)	0
Silk to Silk (Clearance=0.127mm) (All),(All)	0
Net Antennae (Tolerance=0mm) (All)	0
Board Clearance Constraint (Gap=0mm) (All)	4
Component Clearance Constraint (Horizontal Gap = 0.254mm, Vertical Gap = 0.254mm) (All),(All)	0
Height Constraint (Min=0mm) (Max=33mm) (Preferred=12.7mm) (All)	0
Total	6

Hole Size Constraint (Min=0.254mm) (Max=3.556mm) (All)	
Hole Size Constraint: (4mm > 3.556mm) Pad Free-1(-5.7mm,1.5mm) on Multi-Layer Actual Hole Size = 4mm	
Hole Size Constraint: (4mm > 3.556mm) Pad Free-1(-5.7mm,7.3mm) on Multi-Layer Actual Hole Size = 4mm	

Board Clearance Constraint (Gap=0mm) (All)	
Board Outline Clearance(Outline Edge): (0.077mm < 0.381mm) Between Board Edge And Track (15.4mm,-1.4mm)(15.4mm,10.2mm) on Top Overlay	
Board Outline Clearance(Outline Edge): (0.077mm < 0.381mm) Between Board Edge And Track (4.9mm,-1.4mm)(15.4mm,-1.4mm) on Top Overlay	
Board Outline Clearance(Outline Edge): (0.077mm < 0.381mm) Between Board Edge And Track (4.9mm,-1.4mm)(4.9mm,10.2mm) on Top Overlay	
Board Outline Clearance(Outline Edge): (0.077mm < 0.381mm) Between Board Edge And Track (4.9mm,10.2mm)(15.4mm,10.2mm) on Top Overlay	