
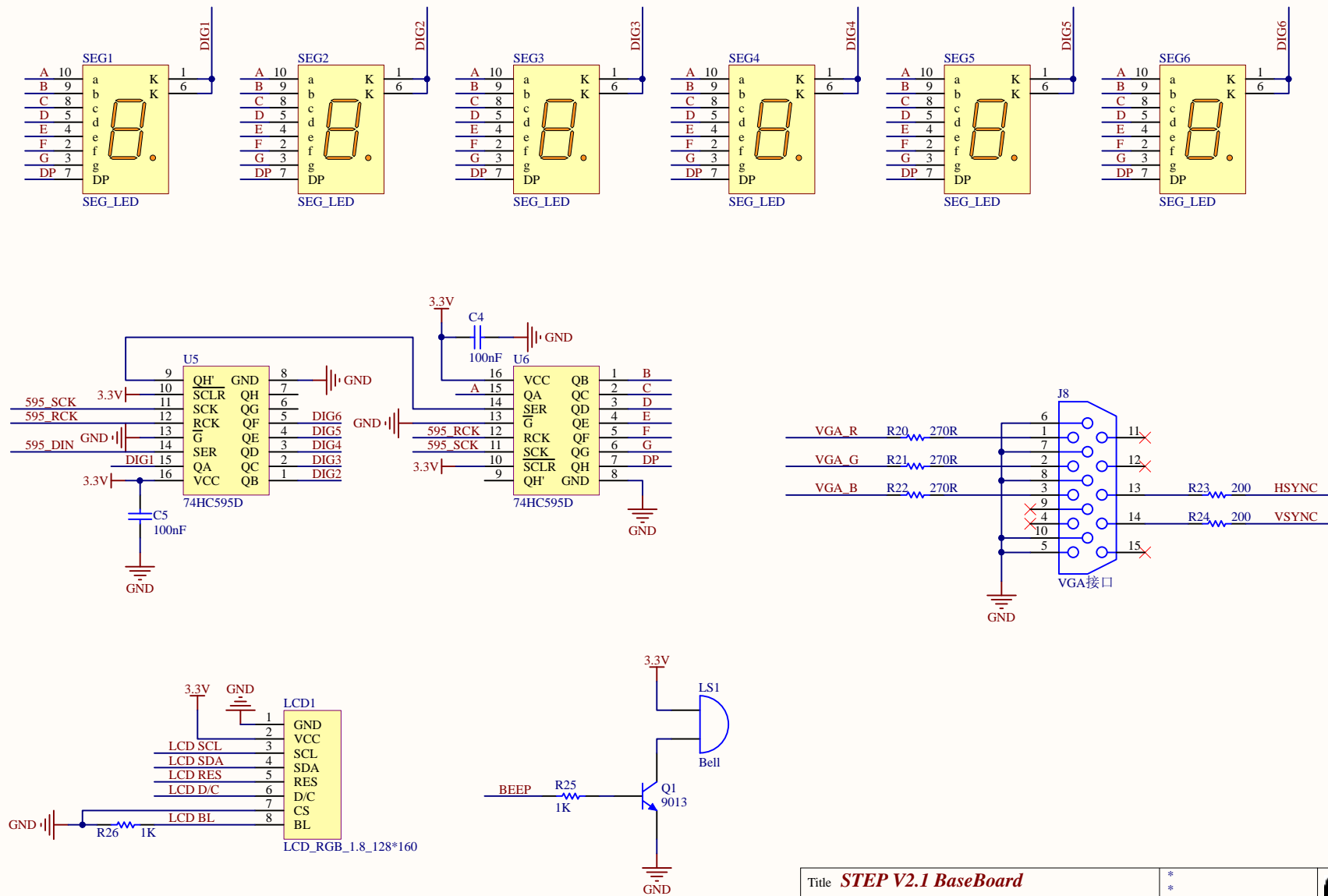



Title STEP V2.1 BaseBoard			* * * * *	
Size: A4	Number:	Revision: V2.1		
Date: 2016/10/10	Time: 10:24:51	Sheet 2 of 3		
File: F:\PCB_Project\STEP V2.1 BaseBoard_002\STEP V2.1 BaseBoard_2.SchDoc				



Title STEP V2.1 BaseBoard			* * * * *	
Size: A4	Number:	Revision: V2.1		
Date: 2016/10/10	Time: 10:25:05	Sheet 3 of 3		
File: F:\PCB_Project\STEP V2.1 BaseBoard_002\STEP V2.1 BaseBoard_3.SchDoc				

