Shaoting Zhang

8000 U.S. 290 West Apt.10407   sxz152930@utdallas.edu

Austin, TX 78736   Mobile: (737)-529-7160

Seek fulltime position on software engineering starting at Spring 2018

Immigration Status: F-1 OPT (No need for H1B sponsorship - I-485 pending)

Education

- **University of Texas at Dallas**   Dallas, TX
  Master Student, Electrical Engineering   *Aug. 2015 – Dec. 2017*
  - GPA: 11.4/12
  - Relevant Courses: Design and Analysis of Computer Algorithms (A), Dynamics of Complex Networks and Systems (A), Data Structure, Advanced Digital Logic, Computer Architecture (A)

- **Nanjing University of Post and Telecommunications**   Nanjing, China
  Bachelor of Engineering, Department of Communication Engineering   *Sep. 2009 – Jul. 2013*
  - Relevant Courses: Principles of Communications, Signals and Systems, Principles of Digital & Analog Integrated Circuits, Random Signals and Statistics, Discrete Structures

Course Projects

- **Algorithms: Design and Analysis:** Algorithm Analysis and Implementations
  - Python implementations of sorting and graph algorithms, such as QuickSort, MergeSort, Dijkstra's algorithm, Karger's algorithm and computing strongly connected components

- **Dynamics of Complex Networks and Systems:** The Network Modeling for Game of Thrones
  - Build the weighted and undirected network for the character relationship in Game of Thrones
  - Describe and analyze the network properties like the centrality, diameter and power law

- **Java Programming: Solving Problems with Software:**
  - Java implementation and data analysis of a baby-name database from excel datesheet
  - Java implementation of a rating-based recommendation system such as movie recommendation

- **System Design:**
  - Mini Twitter: Implement basic functions, such as post tweets, news feed, get timeline, follow/unfollow
  - Distributed File System: One client and multiple servers, where the servers are built with peer to peer or master and slave model
  - Location-based Service Implementation with Geohash

- **Computer Architecture:** Cache Simulation and Optimization
  - Cache performance evaluations varying capacities, associativities and block sizes using SimpleScalar
  - Instruction set optimization with parallelism and pipelining techniques: Adopt a SIMD instruction to unfold the "for" loop; Achieve 26% improvement on the instruction cache miss rate compared with the original C programs with MediaBench test cases

Skills

Languages/Scripting: Python, Java, C language, SPICE/HDL

Applications: Unix/Linux System, Eclipse, Git, Matlab

Miscellaneous: Self-motivated, good communication and team-work skills.

Work Experience

- **China Electronics Technology Group Corporation**   Nanjing, China
  Silicon Testing Engineer   *Jul. 2013 – Jul. 2014*
  - Wafer level testing before IC packaging
  - Quality testing of power tube inside the gold bonding for silicon BJT (Bipolar Junction Transistor) and LDMOS (Laterally Diffused Metal Oxide Semiconductor). Parameter testing and performance evaluations on output power and power gain fluctuations of BJT and LDMOS