

Student ID:		Lab Section:	
Name:		Lab Group:	

Experiment No. 3

Verification of Superposition Principle

Objective

The aim of this experiment is to verify experimentally the Superposition theorem which is an analytical technique of determining currents/voltages in a circuit with more than one emf source.

Theory

The Superposition Principle is a fundamental concept in electrical circuits that states that in any linear, active, bilateral network having more than one source, the response across any element is the sum of the responses obtained from each source considered separately and all other sources are replaced by their internal resistance. The superposition theorem is used to solve the network where two or more sources are present and connected. The current or voltage through any component in a circuit is the sum of the effects of each individual source acting alone. In other words, the principle states that **the total response of a circuit with multiple sources is the sum of the responses of the circuit to each individual source acting alone**. This principle is widely used in circuit analysis to simplify complex circuits and solve them with ease.

In a **linear circuit** containing multiple independent sources and linear elements (e.g., resistors, inductors, capacitors), the voltage across (or the current through) any element when all the sources are acting simultaneously may be obtained by adding algebraically all the individual voltages (or the currents) caused by each independent source acting alone, with all other sources deactivated.

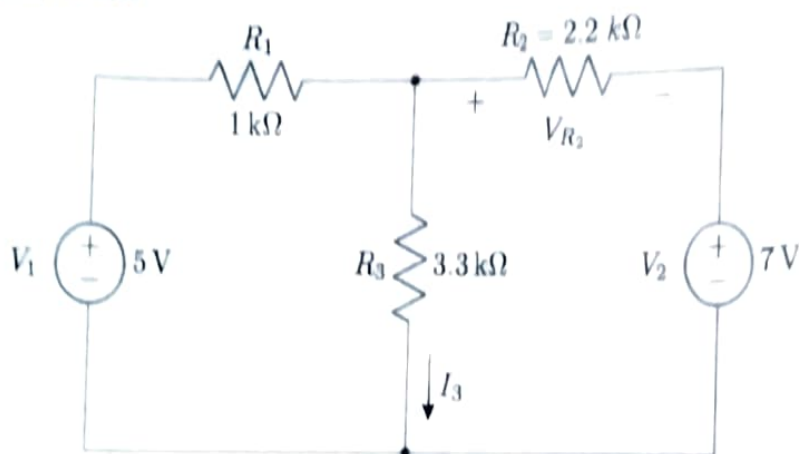
An independent **voltage source** is deactivated (made zero) by **shorting** it and an independent **current source** is deactivated (made zero) by **open circuiting** it. However, if a dependent source is present, it must remain active during the superposition process.

Apparatus

- Multimeter
- Resistors (1 k Ω , 2.2 k Ω , 3.3 k Ω)
- DC power supply
- Breadboard
- Jumper wires

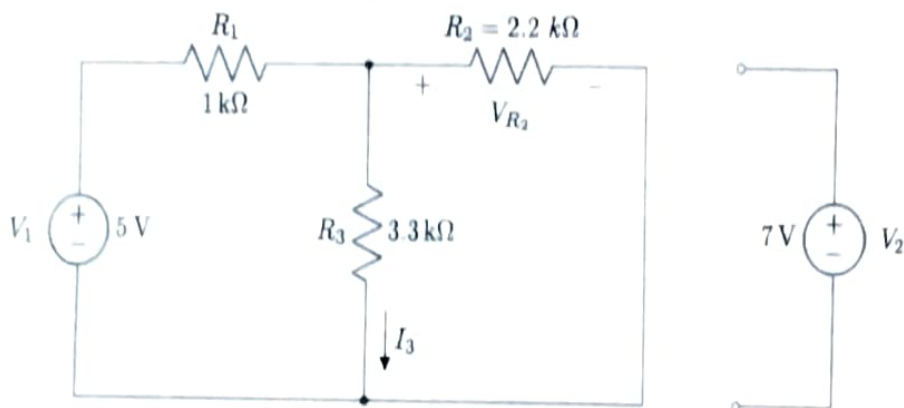
Procedures

- Measure the resistances of the provided resistors and fill up the data table (Table 1)
- Construct the following circuit on a breadboard. Try to use minimum number of jumper wires



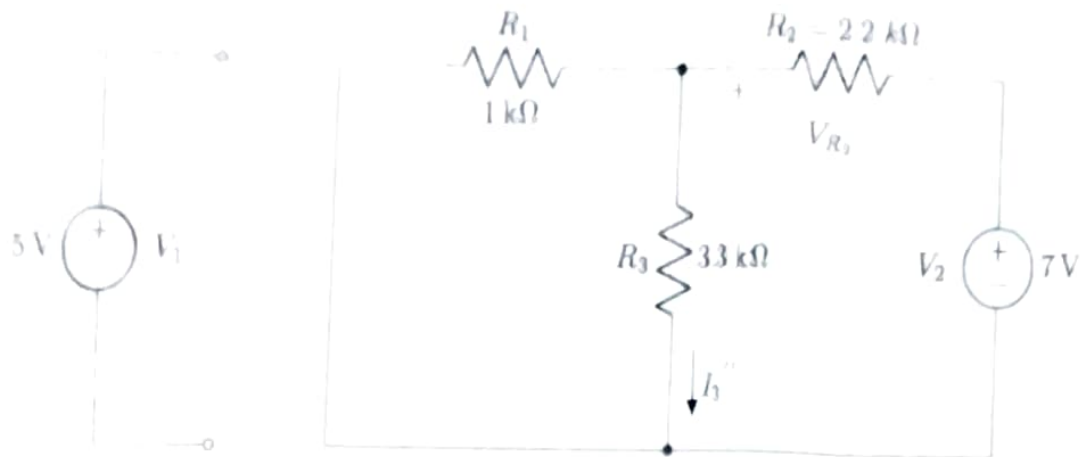
Circuit 1

- Measure the voltage across the resistors R_2 , R_3 and current through the resistor R_3 . Use a Multimeter for measuring the voltage and use Ohm's law to calculate the current (I_3) through R_3 . Fill up the data tables
- Render V_1 inactive (keeping V_2 active) and construct the following circuit



Circuit 2

- Measure the voltage across the resistors R_2 , R_3 and current through the resistor R_3 . Use a Multimeter for measuring the voltage and use Ohm's law to calculate the current (I_3) through R_3 . Fill up the data tables.
- Render V_2 inactive (keeping V_1 active) and construct the following circuit.



Circuit 3

- Measure the voltage across the resistors R_2 , R_3 and current through the resistor R_3 . Use a Multimeter for measuring the voltage and use Ohm's law to calculate the current (I_3) through R_3 . Fill up the data tables
- Verify if $I_3 = I_3' + I_3''$ which would validate the superposition theorem for the current through R_3
- Verify if $V_2 = V_2' + V_2''$ which would validate the superposition theorem for the voltage across R_2

Data Tables

Table 07
Signature of Lab Faculty:

SR

Date:

14-06-2023

**** For all the data tables, take data up to three decimal places, round to two, then enter into the table.**

Table 1: Resistance Data

For all your future calculations, please use the observed values only (even for theoretical calculations)

Notation	Expected Resistance	Observed Resistance (kΩ)
R_1	1 kΩ	0.98
R_2	22 kΩ	2.157
R_3	33 kΩ	3.250

Table 2: Current through R_3 and voltage across R_2

In the following table, V_3 is the voltage drop across resistor R_3 and I_3 is the current through it. V_2 is the voltage drop across resistor R_2 . Similar syntax applies to the remaining resistors.

Also, calculate the percentage of error between expected and observed values of $I_3 + I_3^*$.

Observation	I_3 with both V_1 and V_2 active (mA)	I_3 with only V_1 is active (mA)	I_3^* with only V_2 is active (mA)	$I_3 + I_3^*$ (mA)
Experimental	1.44	0.886	0.557	1.443
Theoretical	1.43	0.88	0.56	1.43
Observation	V_2 with both V_1 and V_2 active (V)	V_2 with only V_1 is active (V)	V_3^* with only V_2 is active (V)	$V_2 + V_2^*$ (V)
Experimental	-2.33	2.87	-5.21	-2.34
Theoretical	-2.35	2.85	-5.20	-2.35

• Percentage of error = $\left| \frac{\text{Observed Value} - \text{Expected Value}}{\text{Expected Value}} \right| \times 100\%$

N.B. Here, the Expected values are I_3 , V_2 and the Observed values are $I_3 + I_3^*$ and $V_2 + V_2^*$ respectively.

Hence, Percentage of error in $I_3 + I_3^*$ calculation =

0.2 %

Hence, Percentage of error in $V_2 + V_2^*$ calculation =

0.4 %

Questions

- Calculate the **power associated** with R_2 using the experimentally measured values of currents or voltages when.
 - Only V_1 source is active
 - Only V_2 source is active
 - Both V_1 and V_2 sources are active

Fillup the Table given below and verify, whether the superposition theorem is verified or not in this case. If not, comment on the reasons. **You don't need to take any new readings for this task.** Use previous data from Tables 2 to calculate the power.

Remember, power consumed by a resistor, $P = VI = I^2 R = \frac{V^2}{R}$

Observation	P_{R_2} when both V_1 and V_2 active $P_{R_2} = \frac{V_2^2}{R_2}$ (W)	P_{R_2} when only V_1 is active $P_{R_2} = \frac{V_2^2}{R_2}$ (W)	P_{R_2} when only V_2 is active $P_{R_2} = \frac{V_2^2}{R_2}$ (W)	$P_{R_2} + P_{R_2}$ (W)
Experimental	2.516	3.818	12.58	16.398
Theoretical	2.5	3.73	12.56	16.29

Is the Superposition Principle applicable in case of Power?

☐ Yes ☒ No

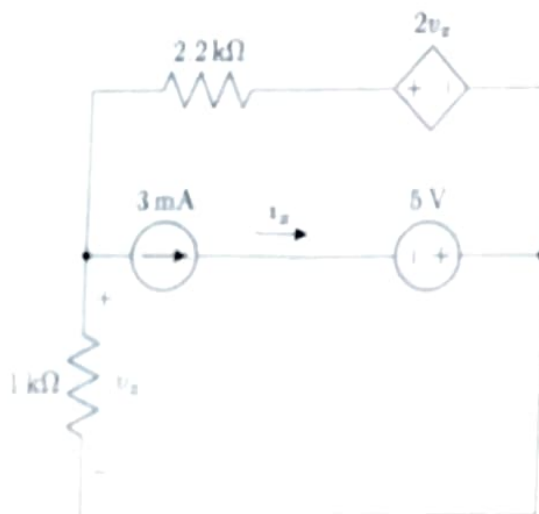
Elaborate the reasons behind your choice.

Superposition can be applied in linear circuits to find voltage or current. Using these values power can be calculated. Therefore linearity comes with two fundamentals that is homogeneity & additivity. For power, input & output is not related linearly. So, we can't write $P = I_1^2 R + I_2^2 R = (I_{1+2})^2 R$.

- Why was a short circuit wire required to be connected between the corresponding terminals while turning off a voltage source? Why wasn't simply turning off the power switch enough to deactivate the source?

connecting a short circuit insures that any leftover charges or potential differences are soon disattached, preventing any transient currents or voltages that could affect the analysis of the circuit.

3. For the circuit shown below,



(a) Show using the Superposition Principle that the 5 V voltage source has no effect on the current i_x .

According to superposition principle, the contribution of individual sources can be tested separately. The voltage source with 5 volt will only create a fixed voltage across its terminals and will not affect current, i_x .

The current source 3mA will determine the current flowing through the circuit which does not impact on voltage source.

(b) Why the 5V voltage source does not contribute to the current i_x .

The voltage source (5V) affects the circuit in terms of the voltage drops across the elements in the circuit but it does not directly determine the current flowing through the circuit.

(c) Can you draw any conclusions about the resistances of an ideal voltage and current source from this? If so, what are they?

An ideal voltage source has zero internal resistance and an ideal current source has infinite internal resistance.

Report

1. Fill up the theoretical parts of all the data tables.
2. Answer to the questions.
3. Comment on the obtained results and discrepancies. Use the next page.

Discussion:

For the verification of superposition principle, we built the circuit on breadboard with 3 resistance SDA circuit power supply properly. We used 2 Jumper wire to measure the voltage across $R_2 (2.2 \text{ k}\Omega)$ & $R_3 (3.3 \text{ k}\Omega)$ & current through resistor $R_3 (3.3 \text{ k}\Omega)$. We used multimeter for measuring voltage & resistance. We used Ohm's law, $\Delta V = IR$ to find I_3 through $R_3 (1.40 \text{ mA})$. And we did the same process with voltage source 1 (5V) active only & voltage source 2 (7V) active only to find current through I_3 & voltages across R_2 & R_3 . According to our measure we found $I_3 = 1.40 \text{ mA}$ which is equal to $I_3' + I_3''$ validate the superposition theorem & also $V_{R_2} = V_{R_2}' + V_{R_2}''$ voltage results & draw across R_2 . After that we obtained actual value. Then we compared calculated & obtained values to find the percentage of error which is $I_3' + I_3'' = 0.2\% \text{ \& } 0.4\% = V_2' + V_2''$.