

# Lecture 5

Wednesday, February 16, 2022 6:41 PM

## Passive & Active Circuit Components



### Active Component

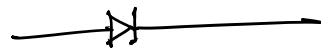
→ can supply power to circuit.



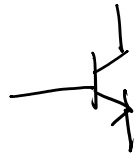
→ Voltage Source



→ Current Source



→ Diode



→ Transistor



→ MOSFET

### Passive Component

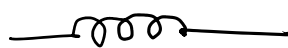
→ Only consumes/dissipates power. / stores



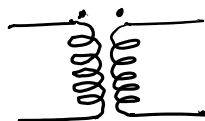
→ Resistor



→ Capacitor (Stores charge)



→ Inductor (Stores magnetic energy)



→ Transformer

# Component Equations

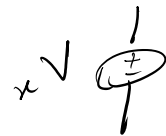
T.B

Rules

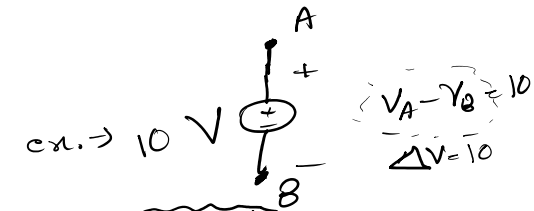
1. 1 Node  $\rightarrow$  1 Voltage  $\Rightarrow$  1 equation
2. 1 Component  $\rightarrow$  1 Current  $\Rightarrow$  1 equation

## Component Equations

### 1. Voltage Source



$$\rightarrow \boxed{\Delta V = x}$$



$\rightarrow$  Nodal Analysis

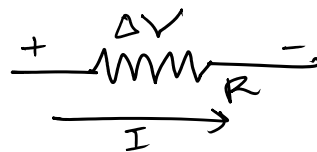
### 2. Current Source



$$\rightarrow \boxed{I = y}$$

$\rightarrow$  Mesh Analysis

### 3. Resistance



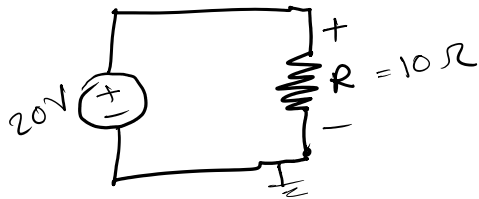
$$\rightarrow \boxed{\Delta V = IR}$$

$\rightarrow$  Mesh Analysis

$$\rightarrow \boxed{I = \frac{\Delta V}{R}}$$

$\rightarrow$  Nodal Analysis

## Passive Sign Convention : Power

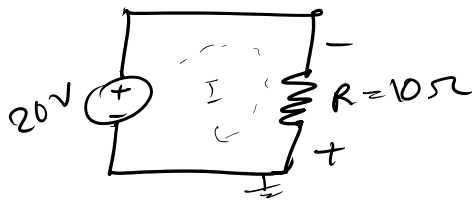


$$P = \frac{V^2}{R} = 40 \text{ W}$$

$$\text{or, } \Delta V = 20 \text{ V}$$

$$I = \frac{20}{10} \text{ A} = 2 \text{ A}$$

$$\therefore P = 2 \times 20 = \Delta V I \\ = 40 \text{ W}$$



$$\Delta V = 0 - 20 = -20 \text{ V}$$

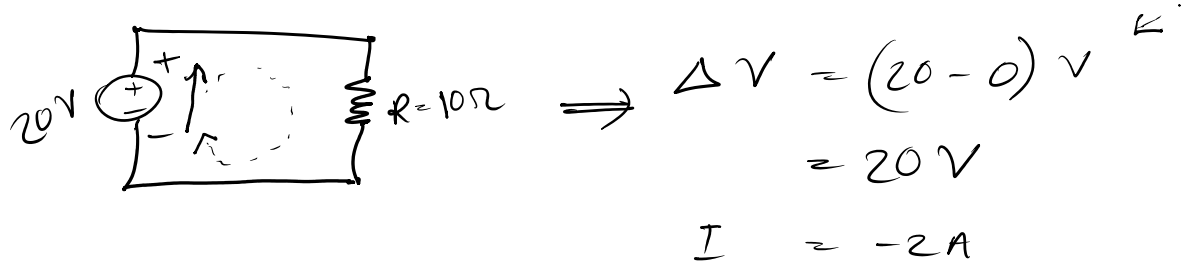
$$I = \frac{-20}{10} \text{ A} = -2 \text{ A}$$

$$\therefore P = (-2) \times (-20) \text{ W} \\ = 40 \text{ W}$$

Power consumption

$\therefore$  Same, in case of Resistance.

When the component is voltage source



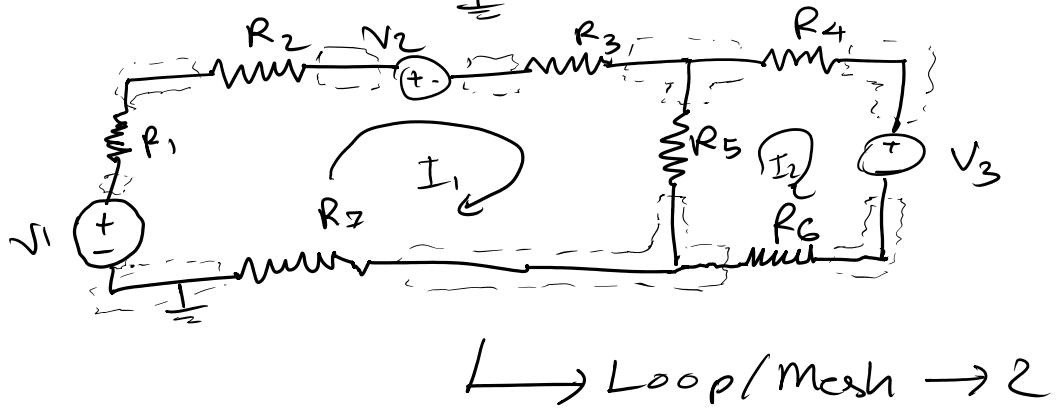
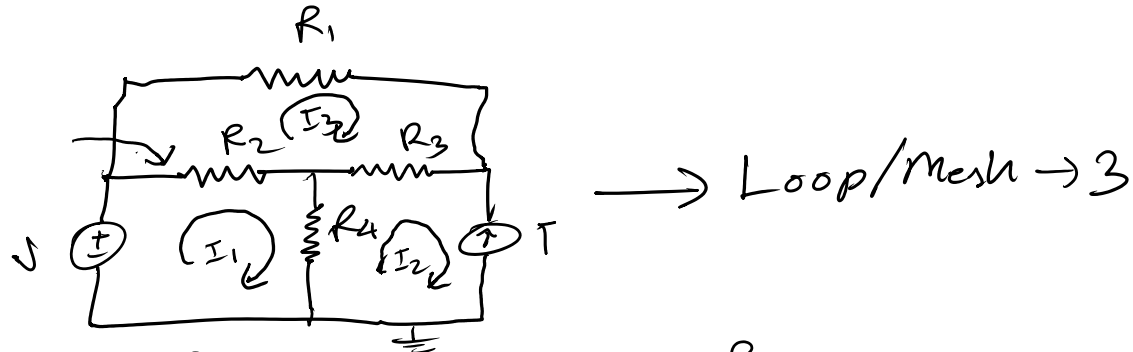
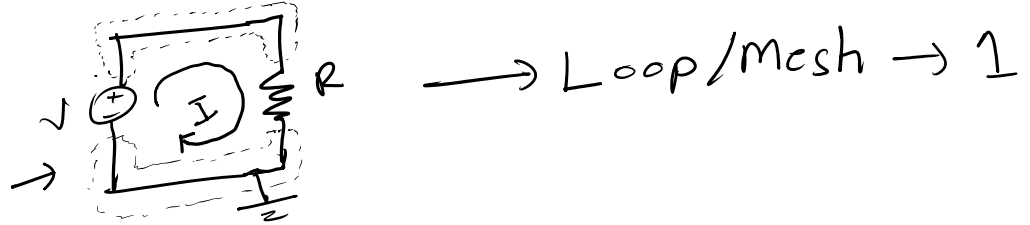
$$I = -2 \text{ A}$$

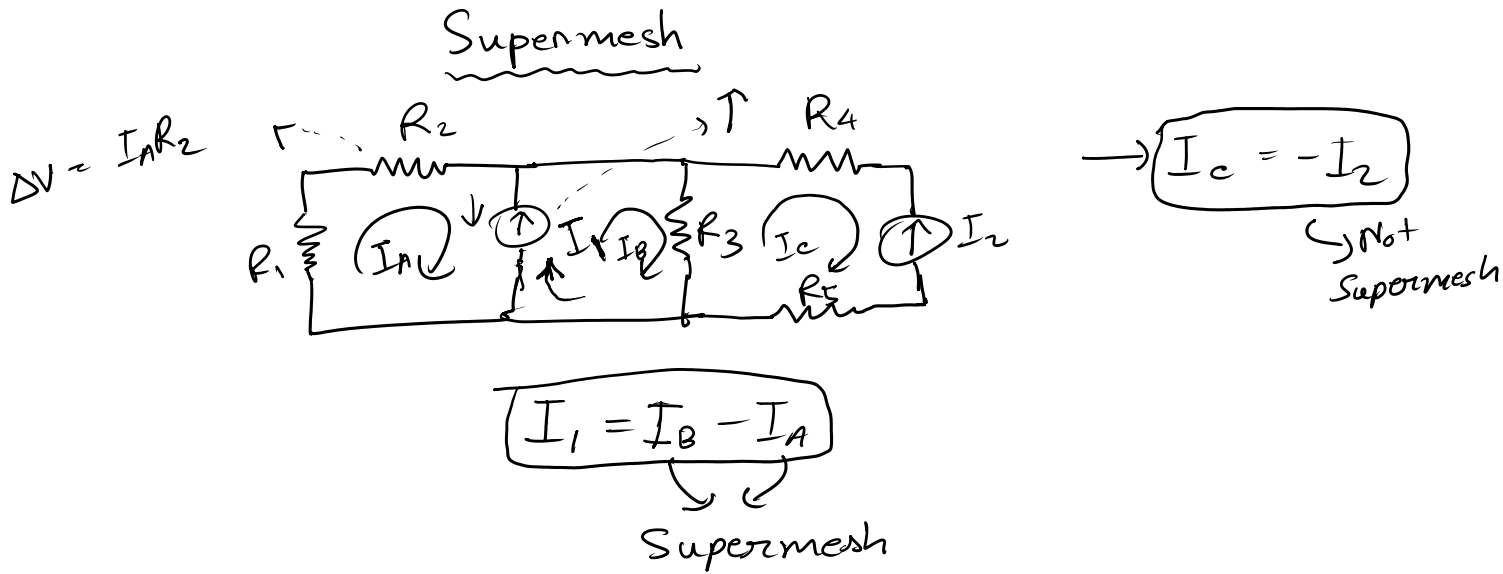
$$P = 20 \times (-2) \text{ W} \\ = \boxed{-40 \text{ W}}$$

Significance

- + Power  $\rightarrow$  Consumption of Power
- - Power  $\rightarrow$  Supply of Power.

# Mesh

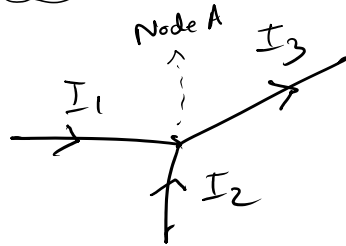




↳ When there is Current source between two loops.  
     ↓        ↓  
     Those two loops/mesh  
     ⌈        ⌋  
     Supermesh

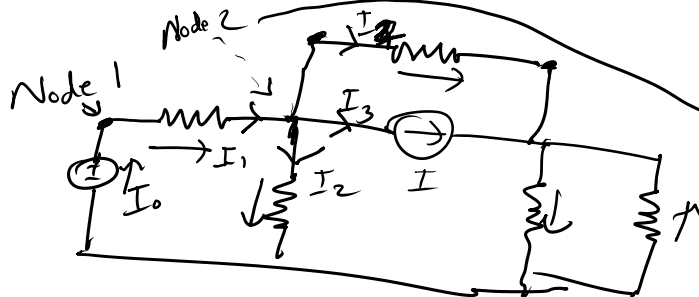
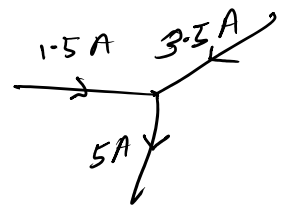
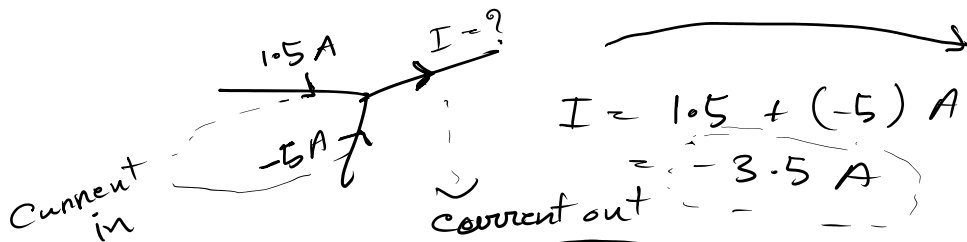
# KCL

## Kinshoff's Current Law



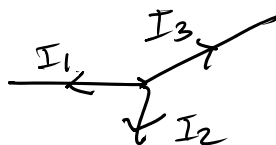
→ Total current In  
= Total current out

⌞  
KCL →  $I_1 + I_2 = I_3$



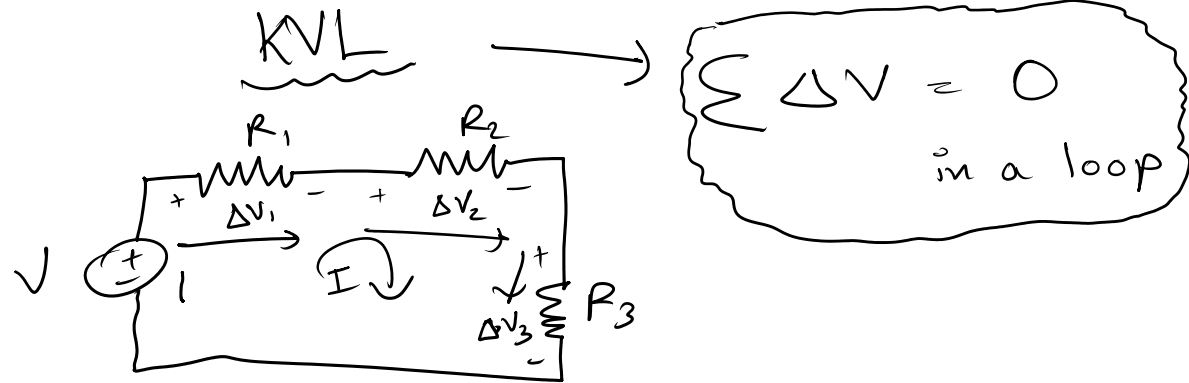
KCL →  $I_1 = I_2 + I_3 + I_4$

Node 1  
⌞ →  $I_0 = I_1$  (KCL)



⌞ → KCL →  $I_1 + I_2 + I_3 = 0$

⌞ →  $\boxed{\sum I = 0}$



$$\Delta V_1 + \Delta V_2 + \Delta V_3 = V$$

$$\Rightarrow \Delta V_1 + \Delta V_2 + \Delta V_3 - V = 0$$

$\Delta V_4 = -V$

∴ Total voltage drop = ~~Total~~ 0  
in a loop.