



CSE251: Electronic Devices and Circuits

Lecture 5:
Closed Loop Op-amp configurations

Prepared By:
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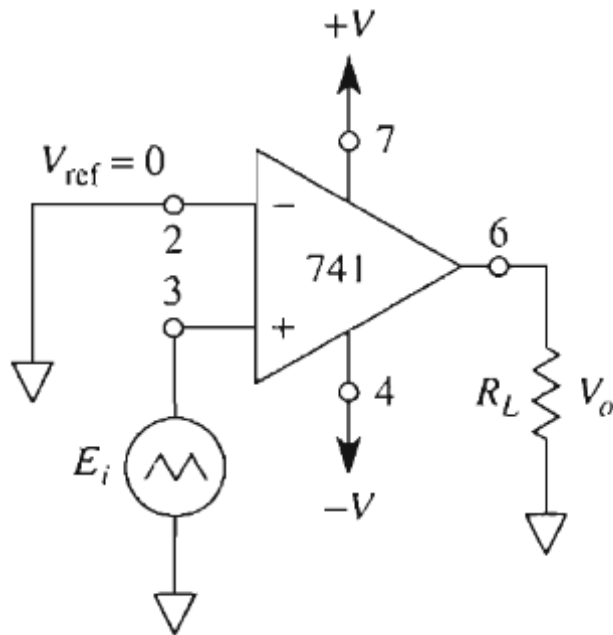
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Basic Op-Amp Configurations

- **Open-loop Configurations**

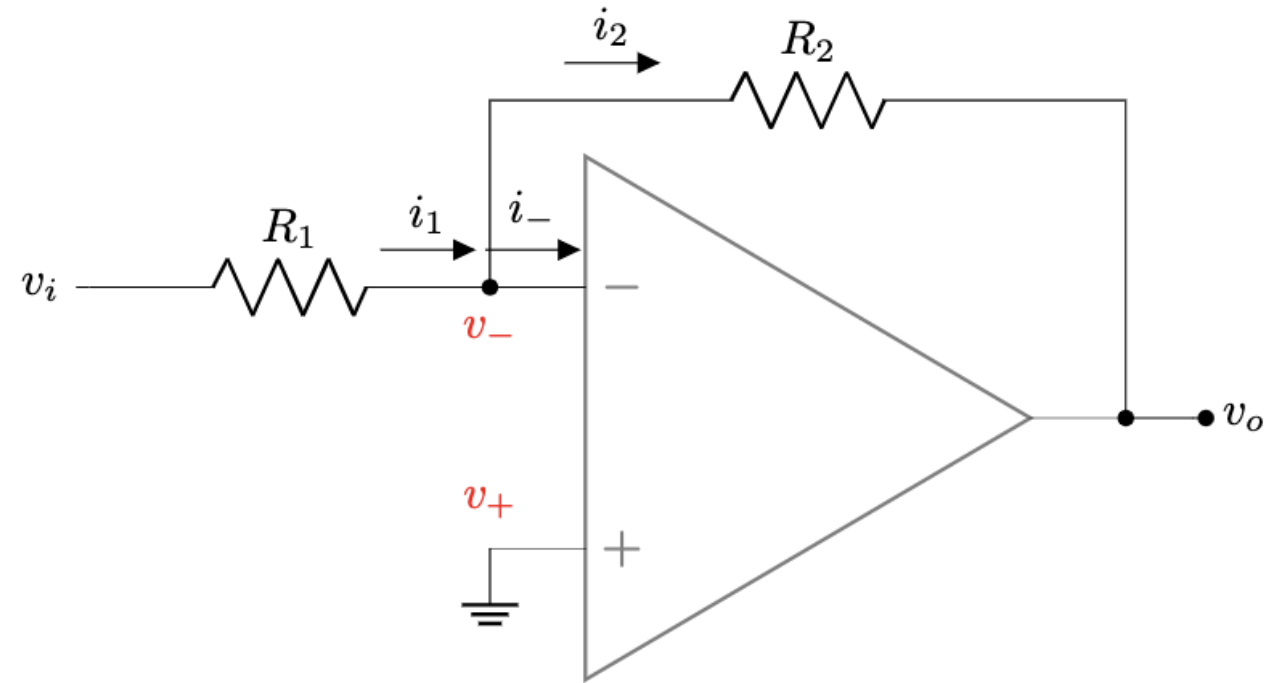
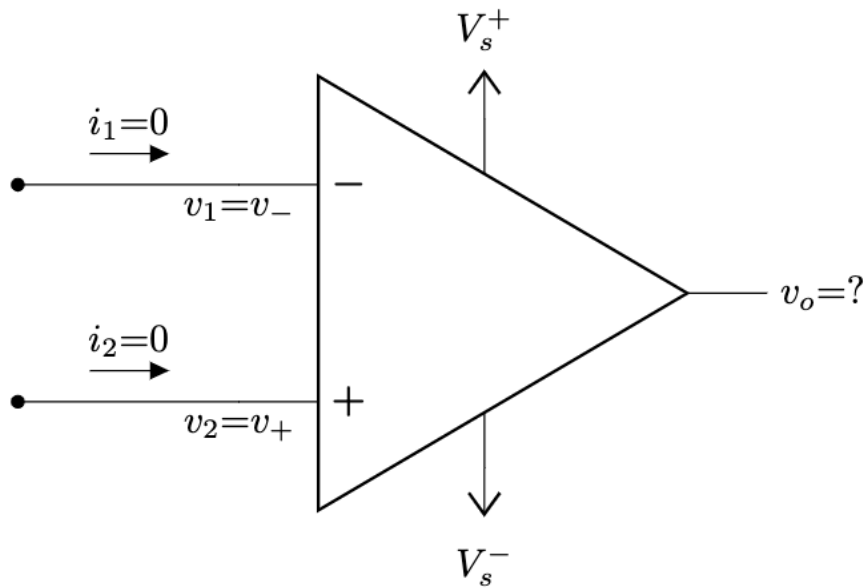
1. Comparator / Voltage Level Detectors



- **Closed Loop Configurations**

1. Voltage Follower
2. Inverting Amplifier
3. Inverting Summer
4. Non-Inverting Amplifier
5. Weighted Subtractor
6. Integrator
7. Differentiator
8. Exponential Converter
9. Logarithmic Converter
10. Multiplier
11. Divider

Solving Closed Loop Op-Amp Circuit



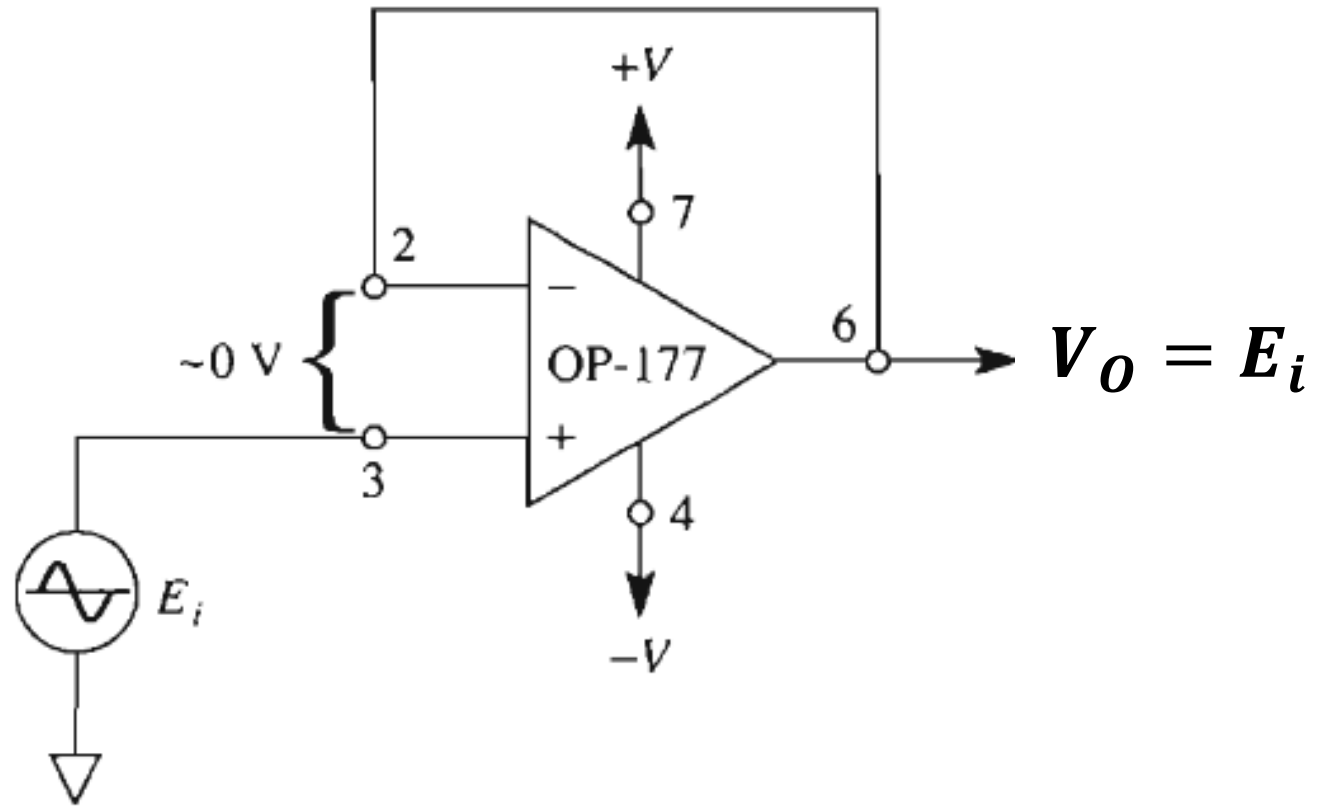
Two Rules:

1. Virtual Shorting:
2. Zero input bias current:

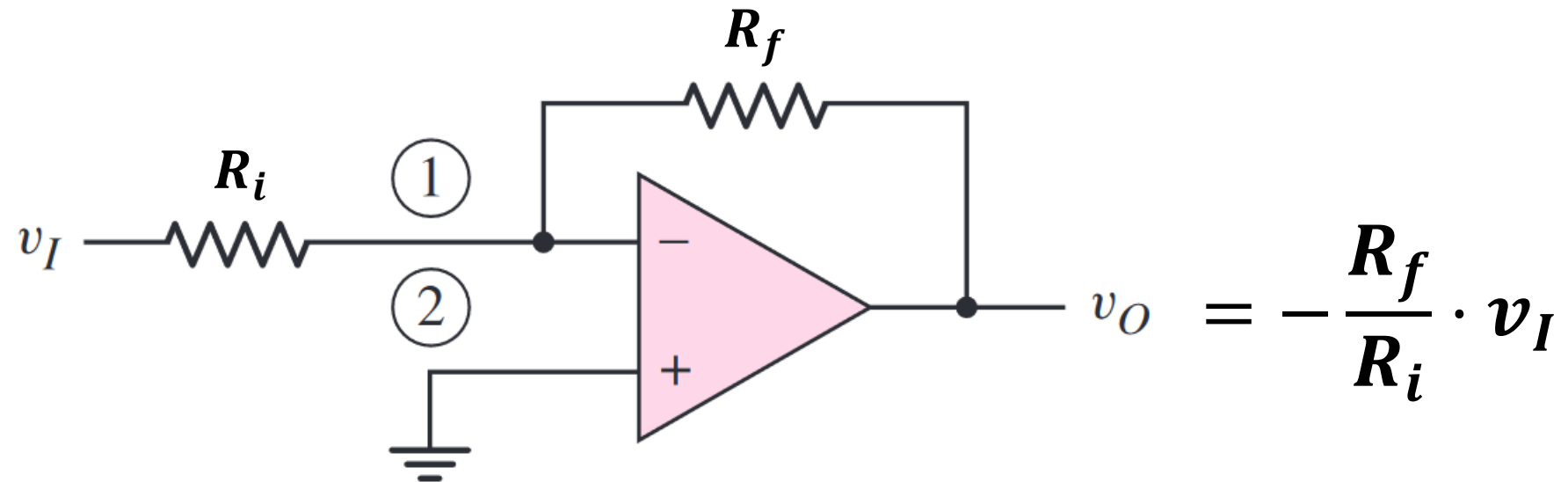
$$v_+ = v_-$$

$$i_- = i_+ = 0$$

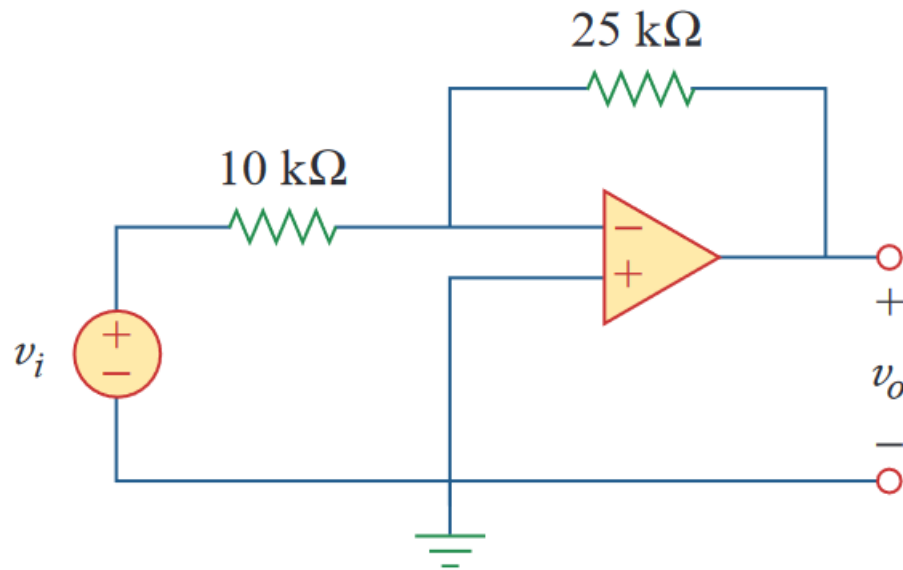
Voltage Follower / Buffer:



Inverting Amplifier



Example - 1



If $v_i = 0.5\text{ V}$, calculate:

- (a) Output voltage v_o .
- (b) Current in the **$10\text{ k}\Omega$** resistor.

(a)

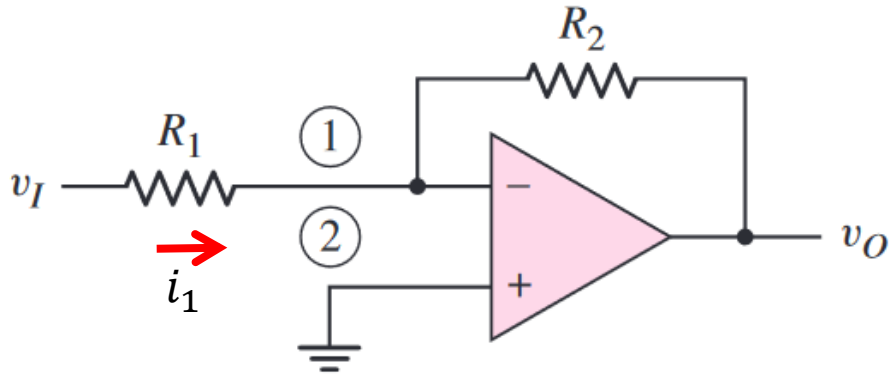
$$v_o = -\frac{R_f}{R_i} \cdot v_i = -2.5v_i = -1.25\text{ V}$$

(b) Current through the **$10\text{ k}\Omega$** resistor is

$$i = \frac{v_i}{R_i} = \frac{0.5}{10}\text{ mA} = 50\text{ }\mu\text{A}$$

Example - 2

Design the circuit such that the closed loop voltage gain is $A_{CL} = -5$. Assume the op-amp is driven by an ideal sinusoidal source, $v_I = 0.1 \sin(\omega t)$ (V), that can supply a maximum current of $5 \mu\text{A}$.



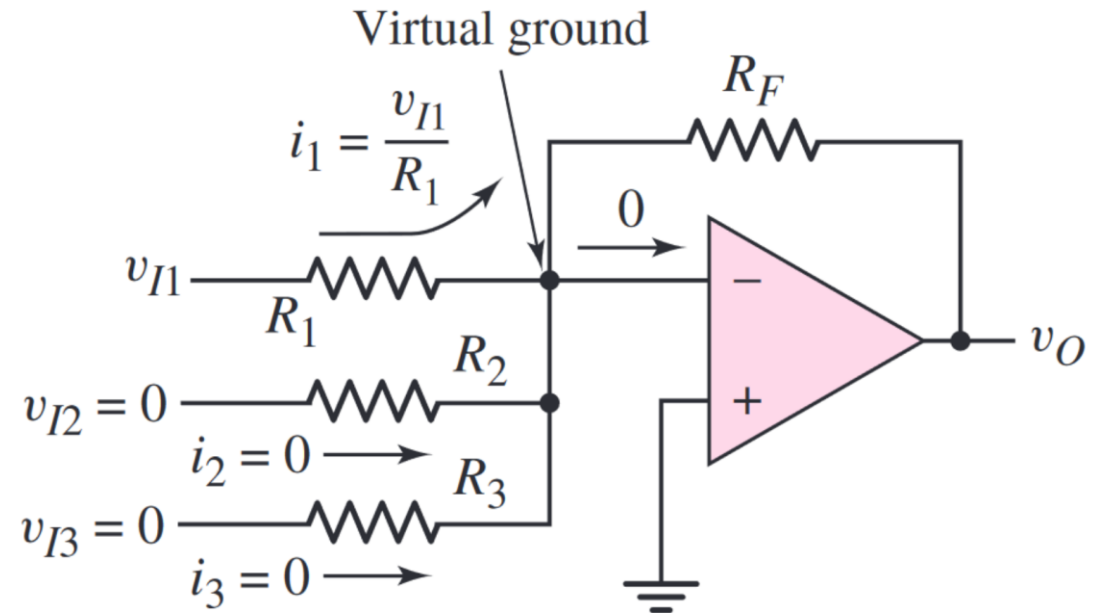
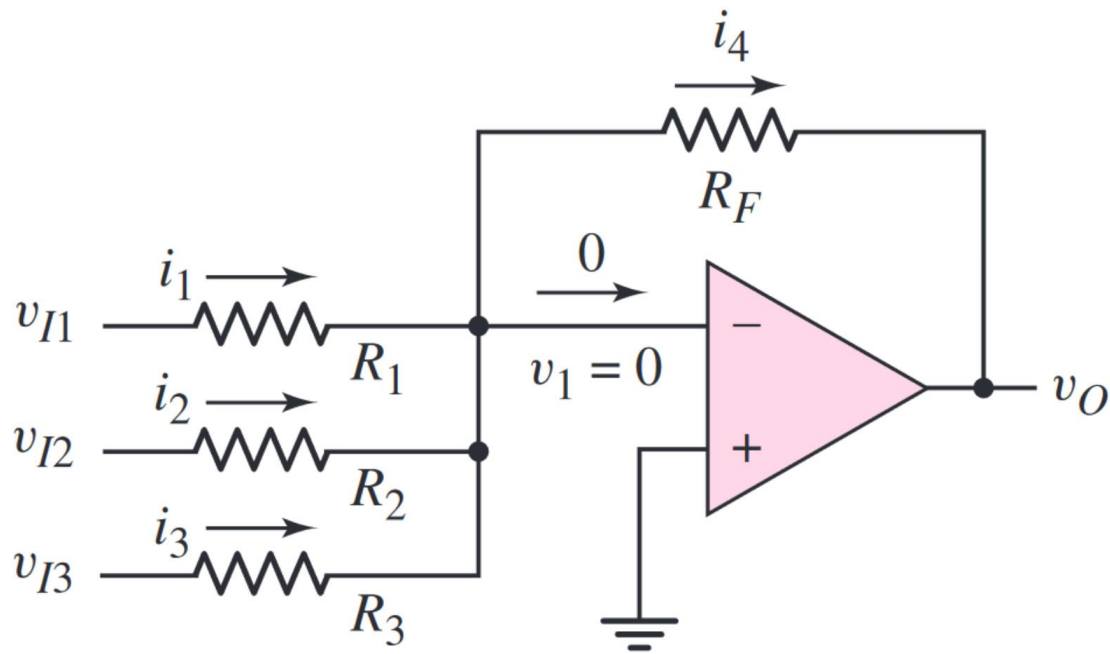
$$i_1 = \frac{v_I}{R_1}$$

$$R_1 = \frac{v_I(\max)}{i_1(\max)} = \frac{0.1}{5 \times 10^{-3}} = 20 \text{ k}\Omega$$

$$R_2 = -A_{CL} \cdot R_1 = 5 \times 20 = 100 \text{ k}\Omega$$

Inverting Summer

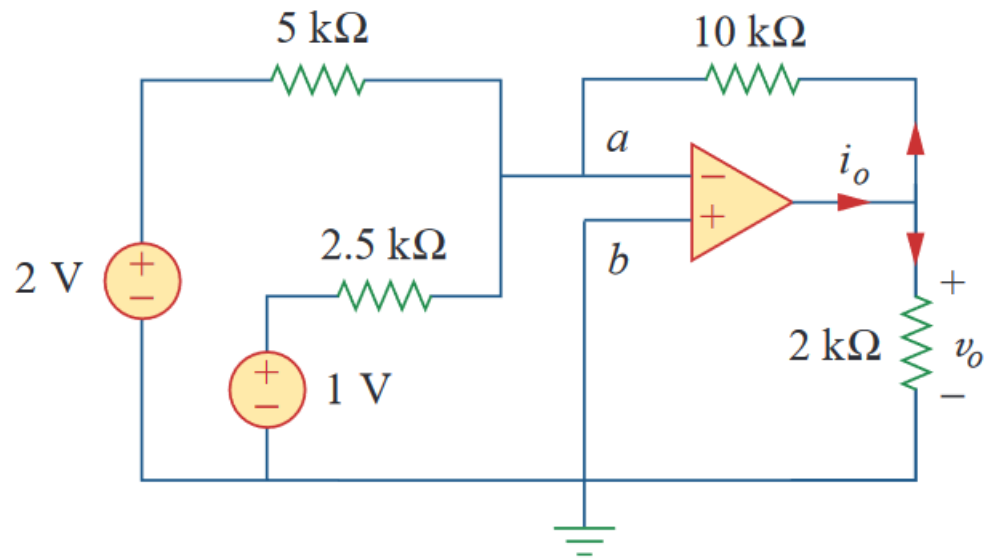
- Multichannel Amplifier



$$v_O(v_{I1}) = -i_1 R_F = -\left(\frac{R_F}{R_1}\right) v_{I1}$$

$$v_O = -\left(\frac{R_F}{R_1} v_{I1} + \frac{R_F}{R_2} v_{I2} + \frac{R_F}{R_3} v_{I3}\right)$$

Example - 3



Calculate:

- (a) Output voltage v_o .
- (b) Output current i_o .

(a)

$$v_o = -\left(\frac{10}{5} \cdot 2 + \frac{10}{2.5} \cdot 1\right) = -8 \text{ V}$$

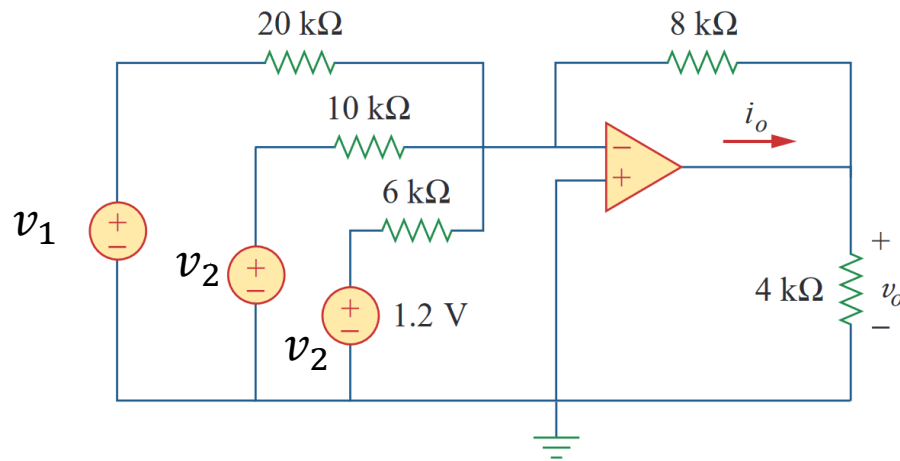
(b)

$$i = \frac{v_o}{10} + \frac{v_o}{2} = (-0.8 - 4) \text{ mA} = -4.8 \text{ mA}$$

Example 4

Design an op-amp circuit with inputs v_1 , v_2 and v_3 such that, output voltage v_o :

$$v_o = -\frac{2}{5}v_1 - \frac{4}{5}v_2 - \frac{4}{3}v_3$$



Solution:

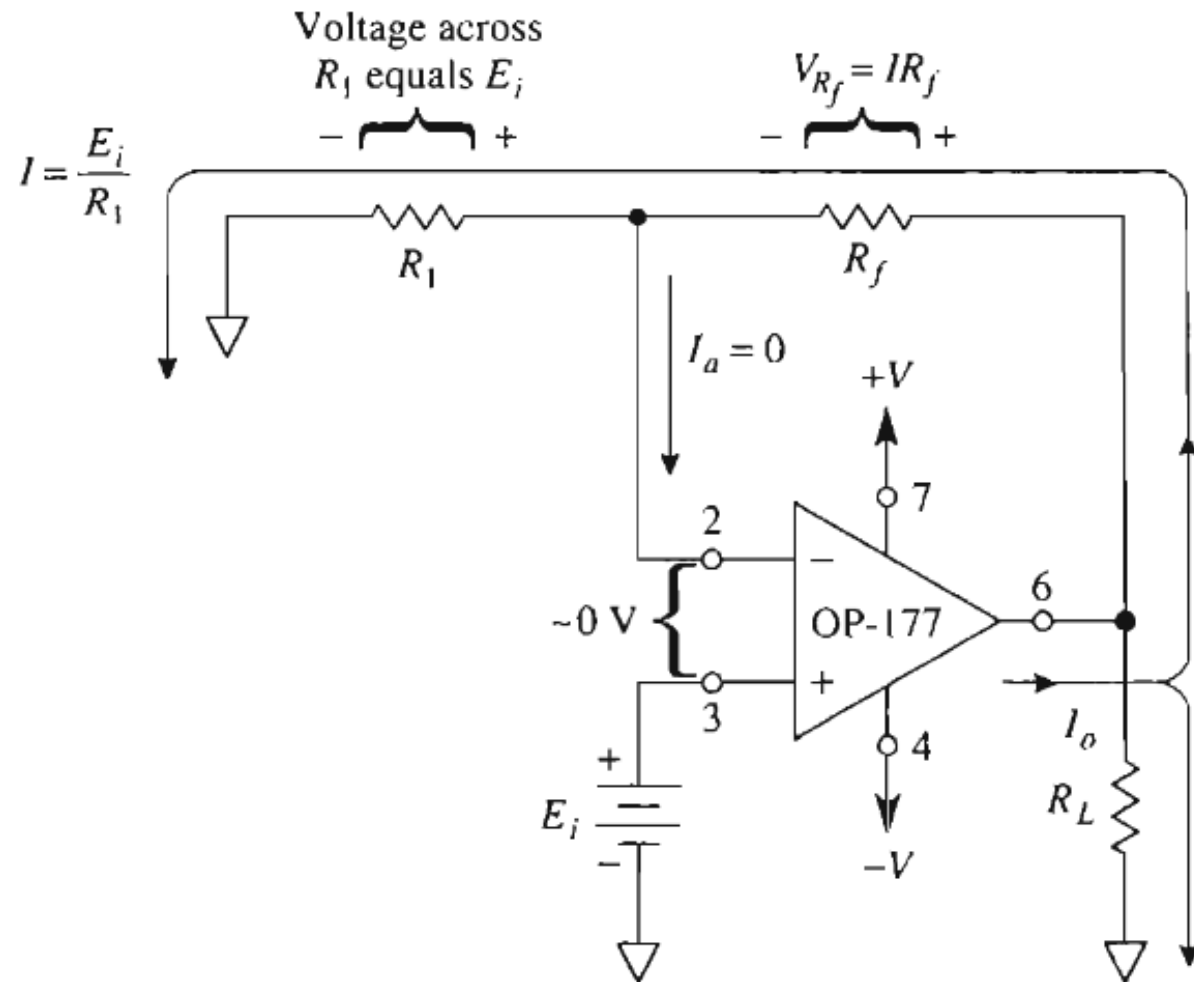
The given function can be achieved by an **inverting summing amplifier**. Having the voltage transfer formula as:

$$v_o = \left(-\frac{R_f}{R_1}v_1 - \frac{R_f}{R_2}v_2 - \cdots - \frac{R_f}{R_n}v_n \right)$$

Here, the numerators of all the coefficients of input voltages are same (R_f). As per the given problem, this can be achieved by setting the numerator to the LCM of 2 and 4 (i.e., to **8**).

$$\begin{aligned} v_o &= -\frac{2}{5}v_1 - \frac{4}{5}v_2 - \frac{4}{3}v_3 \\ &= -\frac{8}{20}v_1 - \frac{8}{10}v_2 - \frac{8}{6}v_3 \end{aligned}$$

Non-Inverting Amplifier



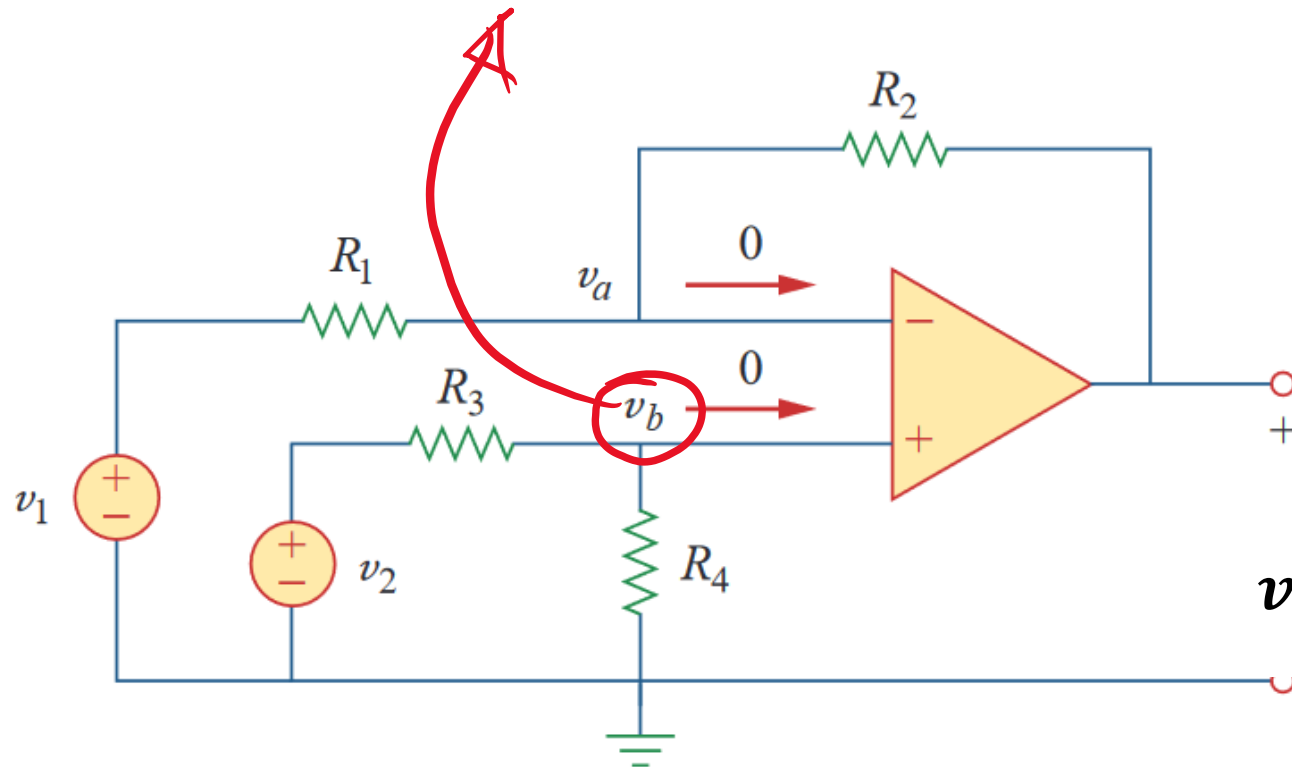
$$V_o > E_i$$

$$V_o = \left(1 + \frac{R_f}{R_1}\right)E_i$$

Difference Amplifier

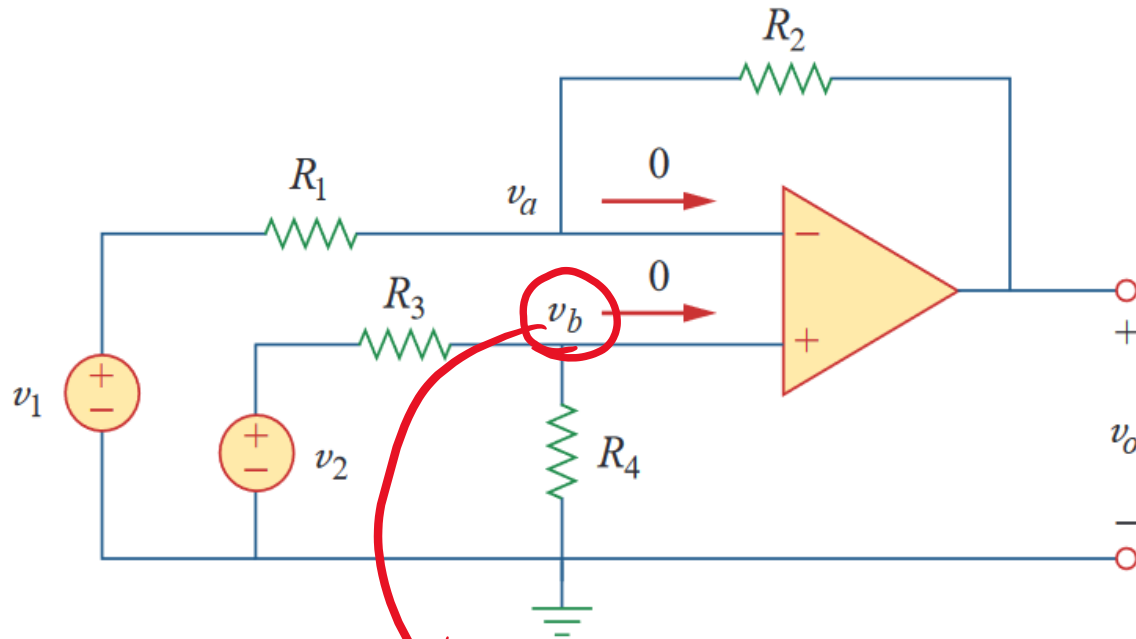
$$v_b = v_a = \frac{R_4}{R_3 + R_4} v_2$$

$$v_o = \left(1 + \frac{R_2}{R_1}\right) v_a - \frac{R_2}{R_1} \cdot v_1$$

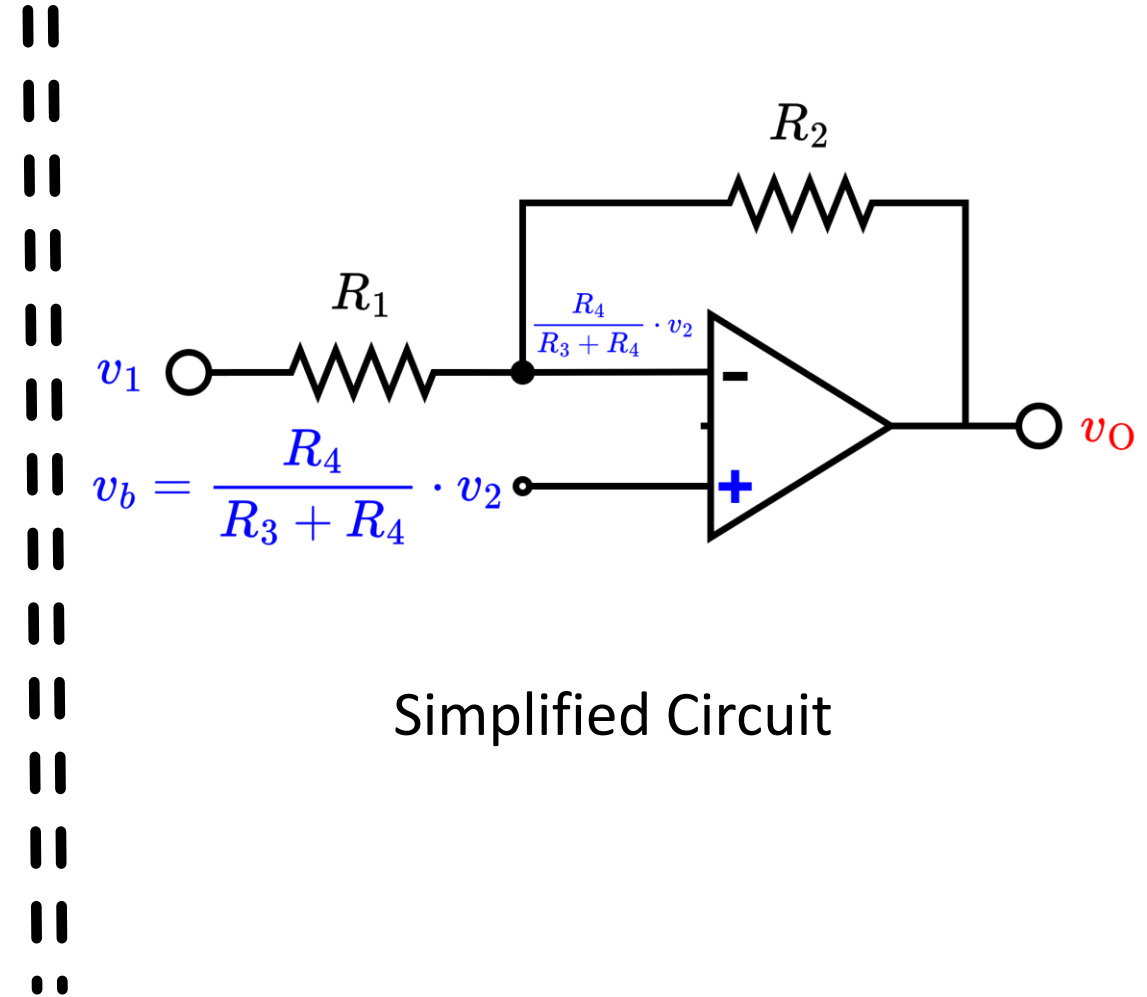


$$v_o = \left(1 + \frac{R_2}{R_1}\right) \cdot \frac{R_4}{R_3 + R_4} v_2 - \frac{R_2}{R_1} \cdot v_1$$

Difference Amplifier

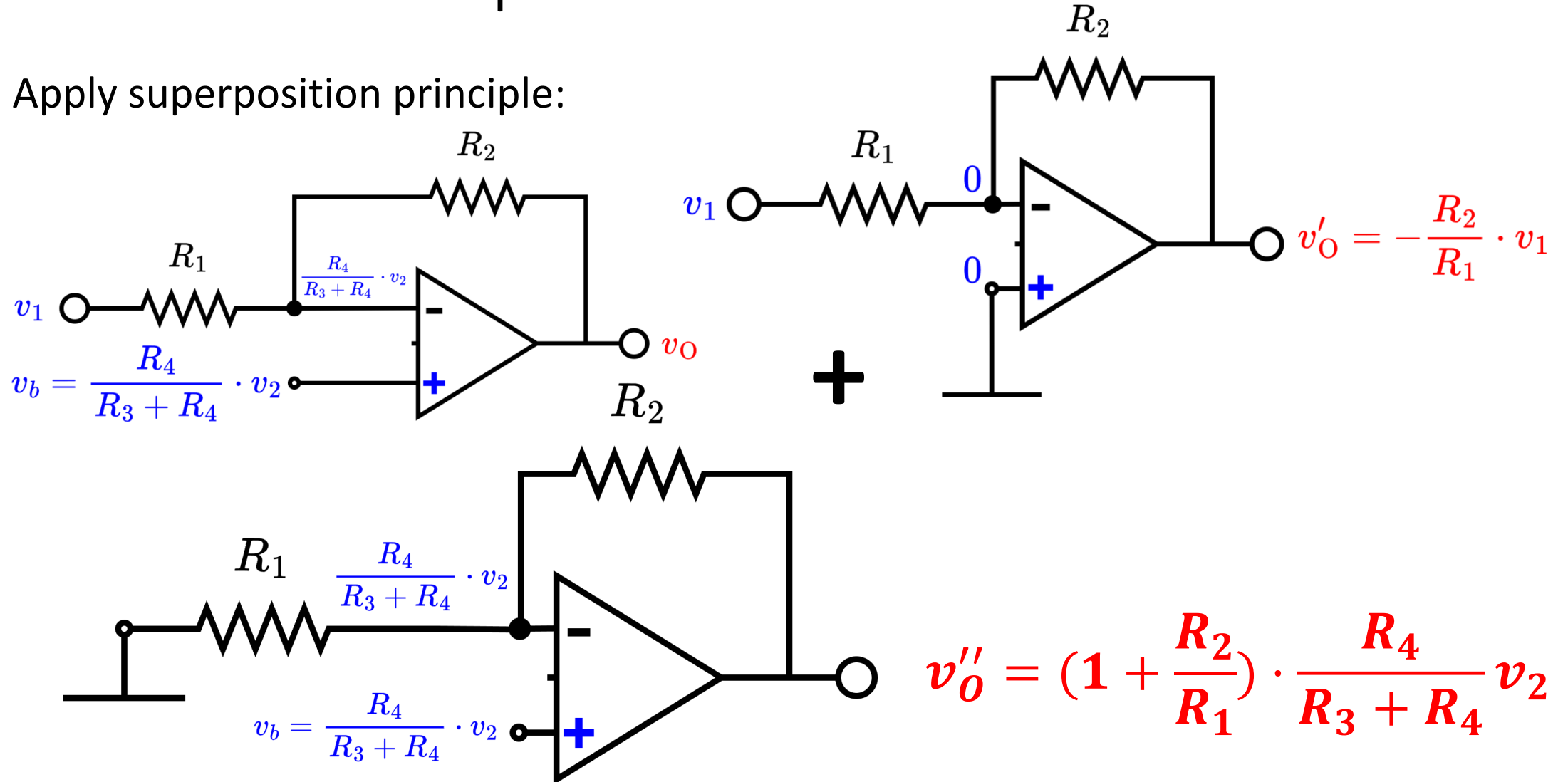


$$v_b = v_a = \frac{R_4}{R_3 + R_4} v_2$$



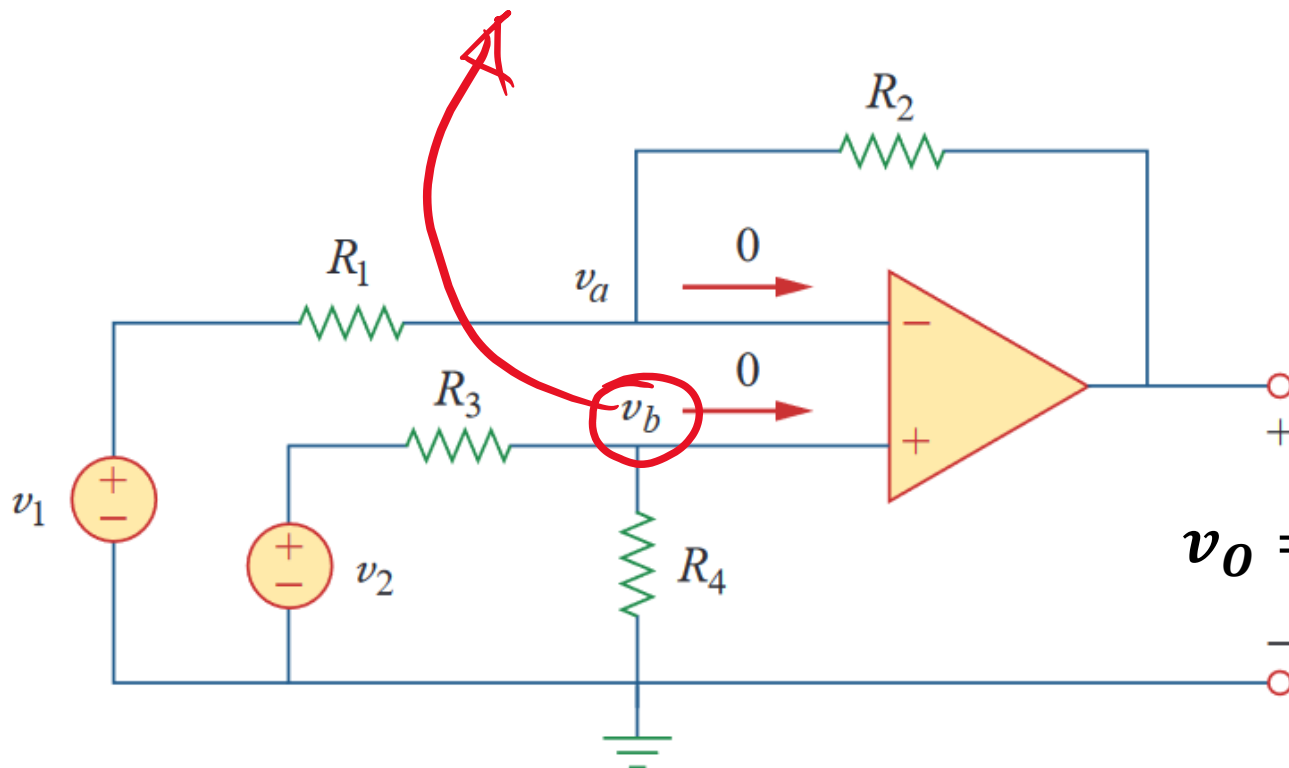
Difference Amplifier

Apply superposition principle:



Difference Amplifier

$$v_b = v_a = \frac{R_4}{R_3 + R_4} v_2$$



$$v_o = v_o'' + v_o'$$

$$v_o = \left(1 + \frac{R_2}{R_1}\right) v_b - \frac{R_2}{R_1} \cdot v_1$$

$$v_o = \left(1 + \frac{R_2}{R_1}\right) \cdot \frac{R_4}{R_3 + R_4} v_2 - \frac{R_2}{R_1} \cdot v_1$$

Difference Amplifier – Example 5

Design an op amp circuit with inputs v_1 and v_2 such that

$$v_o = -5v_1 + 3v_2.$$

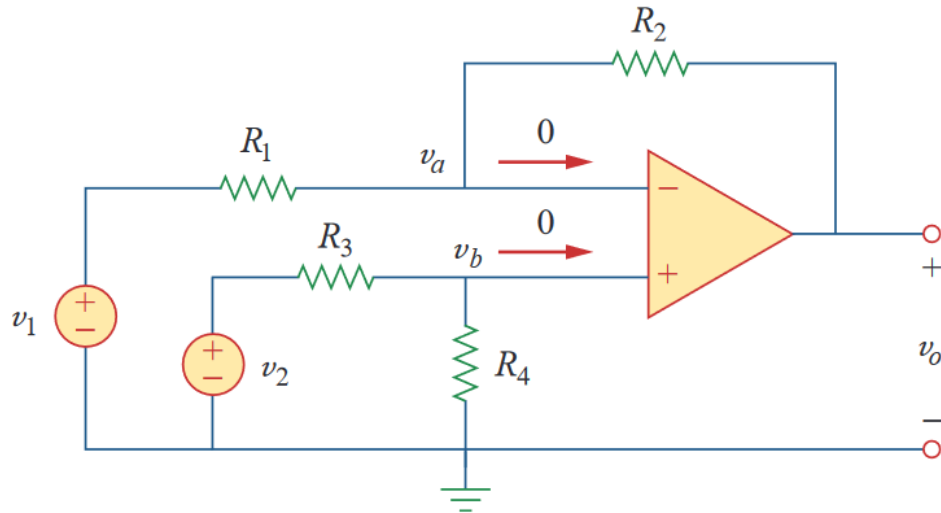
Solution: Method 1

$$v_o = -\frac{R_2}{R_1} \cdot v_1 + \left(1 + \frac{R_2}{R_1}\right) \cdot \frac{R_4}{R_3 + R_4} v_2$$

$$\therefore \frac{R_2}{R_1} = 5$$

$$\therefore (1 + 5) \cdot \frac{R_4}{R_3 + R_4} = 3$$

$$\Rightarrow R_3 = R_4$$



Difference Amplifier – Example 6

Design an op amp circuit with inputs v_1 and v_2 such that

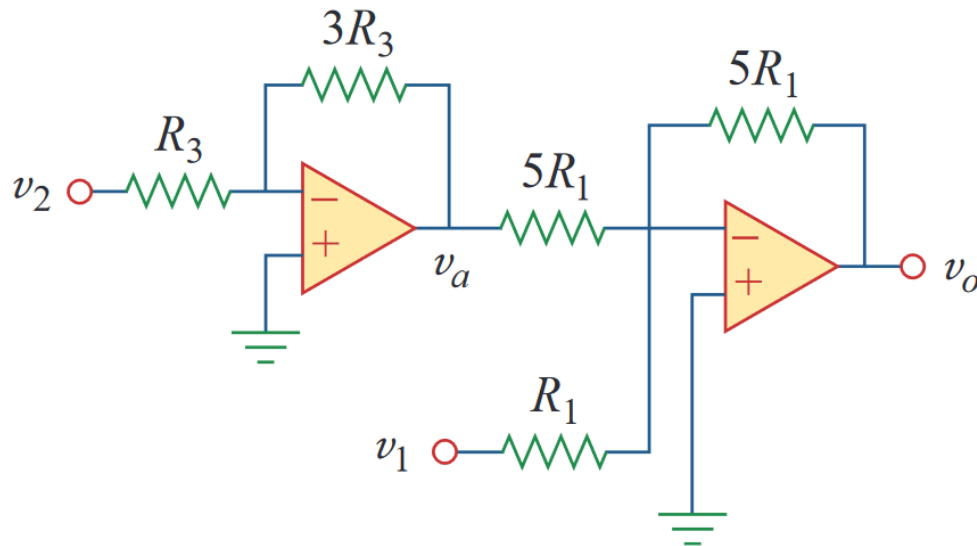
$$v_o = -5v_1 + 3v_2.$$

Solution: Method 2

Using two stages, we can implement this function.

$-5v_1$: Can be achieved with one stage inverting amplifier

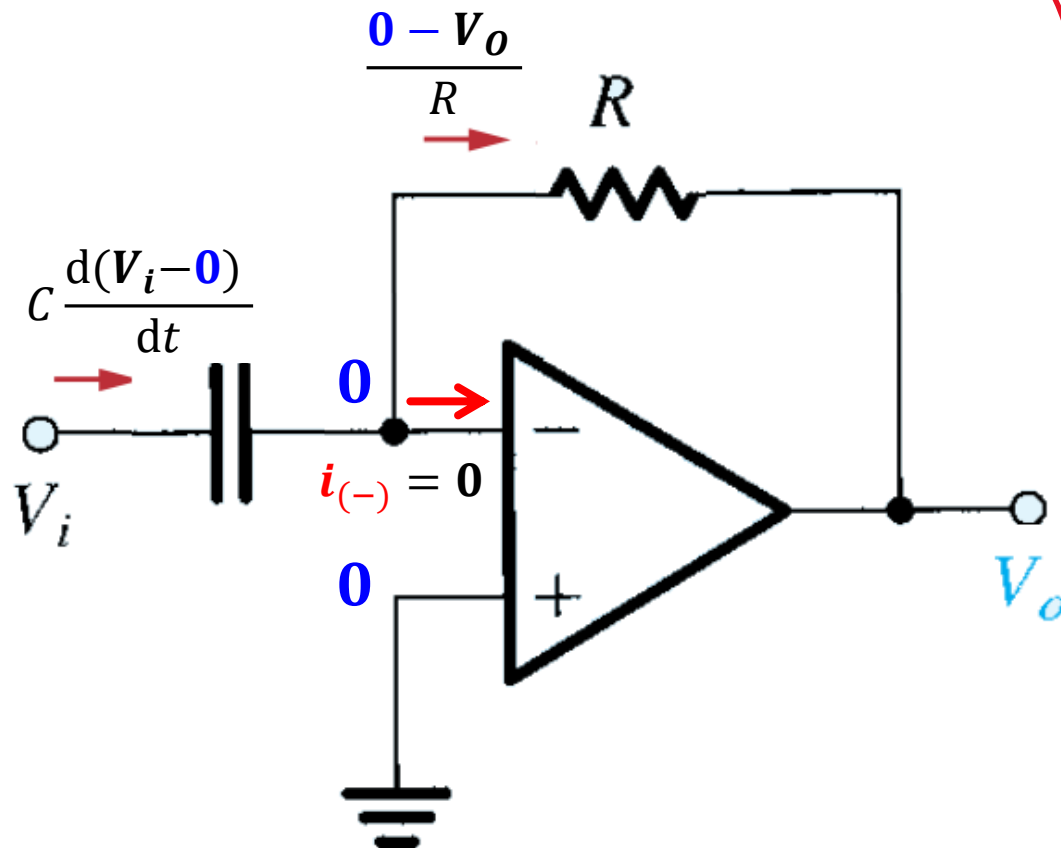
$+3v_2$: Can be achieved by cascading two inverting amplifiers $\rightarrow (- \times - = +)$



Subtractor ($E_1 - E_2$)

Op Amp as Differentiator

Since ideal op-amp, $i_- = i_+ = 0$, so $i_1 = i_2$



Review – Capacitor

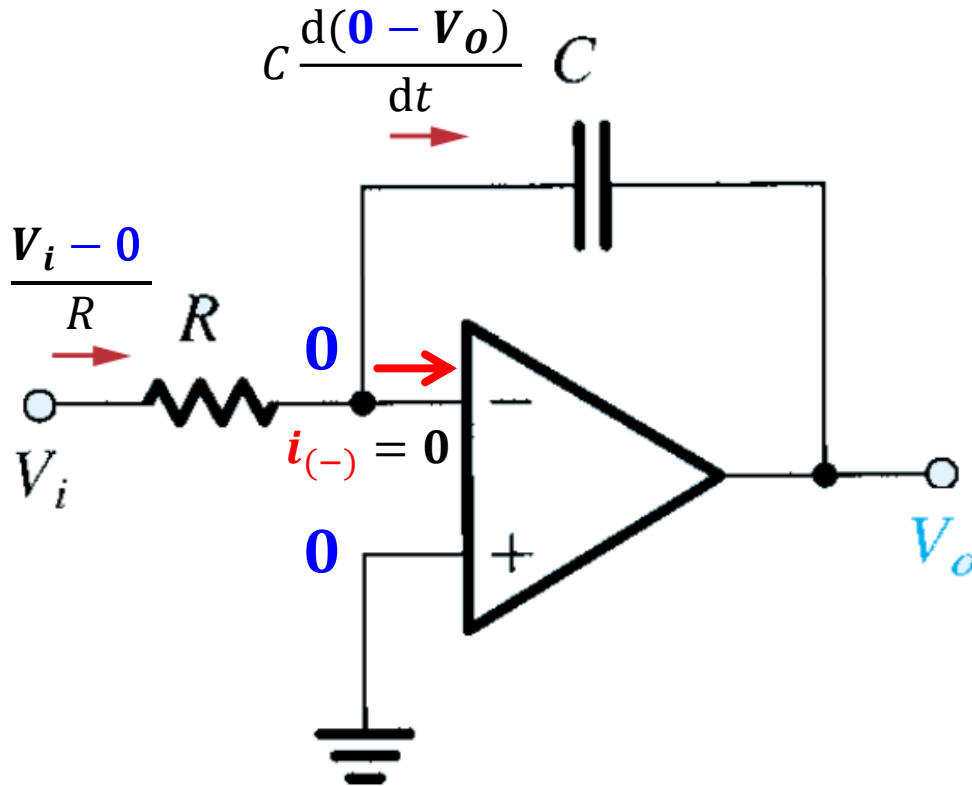
$$\begin{array}{c} i_1 \rightarrow \\ v_1 \text{ --- } \parallel \text{ --- } v_2 \\ + \quad v_C \quad - \\ i_1 = C \frac{dv_C}{dt} = C \frac{d(v_1 - v_2)}{dt} \end{array}$$

$$\Rightarrow -\frac{V_o}{R} = C \frac{dV_i}{dt}$$

$$\Rightarrow V_o = -RC \frac{dv_i}{dt}$$

Op Amp as Integrator

Since ideal op-amp, $i_- = i_+ = 0$, so $i_1 = i_2$



Review – Capacitor

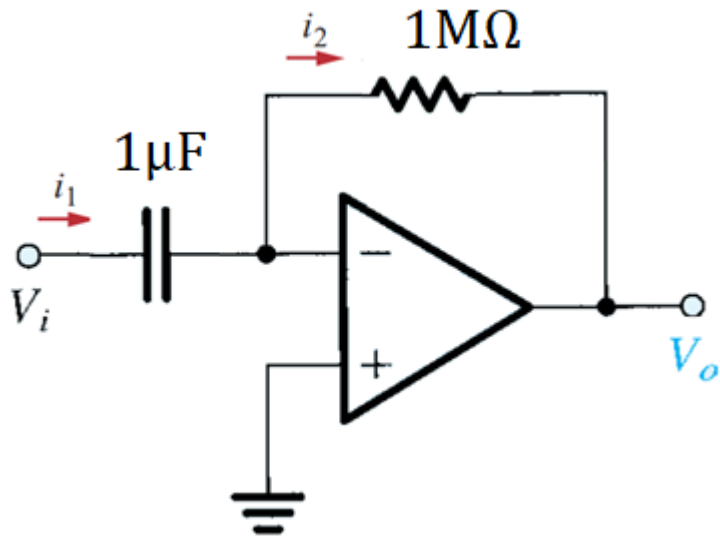
$$\begin{array}{c} i_1 \rightarrow \\ v_1 \text{ --- } \parallel \text{ --- } v_2 \\ \quad \quad \quad + \quad v_C \quad - \\ i_1 = C \frac{dv_C}{dt} = C \frac{d(v_1 - v_2)}{dt} \end{array}$$

$$\Rightarrow \frac{V_i}{R} = -C \frac{dV_o}{dt}$$

$$\Rightarrow V_o = -\frac{1}{RC} \int V_i(t) dt$$

Example 8

Observe the following Figure. If $V_i = 5 \cdot \sin(6t)$, Find the value of V_o



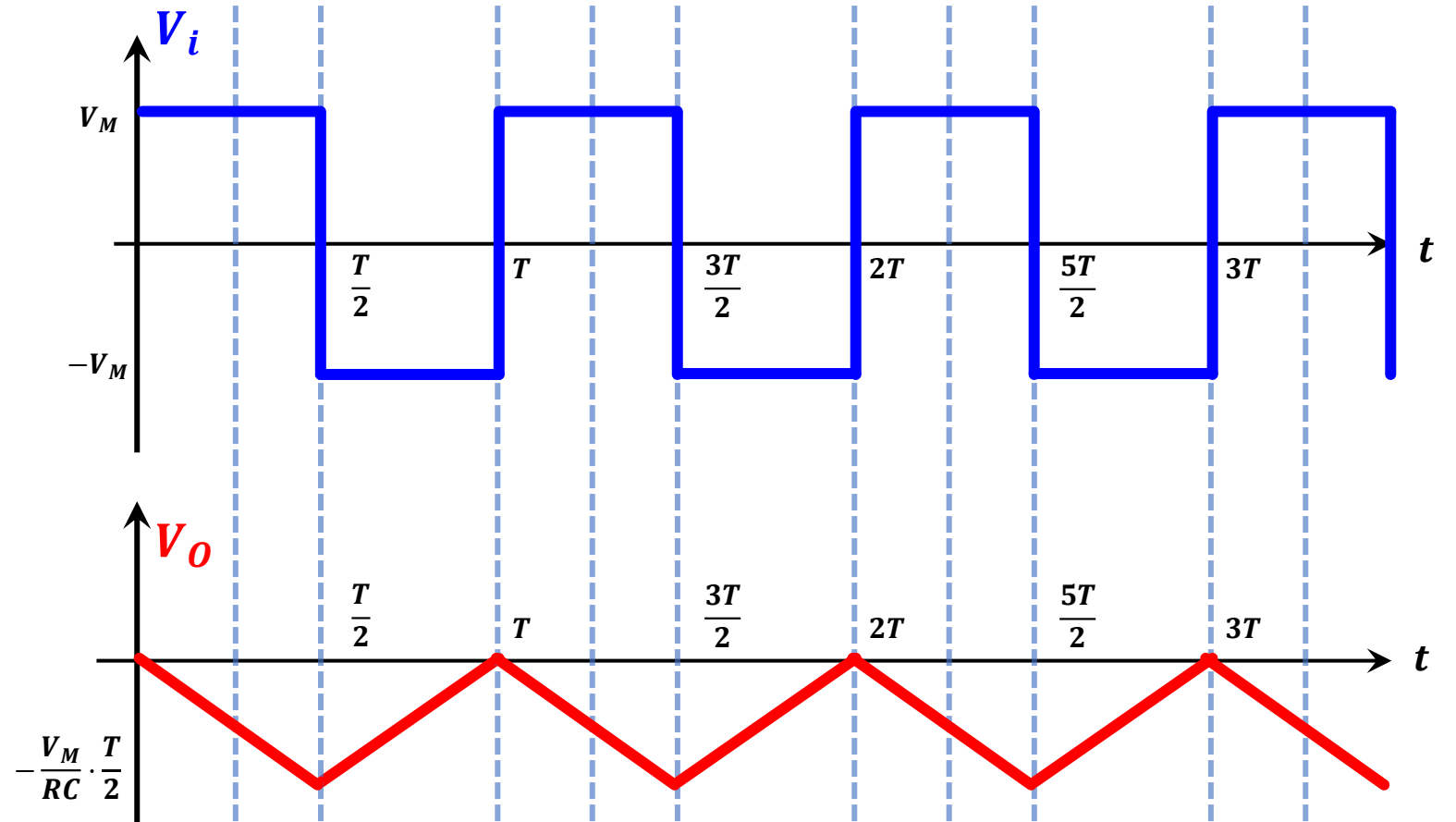
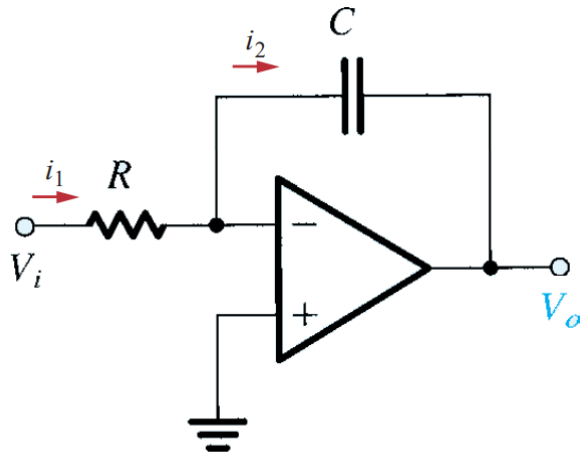
Solution:

This is a **differentiator**.

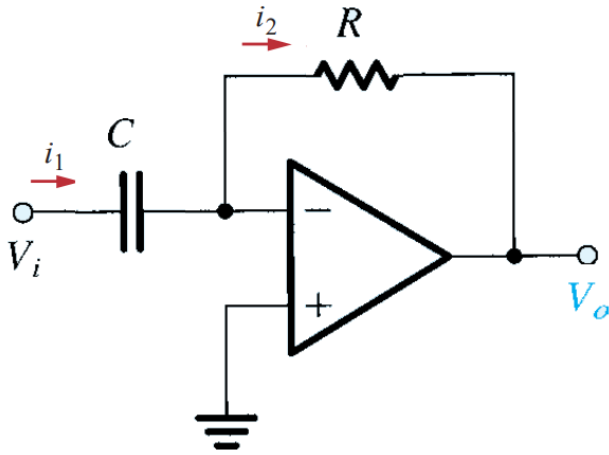
$$\text{So, } v_o = -RC \frac{dV_i}{dt} = -1 \times 10^6 \times 1 \times 10^{-6} \times \frac{d(5 \cdot \sin(6t))}{dt}$$

$$\Rightarrow v_o = -1 \times (5 \times 6 \cos(6t)) = \mathbf{-30 \cos(6t) \text{ [Ans.]}}$$

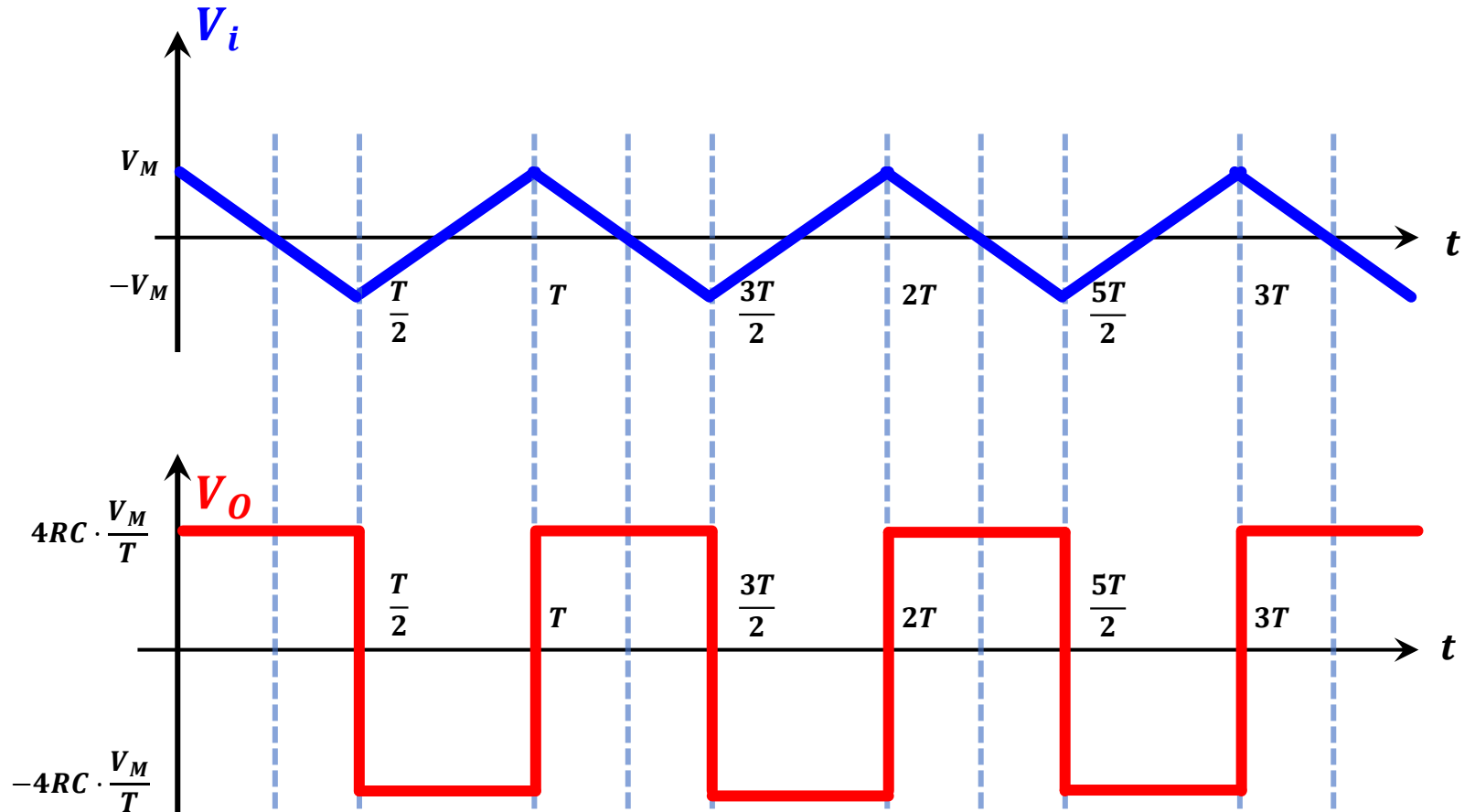
Example 9



Example 9



$$\text{Slope: } \left| \frac{dv}{dt} \right| = \frac{V_M - (-V_M)}{T/2} = \frac{4V_M}{T}$$



APPLICATIONS:

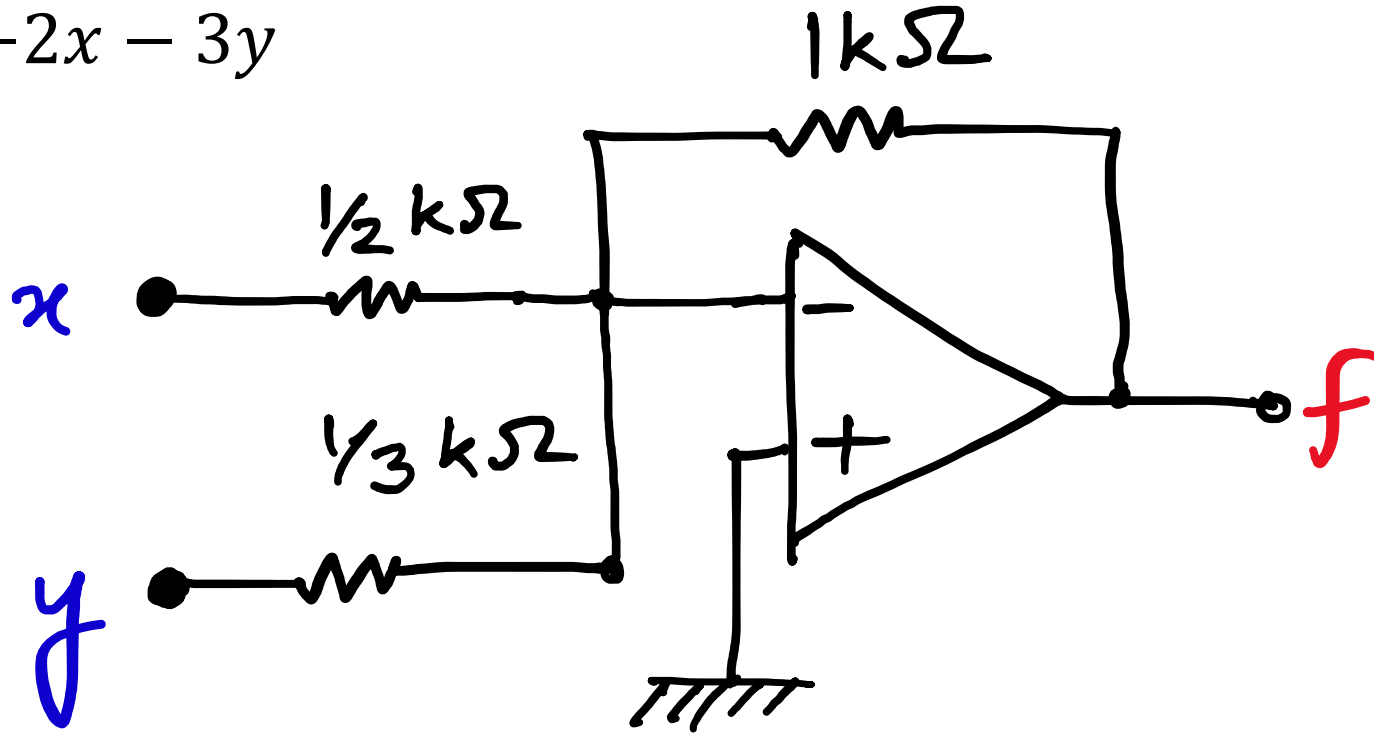
Implementing operational functions

- $f = -2x - 3y$
- $f = -4x + 5y$
- $f = -7x + \frac{d}{dt}y$
- $f = -\frac{1}{3} \int x \cdot dt + 2 \ln y + 4z$
- $f = -3 \frac{dx}{dt} + 2 \exp(y) + 4z$
- $f = xy/z$

APPLICATIONS:

Implementing operational functions

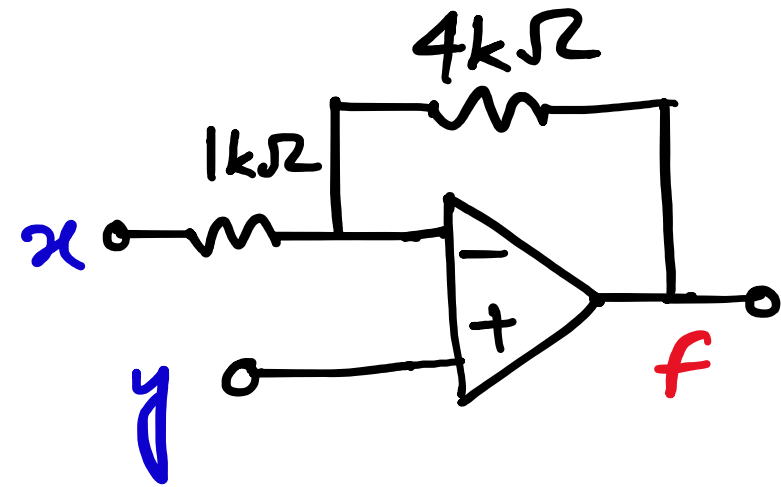
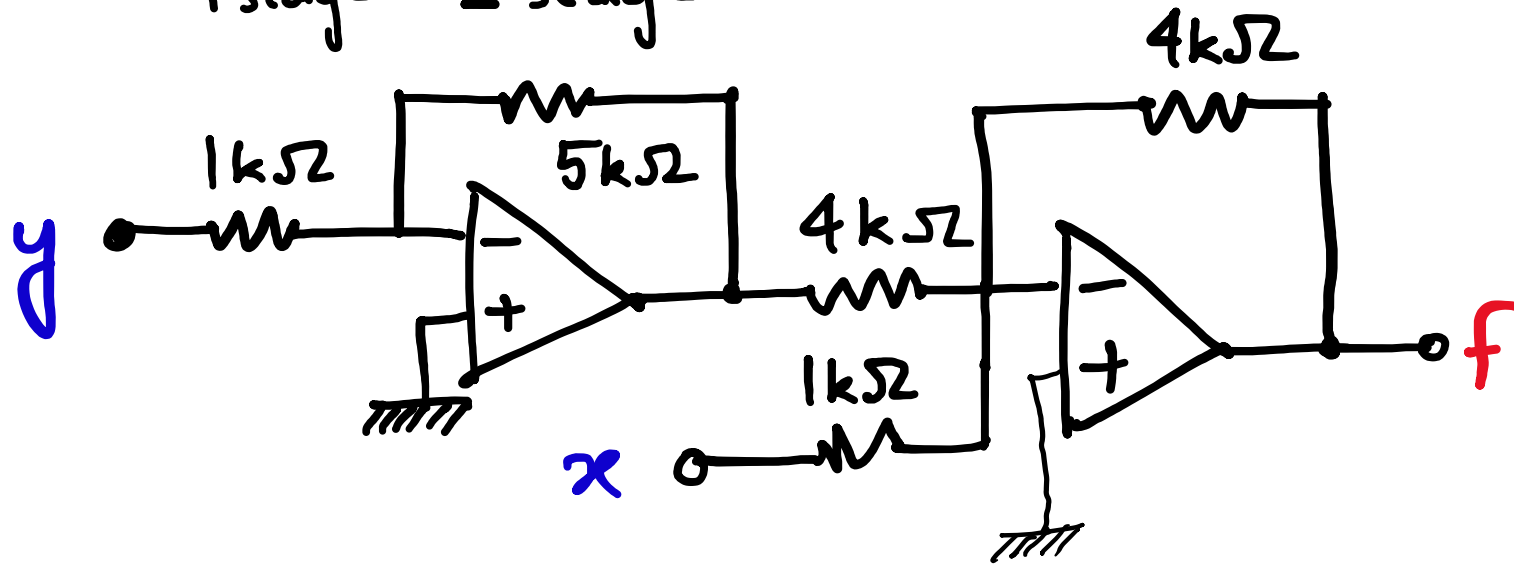
- $f = -2x - 3y$



APPLICATIONS:

Implementing operational functions

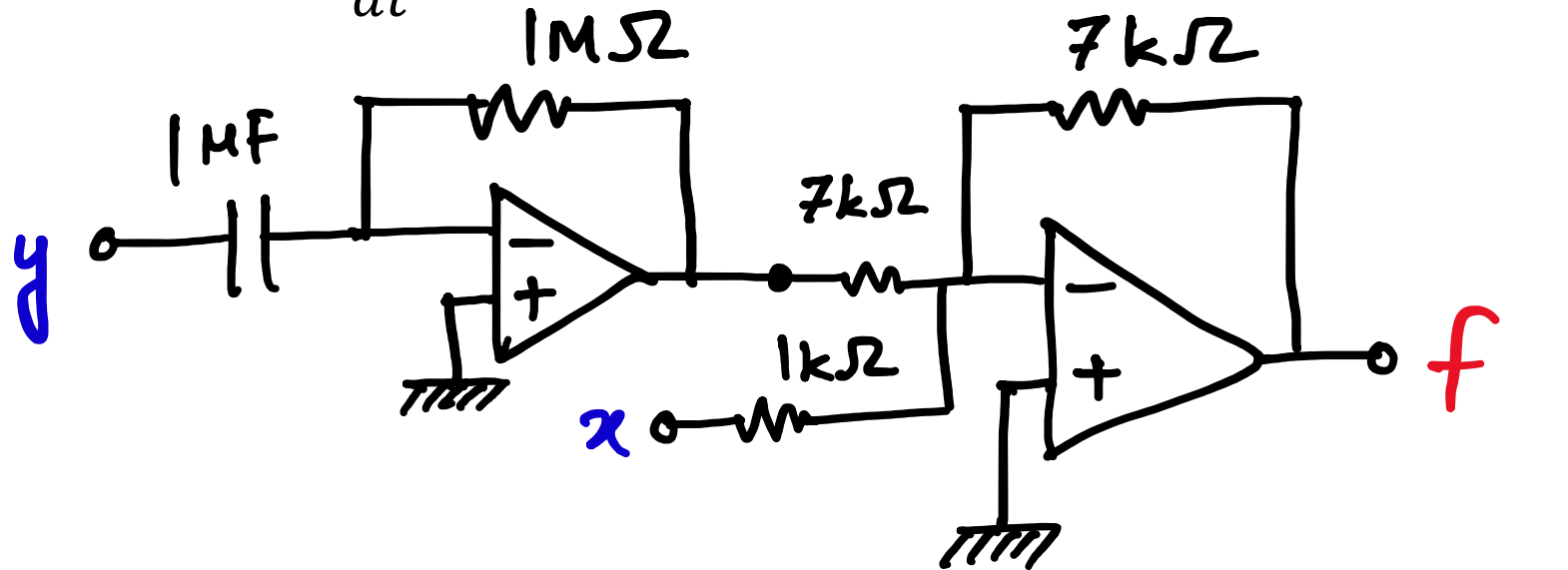
- $f = -4x + 5y$
1 stage 2 stage



APPLICATIONS:

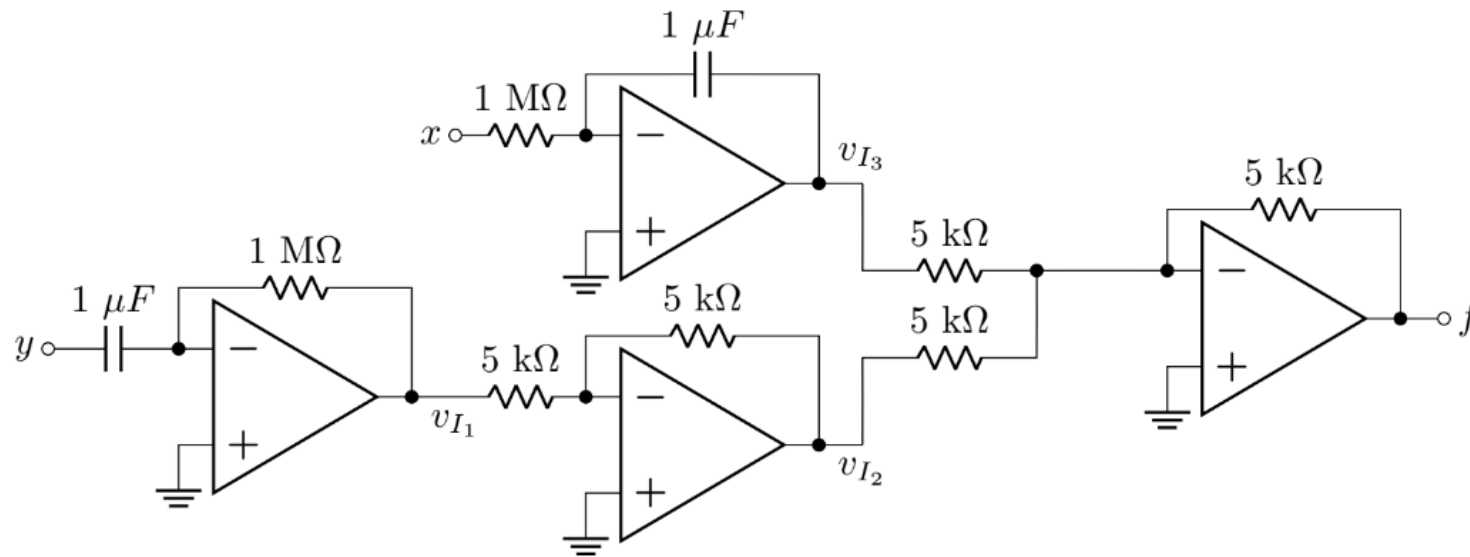
Implementing operational functions

- $f = -7x + \frac{d}{dt}y$



Example

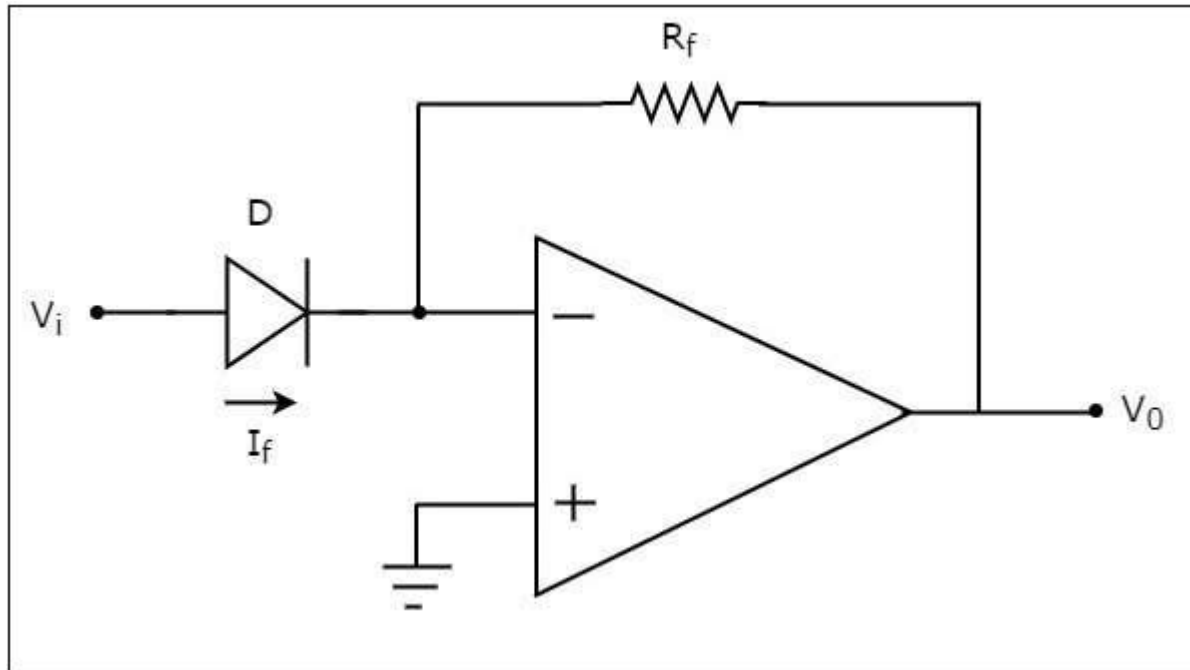
Analyze the circuit below to **find** an expression of f in terms of inputs x and y .



Solution:

$$v_{f1} = -\frac{dy}{dt}; v_{f2} = -\frac{1}{RC} \int x\ dt; v_{f3} = -v_{f1} = \frac{dy}{dt}; v_o = -(v_{f1} + v_{f2} + v_{f3})$$

Exponential (Anti-log) Converter

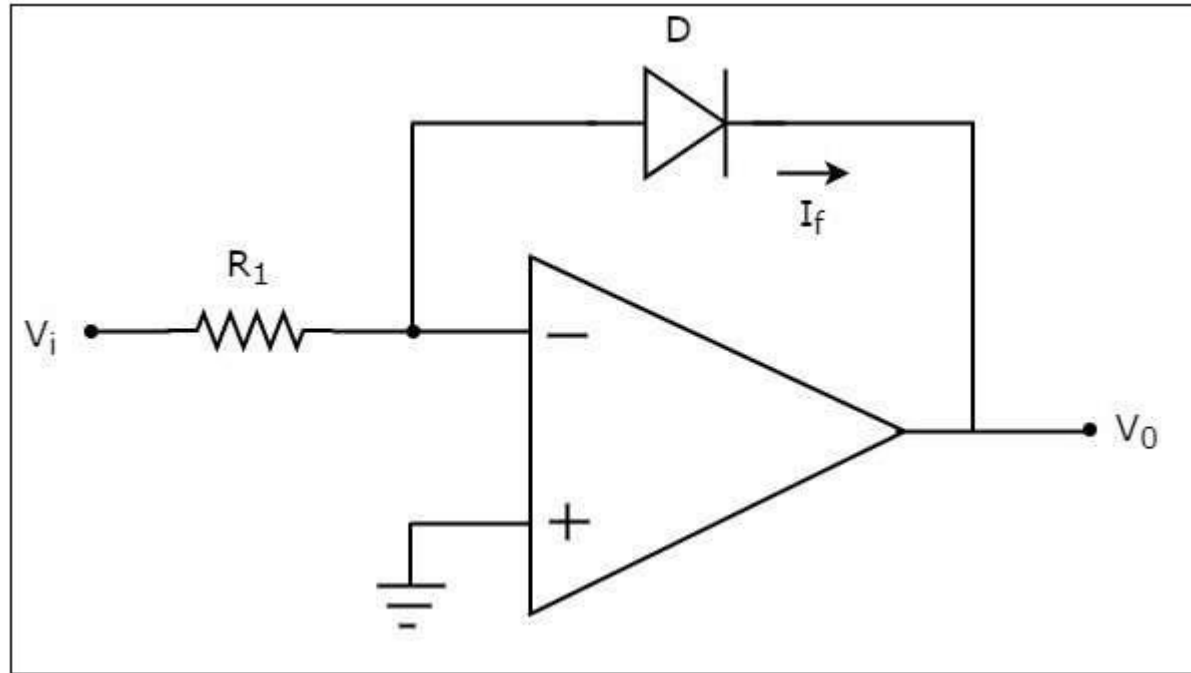


$$I_f = I_S \exp\left(\frac{V_i - 0}{V_T}\right)$$

$$\frac{0 - V_o}{R_f} = I_S \exp\left(\frac{V_i}{V_T}\right)$$

$$V_o = I_S R_f \cdot \exp\left(\frac{V_i}{V_T}\right)$$

Logarithmic Amplifier



$$I_f = I_S \exp\left(-\frac{V_O}{V_T}\right)$$

$$\frac{V_i}{R_1} = I_S \exp\left(-\frac{V_O}{V_T}\right)$$

$$\frac{V_i}{I_S R_1} = \exp\left(-\frac{V_O}{V_T}\right)$$

$$V_O = -V_T \cdot \ln\left(\frac{V_i}{I_S R_1}\right)$$

APPLICATIONS:

Implementing operational functions

- $f = -\frac{1}{3} \int x \cdot dt + 2 \ln y + 4z$

APPLICATIONS:

Implementing operational functions

- $f = -3 \frac{dx}{dt} + 2 \exp(y) + 4z$

Multiplier

$$f = xy$$

$$\ln(f) = \ln(xy) = \ln(x) + \ln(y)$$

$$f = \exp(\ln(x) + \ln(y))$$

So,

$$f = \exp(z) \text{ where } z = \ln(x) + \ln(y)$$

Divider

$$f = xy/z$$

$$\ln(f) = \ln(xy/z) = \ln(x) + \ln(y) - \ln(z)$$

$$f = \exp(\ln(x) + \ln(y) - \ln(z))$$

So,

$$f = \exp(z) \text{ where } z = \ln(x) + \ln(y) - \ln(z)$$

APPLICATIONS:

Implementing operational functions

- $f = xy/z$