



CS & IT ENGINEERING

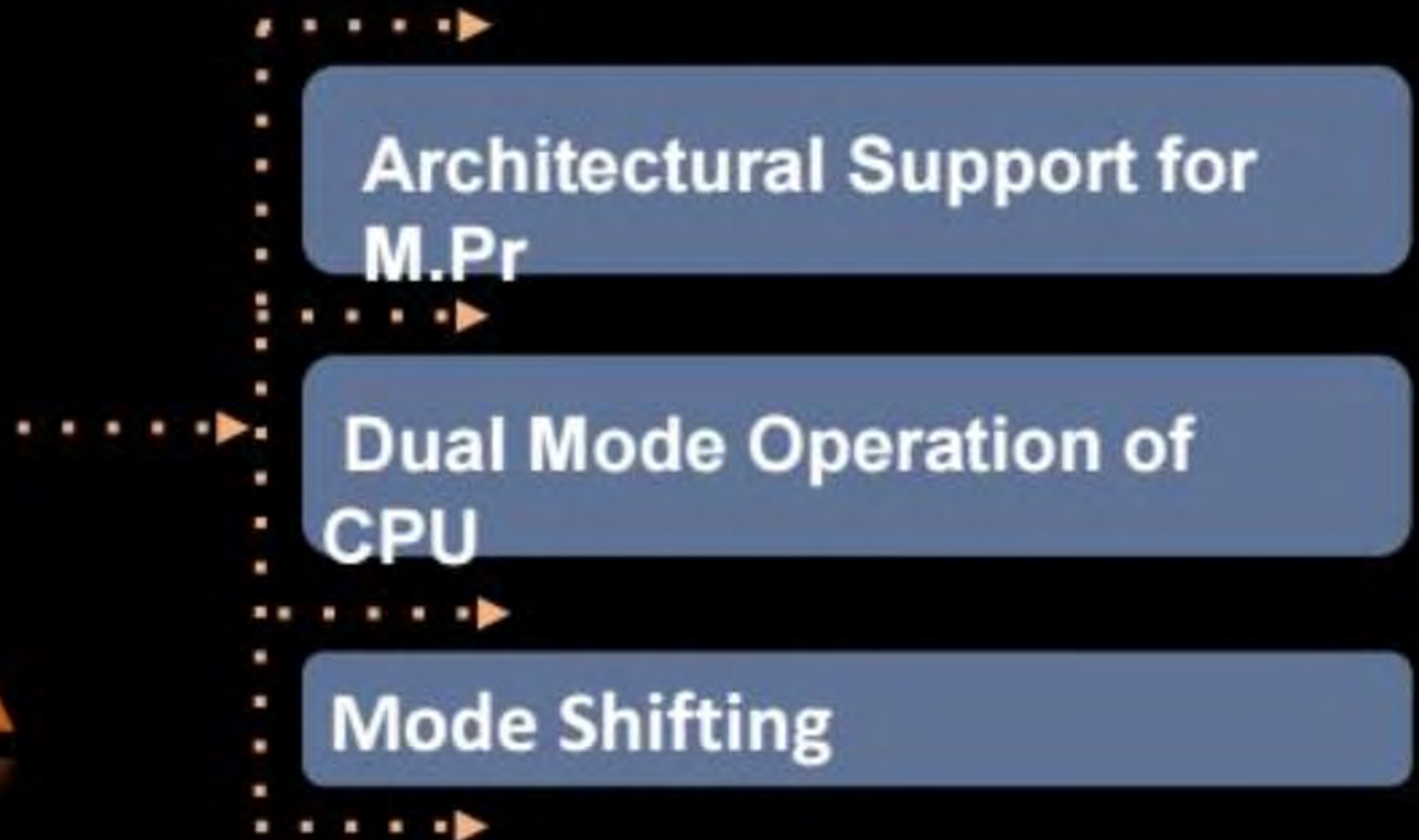
Operating Systems

Introduction & Background

Lecture No. 3



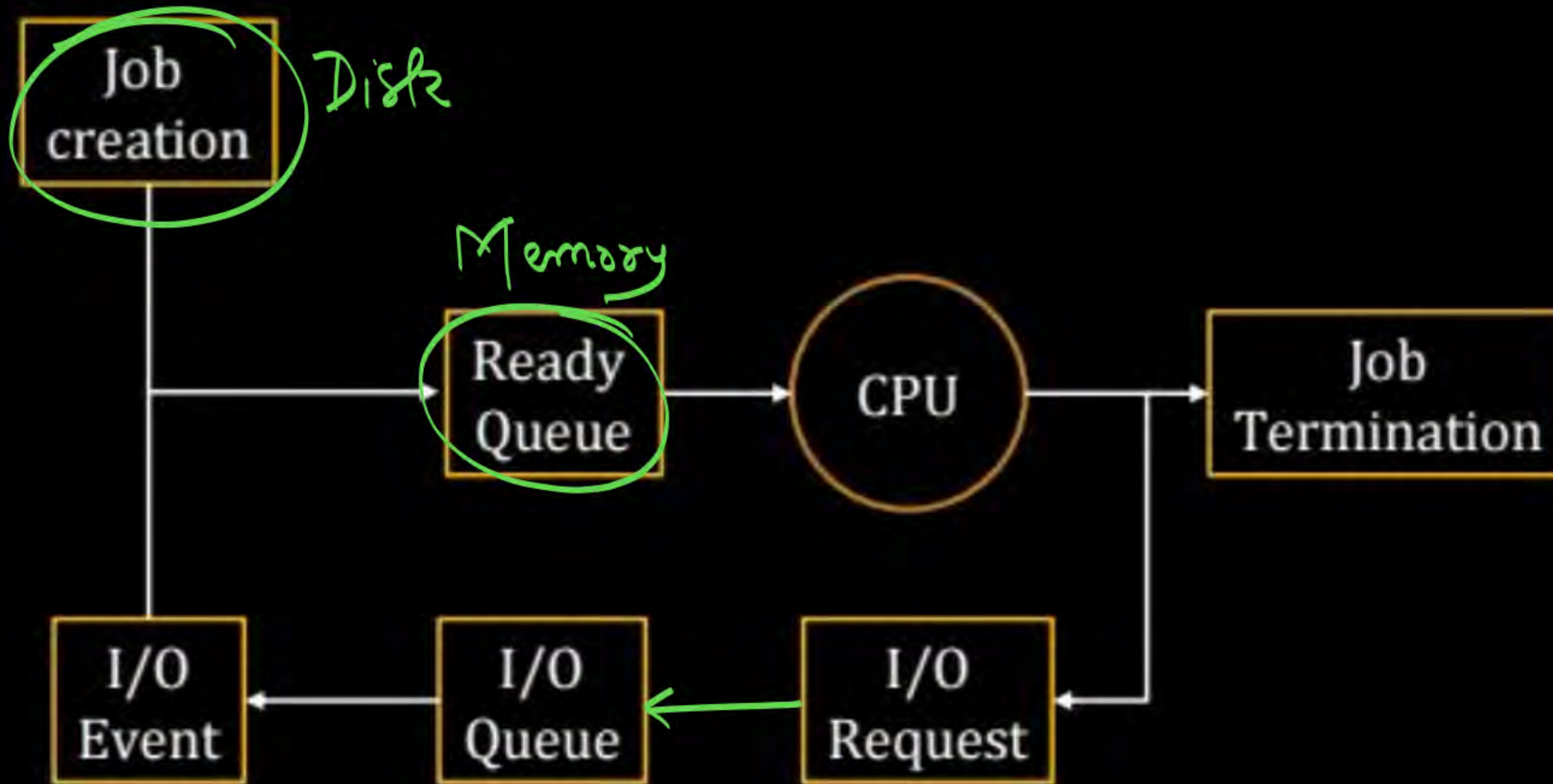
By- Dr. Khaleel Khan sir



Program VS Process

Multiprogramming

A Schematic view of Multiprogramming



(iii) Dual Mode operation :

- User Mode (u.m) / Non-Privileged
- Kernel Mode (k.m)

Privileged Mode

Protected Mode
System II

u.m

1. user appl's runs in u.m
2. u.m is PreEmptive (Non-Atomic)

k.m

1. o.s routines/services runs in k.m
2. k.m is Non-Pr (Atomic)

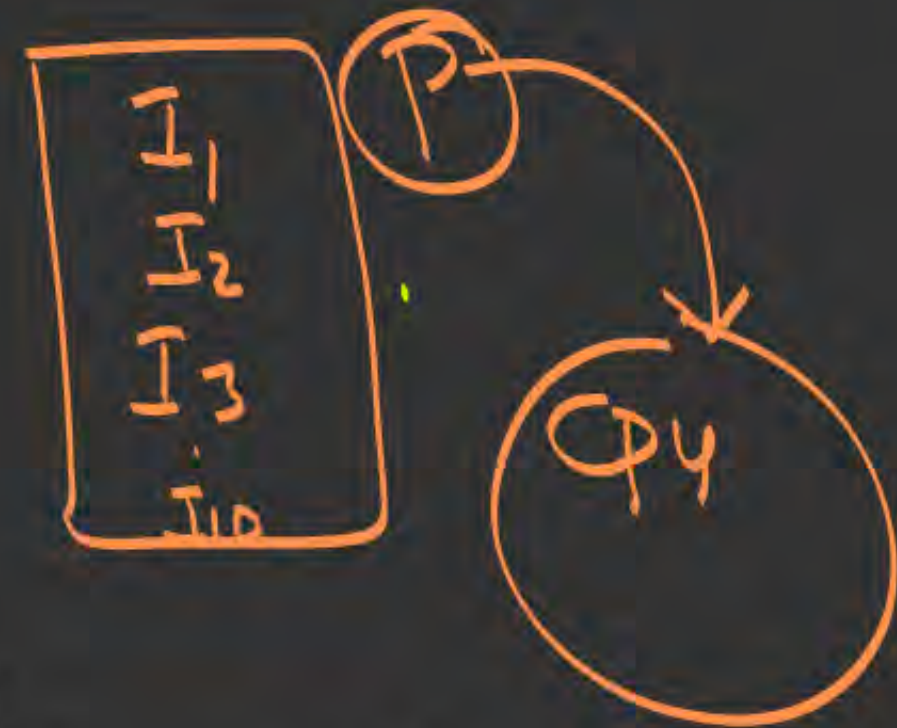
0: k.m
1: u.m
Mode bit

CPU



PSW

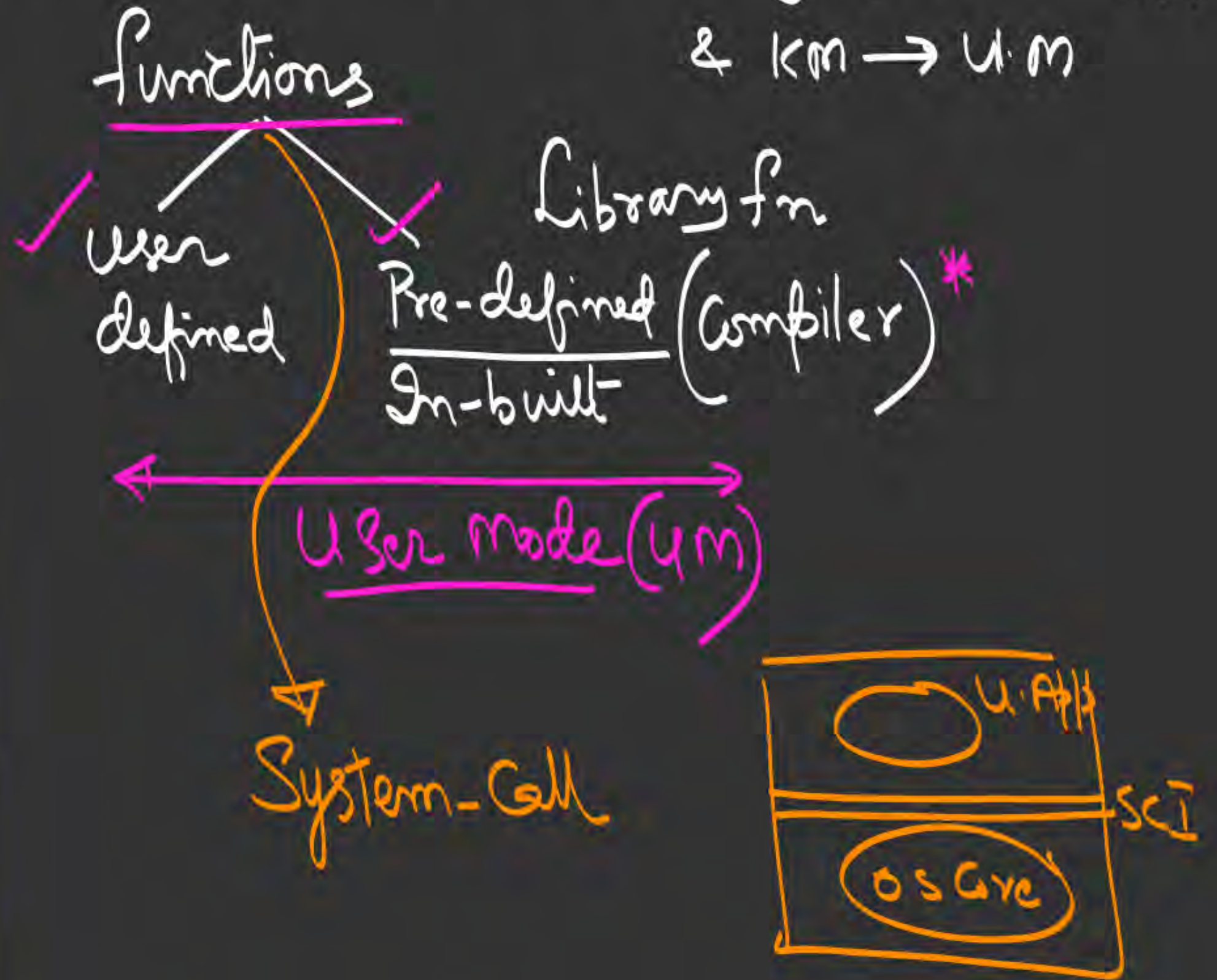
(H/W) < Processor Status word >



Mode Shifting: \rightarrow O.S is a Service Provider & user appl's may need
O.S Services(K.m); \therefore Mode Shifting from $u.m \rightarrow k.m$
& $k.m \rightarrow u.m$

```
main()  
{  
  int a, b, c;  
  1. a = 1;  
  2. b = 2;  
  3. c = a + b;  
  4. f(c);  
}
```

```
f(int k)  
{  
  sum(k);  
  printf(k);  
}
```




```

my.c **
main()
{
    int a, b, c;
    void f(int z) : u.m
    {
        printf("%d", z); u.m
    }
}

```

```

1. a = 1; u.m
2. b = 2; u.m
3. c = a + b; u.m
4. fork();
5. f(c);

```

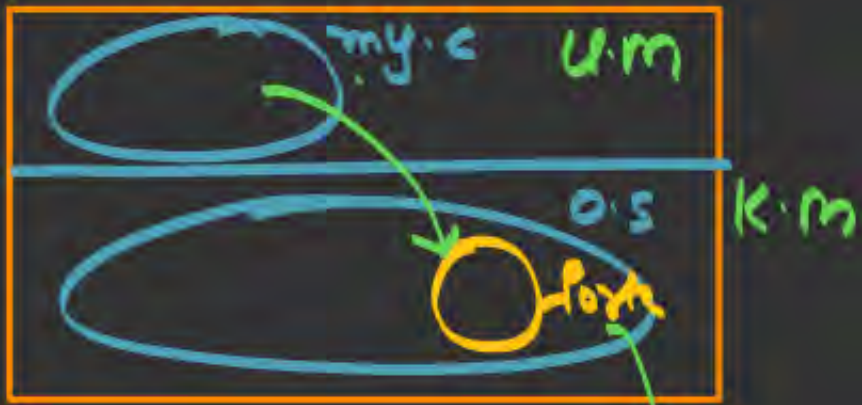
Library:

```

printf() : Library
um {

```

write(); Sys-call



Svc: Supervisory Call
 For Instr.
 (Creates a new Process)
 s/w Gnt. Instr.

BSA: Branch & Save Address
 Non-Privileged Instr.
 ISR: Interrupt Serv. Rtn

s/w Interrupt

ISR

1) change the Mode bit

2)

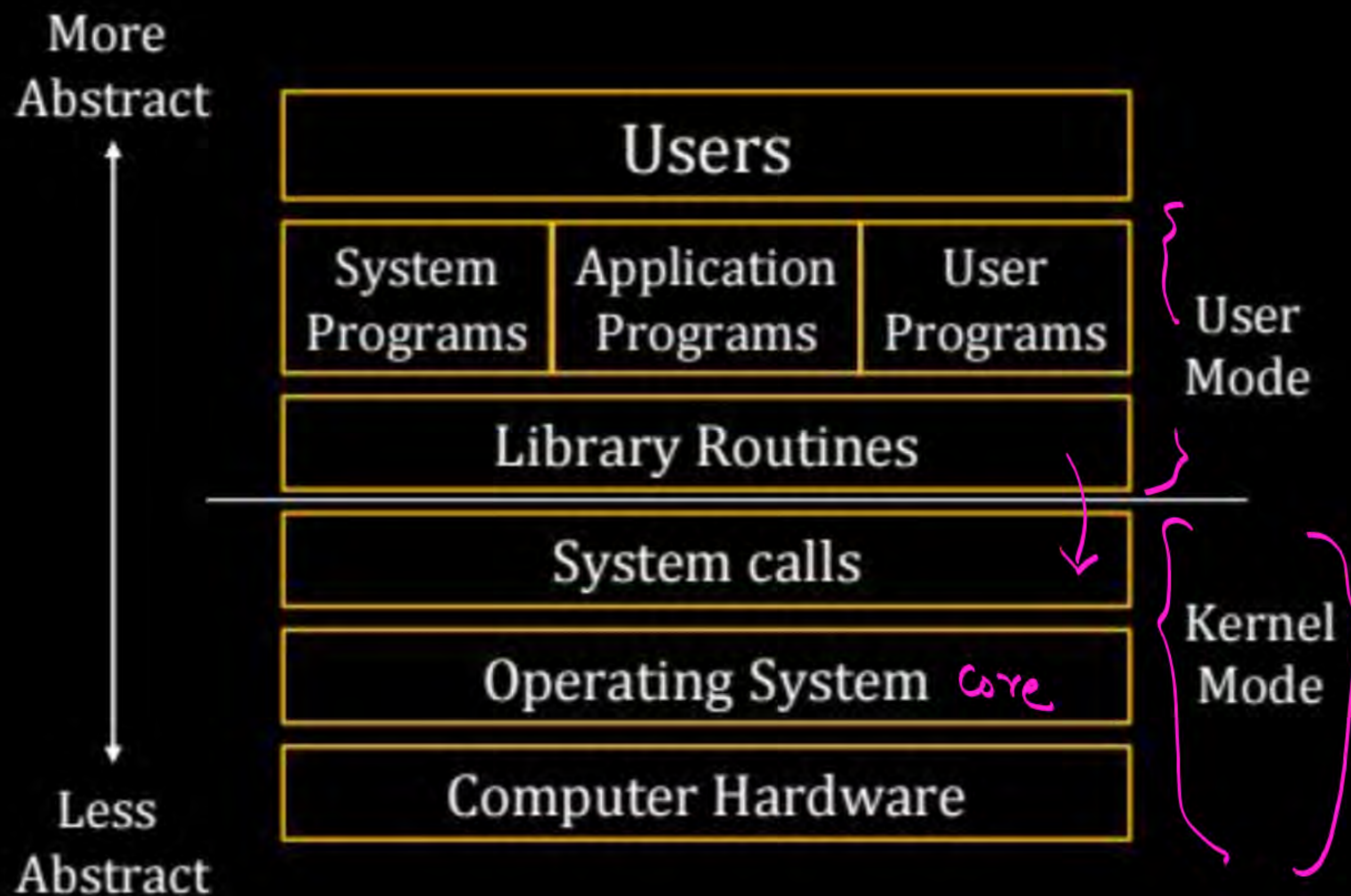
...	
...	
fork	(x)
...	

Dispatch Table

(x)
PC

OS





1. Kernel Mode

In Kernel mode, the executing code has complete and unrestricted access to the underlying hardware. It can execute any CPU instruction and reference any memory address. Kernel mode is generally reserved for the lowest-level, most trusted functions of the operating system. Crashes in kernel mode are catastrophic; they will halt the entire PC.

2. User Mode

In User mode, the executing code has no ability to directly access hardware or reference memory. Code running in user mode must delegate to system APIs to access hardware or memory. Due to the protection afforded by this sort of isolation, crashes in user mode are always recoverable. Most of the code running on your computer will execute in user mode.

User process

User Process
Executing

Calls System
call

Return from
System call

User mode
(Mode bit = 1)

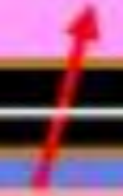
Kernel

Trap
Mode bit = 0

Return
Mode bit = 1

Kernel mode
(Mode bit = 0)

Execute System call



Q.1

A Processor needs Software Interrupt to



- ☐ A Test the Interrupt System of the Processor.
- ☐ B Implement Co-Routines.
- ☒ C Obtain system services which need execution of privileged instructions.
- ☐ D Return from subroutine.

Q.2



A CPU has two Modes-Privileged and Non-Privileged. In order to change the mode from Non-Privileged to Privileged.

- ☐ A A Hardware Interrupt is needed.
- ☒ B A Software Interrupt is needed.
- ☐ C A Privileged Instruction (which does not generate an interrupt) is needed.
- ☐ D A Non - Privileged Instruction (which does not generate an interrupt) is needed.

Q.3

System Calls are usually invoked by using:

- ☒ A A Software Interrupt
- ☐ B Polling
- ☐ C An Indirect jump
- ☐ D A Privileged Instruction.

A computer handles several interrupt sources of which the following are relevant for this question:

- Interrupt from CPU temperature sensor (raises interrupt if CPU temperature is too high)
- Interrupt from Mouse (raises interrupt if the mouse is moved or a button is pressed)
- Interrupt from Keyboard (raises interrupt if a key is pressed or released)
- Interrupt from Hard Disk (raises interrupt when a disk read is completed)

Which one of these will be handled at the HIGHEST priority?

A

Interrupt from Hard Disk

B

Interrupt from Mouse

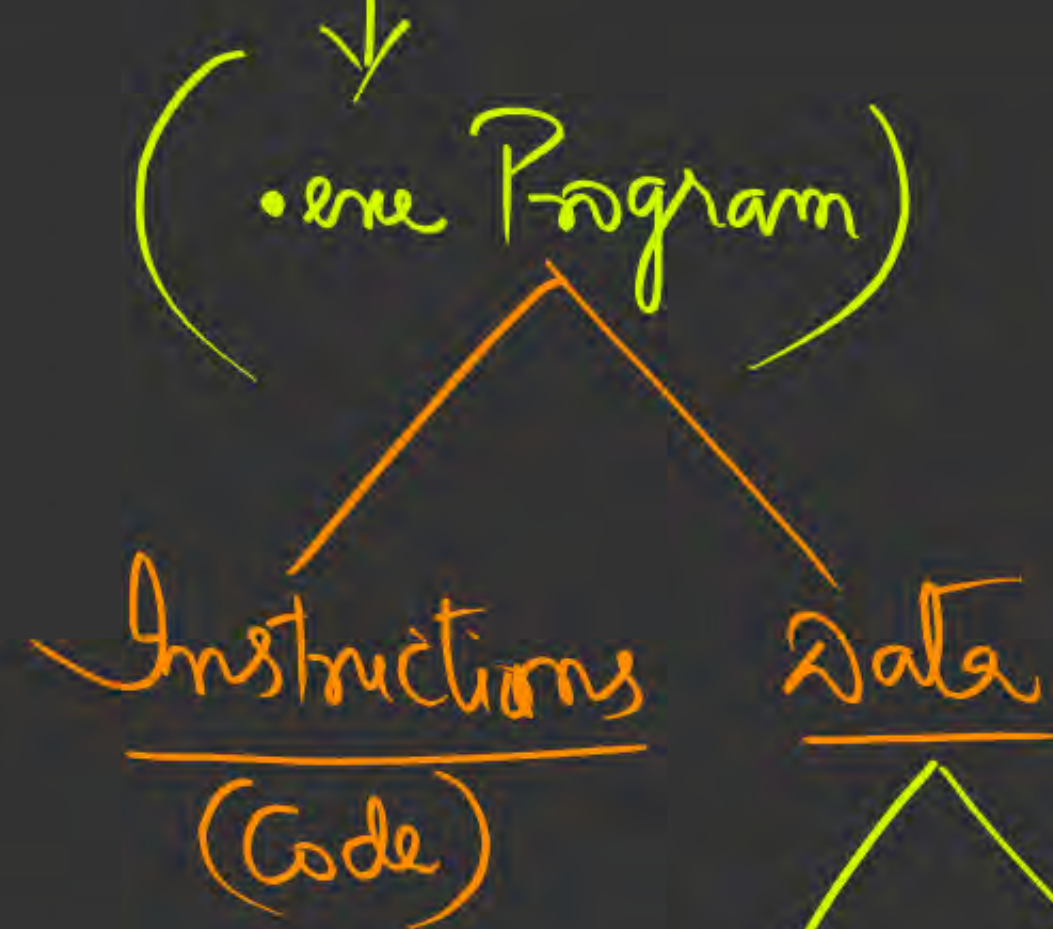
C

Interrupt from Keyboard

D

✓ Interrupt from CPU temperature

Program vs Process

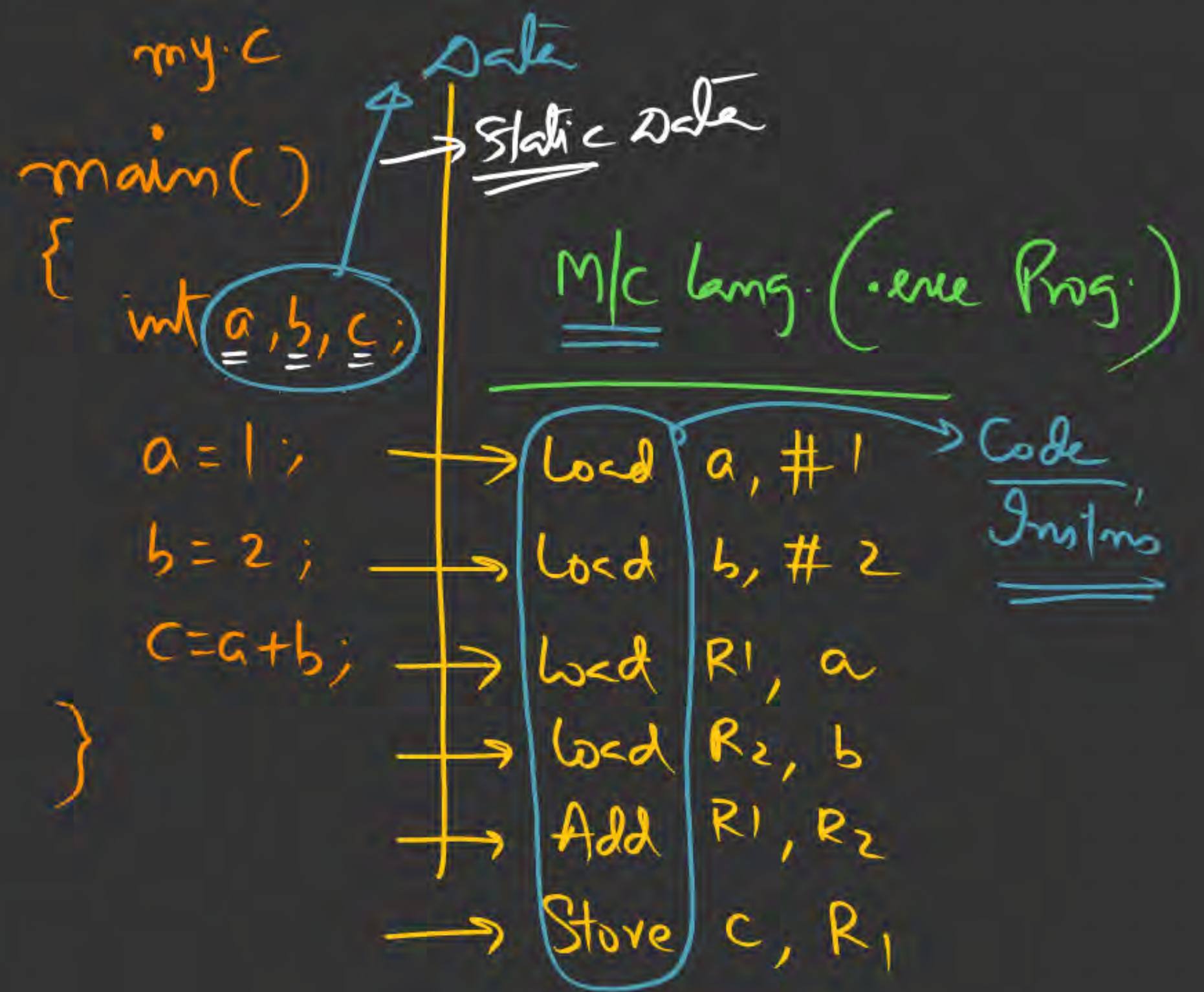


Static

- Fixed (Known) Size
- Alloc b/f Run Time

Dynamic

- Unknown Size (Variable Size)
- Alloc @ Run Time




```
main()  
{  
  int *pth;
```

```
  pth = (int*) malloc(sizeof(int));  
}
```

