CS & IT ENGINEERING



COMPUTER NETWORKS



Error Control

Lecture No-4

By- Ankit Doyla Sir



TOPICS TO BE COVERED

Simple Parity

D Parity



Eggor control

Error detection

Simple Parity

- @ QD Parity
- 3 CRC
- 4 chucksum

Error correction

Hamming code





Simple parity:

- In the Simple parity concept one extra bit (parity bit) is added to each dataword.
- Simple parity check can detect all single bit error.
- Simple parity check can not detect an even number of errors.
- Simple parity check can detect an odd number of errors.

Simple Parity



even parity

odd Parity

No of 1's must be even in each code word including the Parity bit

No. of 1's must be odd in each code word in cluding the Parity bit





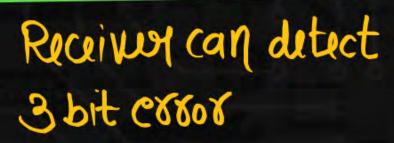
dataword

Dataword	code word	(1)	Sent	1 bit error	Revold 100	No-0F1's = odd
00	000					Received can detect one bit exxox
10	101	Air				our Bet one enough
11	110	(II)	000	2 bit croor	110	No. of 1's = even
						Received can not detect a bit expos













2D parity:



- Two dimensional parity check can detect and correct all single bit error and detect two or three bit error that occur any where in the matrix
- However only some pattern with four or more Error can be detected.
- In a 2D-parity check code, the information bits are organized in a matrix consisting of row and columns.
- For each row and each column one parity check bits is calculated.

Original Data or Message



0100100101101101101100101 3d Yow 11110110011001

By using even?
Parity

0	1	0	0	1	0	0	
0	1	0	1	0	1	1	
1	0	0	1	0	1	1	
1	1	1	0	1	1	1	
0	0	1	0	0	1	0	
0	1	0	0	0	0	1	

column Parity

Yow Parity

No of You's = 5

No of columns = 6

Transmitted data:

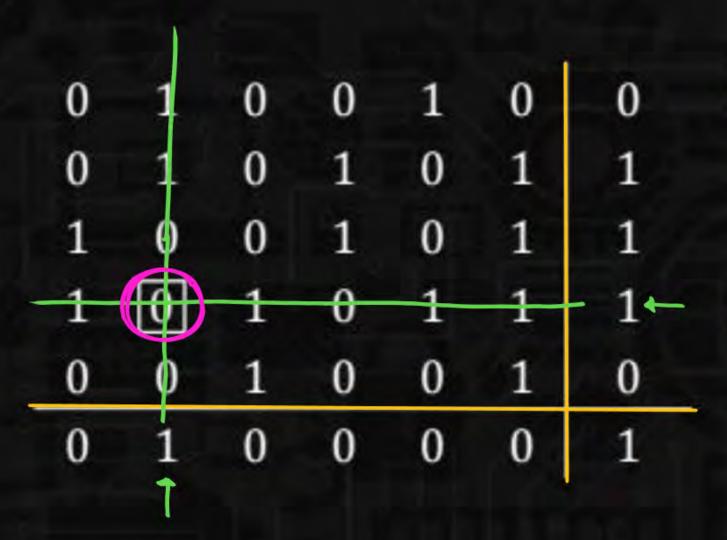
0100 100 WOD +21 0101011 and 60m) 309 RM 400 TOTT

4th 10m

0010010 Sth Kow 010000T



One Error:



- 1 one bit error can be detected as well as corrected
- @ one bit error will effect a Parity bits



Two - Error:

						0
0	1	0	0	0	1	1←
						1
1	0	1	0	1	1	1←
0	0	1	0	0	1	0
0		0	0	0	0	1
	1		T			

- 1) 9t can detect ? bit essor but 9t can not correct ? bit error
- @ 2 bit error will effect maximum 4 Parity bits
- 3) & bit error will effect minimum a Parity bits

Two - Error:

0	1	0	0	1	0	0
0	1	1	0	0	1	1
			1			
1	1	1	0	1	1	1
			0			
0	1	0	0	0	0	1
		T	4			





3 - Error:

0	1	0	0	0	0	0 -
				0		
1	0	0	1	0	1	1
1	1	0	0	1	1	1←
0	0	1	0	0	1	0
0	1	0	0	0	0	

- 1) 9t can detect 3 bit error but 9t can not correct 3 bit error
- @ 3 bit error will effect
 Maximum 6 Parity bit



3 - Error:

0	1	0	0	1	0	0
0	0	0	0	0	1	1
			1			
1	0	1	0	1	1	1-
0	0	1	0	0	1	0
0	1	0	0	0	0	

Note:
3 bit error wise effect
minimum a parity bitz



4 - Error:

			0				
			0				
1	0	0	1	0	1	1	
			1				
0	0	1	0	0	1	0	
0	1	0	0	0	0	1	

It can not detect 4 bit exsor

0	0	0	0	1	0	0
0	1	1	1	0	1	1-
1	0	0	0	0	1	1-
1	1	1	0	0	1	1
0	0	1	0	0	1	0
0	1	0	0	0	0	1
	4	bit e	stor a	detecta	ed	



Disadvantage of 2D parity:

If we have a error in the parity then this scheme does not work fine

