

COMPUTER SCIENCE

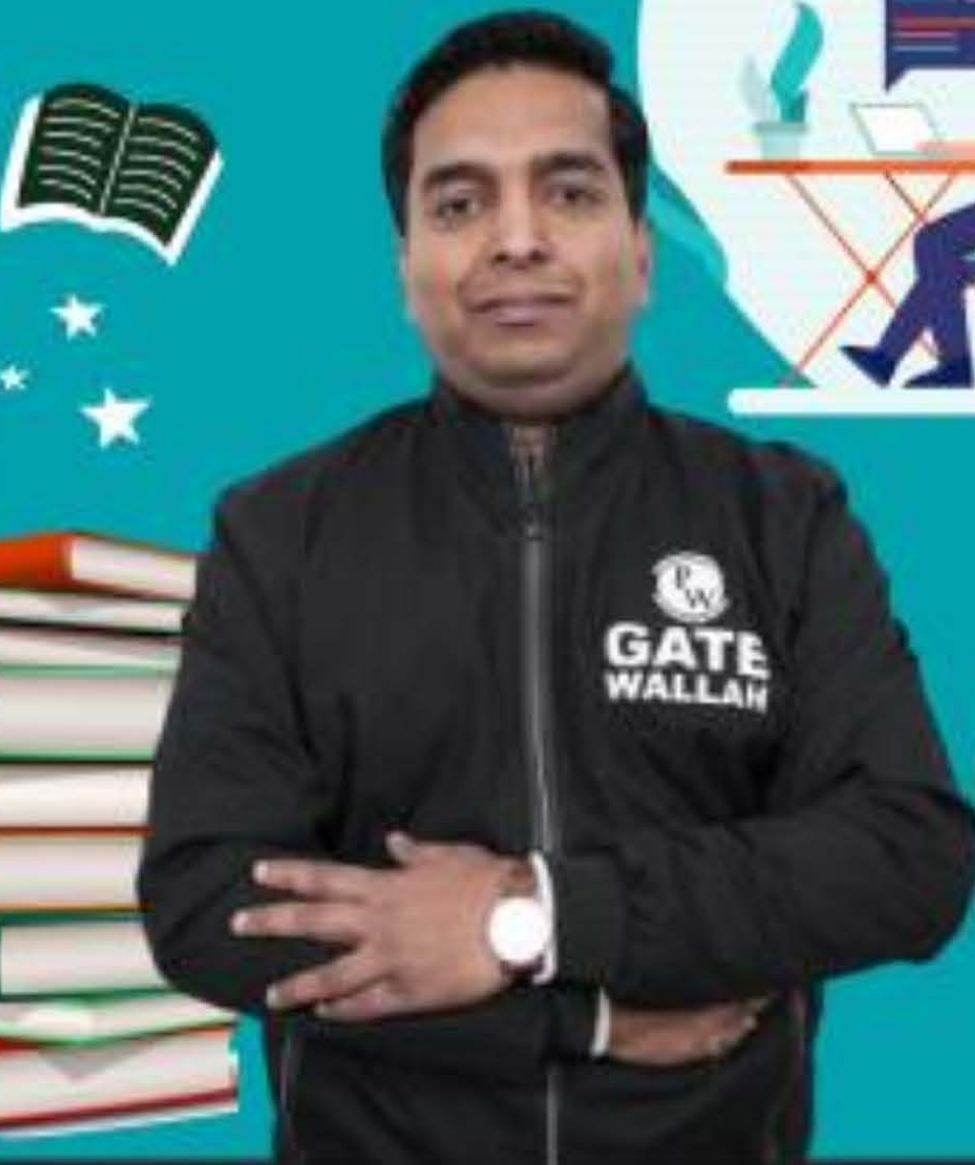


Database Management System

Transaction & Concurrency Control

Transaction Concept Part-02

Lecture_3



Vijay Agarwal sir



An orange diamond-shaped sign with a black border and the text 'TOPICS TO BE COVERED' in black capital letters.

**TOPICS
TO BE
COVERED**

A red diamond-shaped marker with a white border and the number '01' in white.

01

Serializable Schedule

A red diamond-shaped marker with a white border and the number '02' in white.

02

Conflict & View Serializable





Transaction

Transaction Concept

Read | Write | Commit

ACID Properties

Transaction State

Schedule

Serial

Schedule

$(m! \quad m: \# \text{ of transaction})$

Non Serial

Schedule.



Serial Schedule

m Transaction then $m!$ Serial Schedule

2 Transaction then

$2! = 2$ Serial Schedule

$\langle T_1 T_2 \rangle$ T_1 Followed by T_2

$\langle T_2 T_1 \rangle$ T_2 followed by T_1

3 Transaction then $3!$

$= 6$ Serial
Schedule

Non Serial Schedule ∴ Interleaved execution
of Two @ More Transaction.

Let T_1 transfer 100 Rs from A to B, and T_2 transfer 10% of the balance from A to B.

Schedule 1

$A=2000$
 $B=3000$
5000

S_1

T_1	T_2
-------	-------

read (A) $A=2000$
 $A := A - 100$
 write (A) $A=1900$
 read (B) $B=3000$
 $B := B + 100$
 write (B) $B=3100$
 commit

$A=1710$
 $B=3290$
5000

S_1

Consistent

$S_1 < T_1 T_2 >$

Serial schedule in which T_1 is followed by T_2 :

read (A) $A=1900$
 $temp := A * 0.1$ $temp=190$
 $A := A - temp$ $1900 - 190 = 1710$
 write (A) $A=1710$
 read (B) $B=3100$
 $B := B + temp$
 write (B) $B=3290$
 Commit

Schedule 2

$A=2000$
 $B=3000$
5000

S_2

T_1	T_2
-------	-------

read (A) $A=2000$
 $temp := A * 0.1$ $temp=200$
 $A := A - temp$
 write (A) $A=1800$
 read (B) $B=3000$
 $B := B + temp$
 write (B) $B=3200$
 Commit

read (A) $A=1800$
 $A := A - 100$
 write (A) $A=1700$
 read (B) $B=3200$
 $B := B + 100$
 write (B) $B=3300$
 commit

$A=1700$
 $B=3300$
5000

Consistent

$S_2 < T_2 T_1 >$

serial schedule where T_2 is followed by T_1

Serial Schedule

$\langle T_1 T_2 \rangle$

$\langle T_2 T_1 \rangle$

$n!$

All Serial
Schedules
Always
consistent.

Non Serial
Schedule means Concurrent
Execution

C₁
 $A: 2000$
 $+ B: 3000$
 $\hline 5000$

Schedule 3

T ₁	T ₂
read (A). $A=2000$ $A := A - 100$ <u>write (A)</u> $A=1900$	read (A) $A=1900$ $temp := A * 0.1$ $A := A - temp$ $temp=190$ <u>write (A)</u> $A=1710$
read (B) $B=3000$ $B := B + 100$ <u>write (B)</u> $B=3100$ commit	read (B) $B=3100$ $B := B + temp$ <u>write (B)</u> $B=3290$ Commit

$= S, \{T_1, T_2\}$

C₁
 $A: 1710$
 $+ B: 3290$
 $\hline 5000$
Consistent

S₁

C₂
 $A: 2000$
 $+ B: 3000$
 $\hline 5000$

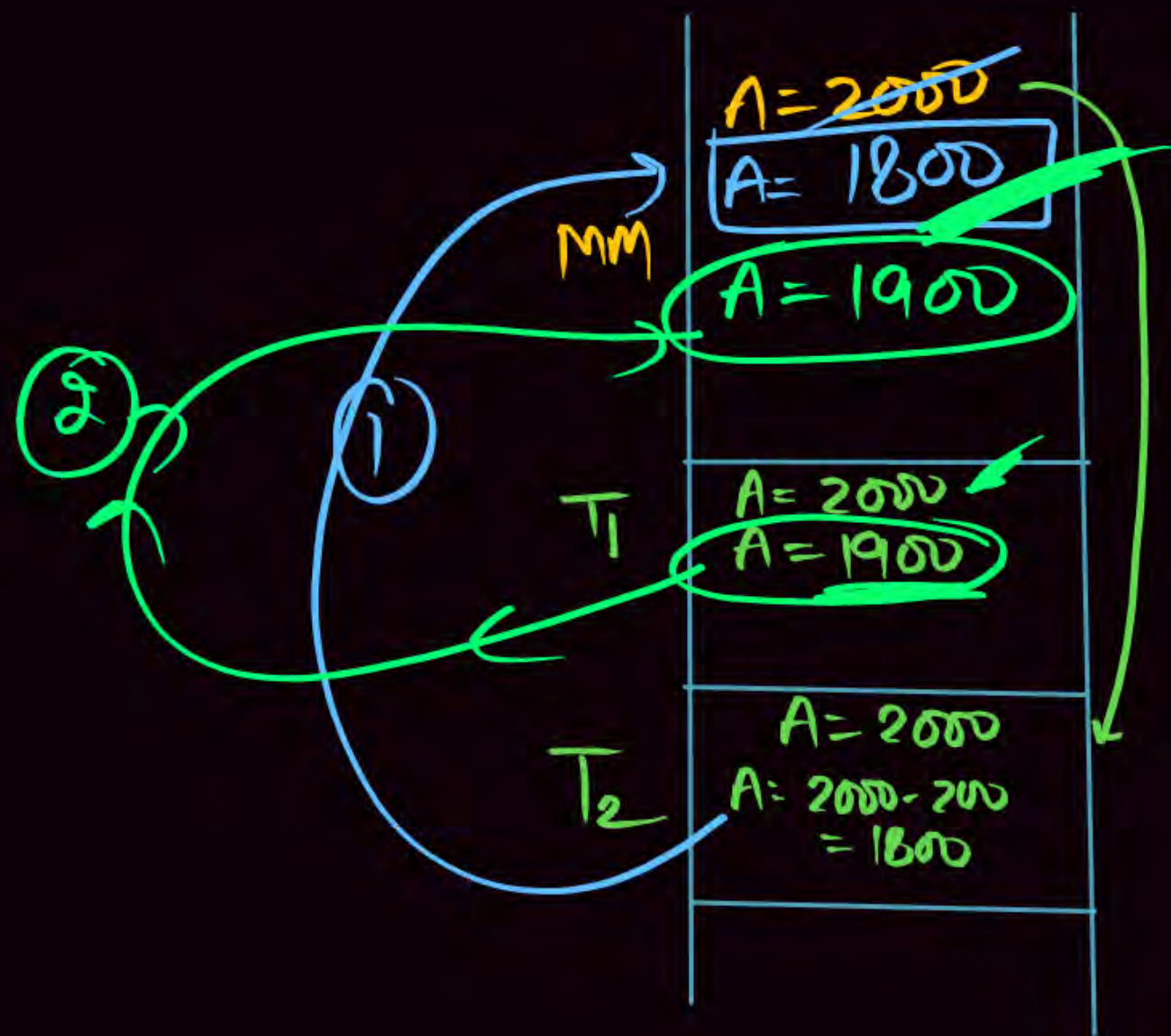
Schedule 4

T ₁	T ₂
$A=2000$ read (A) $A := A - 100$ 1900 <u>write (A)</u> $R=3000$ read (B) $B := B + 100$ $R=3100$ write (B) <u>commit</u>	read (A) $A=2000$ $temp := A * 0.1$ $temp=200$ $A := A - temp$ <u>write (A)</u> $A=1800$ read (B) $B=3100$ $B := B + temp$ <u>write (B)</u> $B=3300$ Commit

C₂
 $A: 1900$
 $+ B: 3300$
 $\hline 5200$

Inconsistent

C₂



DB

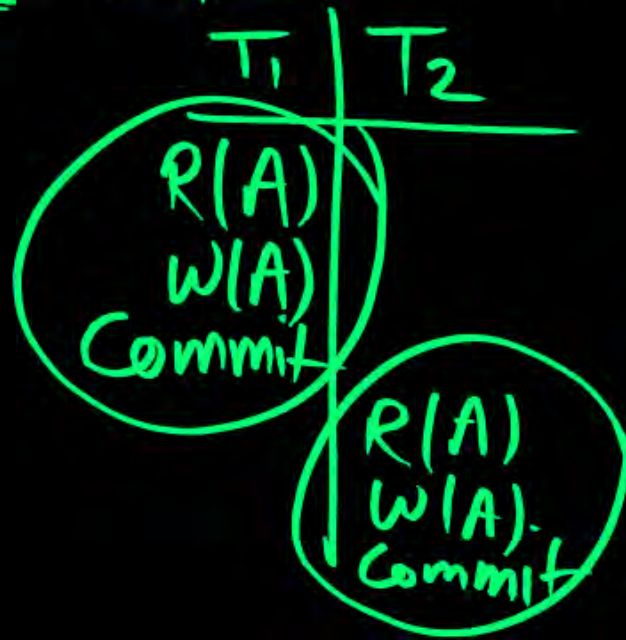
$A=2000$
 $B=3000$

Serial Schedule

- ❑ After Commit of one transaction, begins (Start) another transaction.
- ❑ Number of possible serial Schedules with 'n' transactions is "n!"
- ❑ The execution sequence of Serial Schedule always generates consistent result.

Example

S : $R_1(A)$ $W_1(A)$ Commit (T_1) $R_2(A)W_2(A)$ commit (T_2).



$\langle T_1, T_2 \rangle$; T_1 followed by T_2 .

Advantage

- Serial Schedule always produce correct result (integrity guaranteed) as no resource sharing.

Disadvantage

- Less degree of concurrency.
- Through put of system is low.
- It allows transactions to execute one after another.

n : # of transaction.

Note Serial Schedule ($n!$) are always Consistent.

But Non Serial Schedule May @ May Not be Consistent.

But we execute a schedule in Concurrent Manner.

Why Concurrent execution?

- To Improve CPU Utilization.
- Enhanced throughput.
- Decrease the waiting time.
- Fast Response, Effective Utilization of Resource etc.

Enjoying

CC

C

Doubt

WHY ?

- Serializability

Serializable Schedule.

Concurrent Execution \Downarrow
 \equiv Any Serial Schedule \Rightarrow Consistent

& the Process is called serializability.

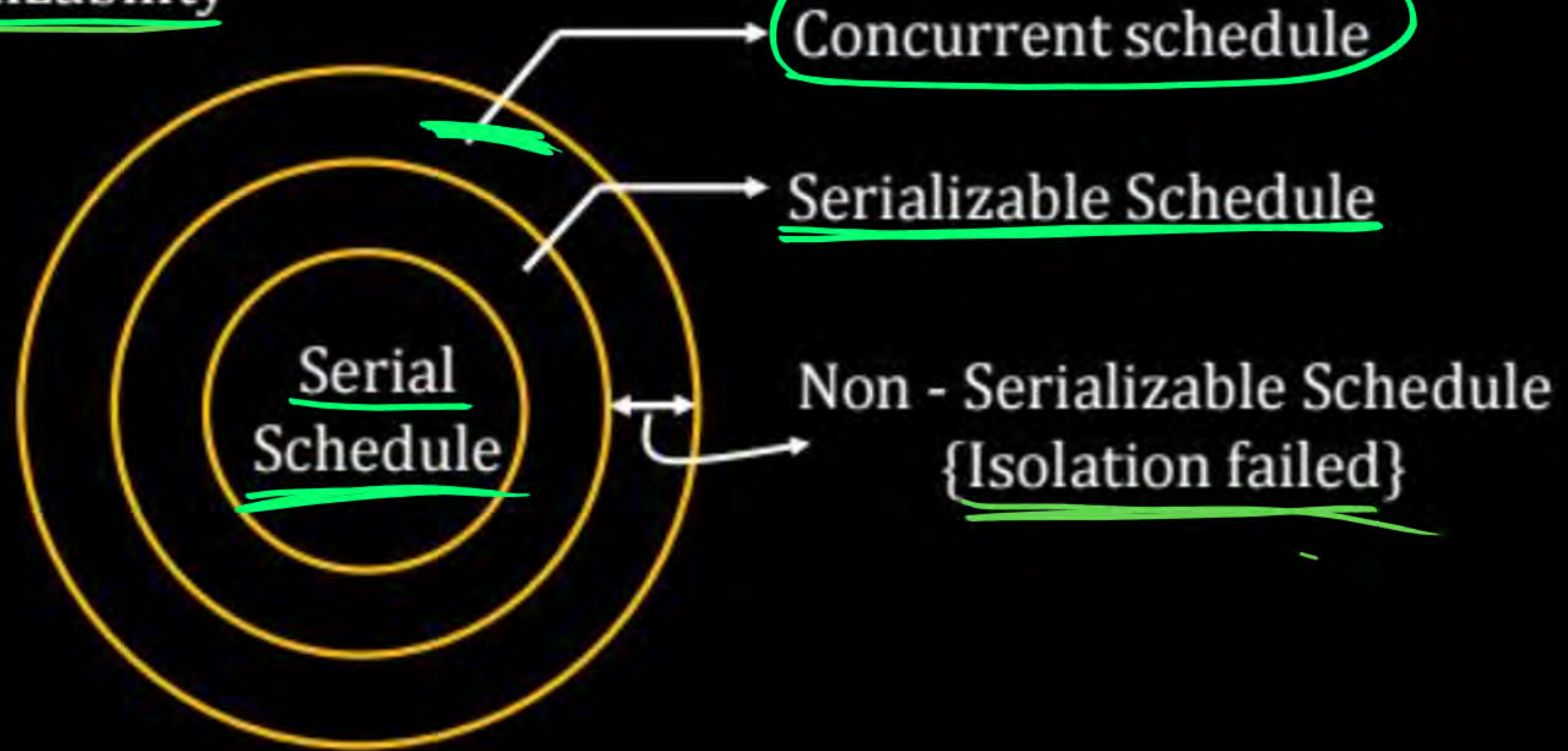
- ① Conflict Serializable
- ② View Serializable

Serializable Schedule

A Schedule is serializable Schedule if it is equivalent to a Serial Schedule.

(i) Conflict Serializability

(ii) View Serializability



Serializability

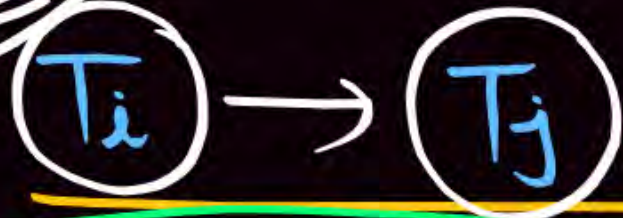


- ❑ **Basic Assumption:** Each transaction preserves database consistency.
- ❑ Thus, serial execution of a set of transactions preserves database consistency.
- ❑ A (possibly concurrent) schedule is serializable if it is equivalent to a serial schedule. Different forms of schedule equivalence give rise to the notions of:
 1. Conflict serializability
 2. view serializability

conflict serializability.

Conflict Instruction

Same Data Item



$R(A)$

$R(A)$

Non Conflict Instruction

$R(A)$

$W(A)$

$W(A)$

$R(A)$

$W(A)$

$W(A)$

Conflict Instruction / operation [Swapping Not possible]

T_i	T_j
$W(A)$	$R(B)$
$W(A)$	$W(B)$
$R(A)$	$W(B)$

Non
Conflict
Instrⁿ/operation

Different
Data Item

Conflict Serializable

① Basic Concept

~~Imp~~ ② Testing Method (Precedence Graph Method)

③ Conflict Equivalent
(Conflict Pair)

Conflict Equivalent Schedule.

Conflict Serializability

- If a schedule S can be transformed into a schedule S' by a series of swaps of non-conflicting instructions, we say that S and S' are conflict equivalent.
- We say that a schedule S is conflict serializable if it is conflict equivalent to a serial schedule.

\nearrow Question
 S : Given Non Serial Schedule (Concurrent Execution)
 S' : Any Serial Schedule of S (Given Question)

Given Schedule S

Q.L

T ₁	T ₂
Read(A) Write(A)	
	Read(A) Write(A)
Read(B) Write(B)	
	Read(B) Write(B)

C₁: 1710
3290 = S1
5000 C₁T₂

C₁ ≡ S < T₁ T₂ >

S₁ < T₁, T₂ >
T₁ followed by T₂.

(S) C₁
→ < T₂, T₁ > ? X

T ₁	T ₂
Read(A) Write(A) Read(B) Write(B)	
	Read(A) Write(A) Read(B) Write(B)

T ₁	T ₂
	Read(A) Write(A) Read(B) Write(B)
Read(A) Write(A) Read(B) Write(B)	

~~X~~

S₂ < T₂, T₁ >
T₂ followed by T₁.

(i) $C_1 \not\equiv S_2' \langle T_2, T_1 \rangle$

C_1 is Not possible to convert Serial Schedule S_2'

$S_2' \langle T_2, T_1 \rangle$ T_2 followed by T_1 .

But

C_1 is possible to convert ^{into} Serial Schedule $S_1' \langle T_1, T_2 \rangle$

(T_1 followed by T_2) So $C_1[S]$ is Conflict Serializable

$C_1 \equiv S_1 \langle T_1, T_2 \rangle$

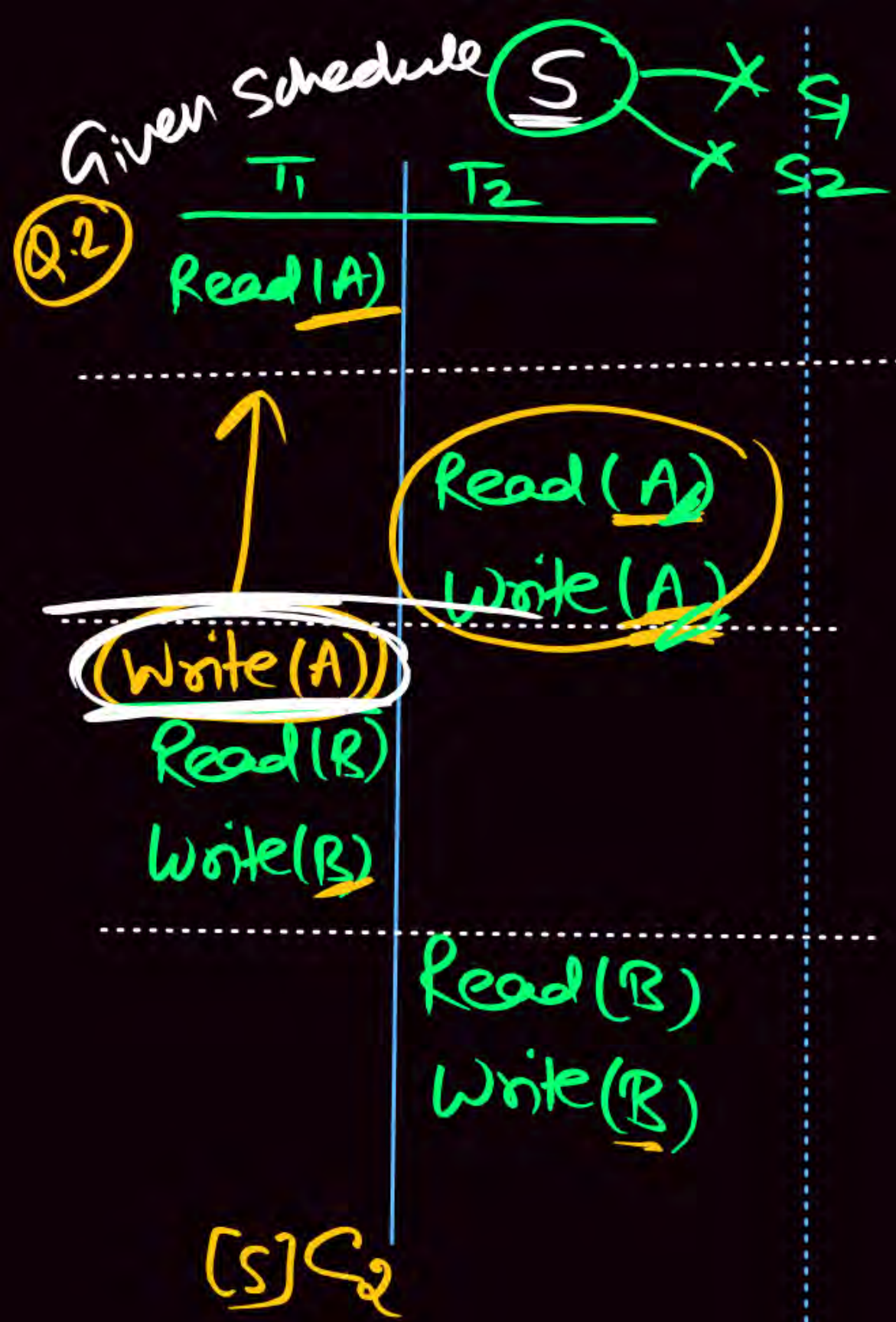
as a Equivalent Serial Schedule

$\langle T_1, T_2 \rangle$ T_1 followed by T_2 .

A: 1710

B: 3290

5000



T ₁	T ₂
Read(A)	
Write(A)	
Read(B)	
Write(B)	
	Read(A)
	Write(A)
	Read(B)
	Write(B)

$S_1 \langle T_1, T_2 \rangle$
 T_1 followed by T_2 .

T ₁	T ₂
	Read(A)
	Write(A)
	Read(B)
	Write(B)
Read(A)	
Write(A)	
Read(B)	
Write(B)	

$S_2 \langle T_2, T_1 \rangle$
 T_2 followed by T_1 .

$C_2[S]$ is Not possible to Convert into

Serial Schedule $\langle T_1, T_2 \rangle$ & $\langle T_2, T_1 \rangle$.

$$C_2[S] \neq S_1 \langle T_1, T_2 \rangle$$

$$C_2[S] \neq S_2 \langle T_2, T_1 \rangle$$

$C_2[S]$ is Not Conflict Serializable

Conflict Serializability (Cont.)

- Schedule 3 can be transformed into Schedule 6, a serial schedule where T_1 follows T_2 , by series of swaps of non-conflicting instructions. Therefore Schedule 3 is conflict serializable.

Schedule 3

T_1	T_2
read (A)	
Write (A)	
	read (A)
	write (A)
read (B)	
write (B)	
	read (B)
	write (B)

Schedule 6

T_1	T_2
read (A)	
write (A)	
read (B)	
write (B)	
	read (A)
	write (A)
	read (B)
	write (B)

Concept

- a) enjoying & CC
- b) CC
- c) C
- d) Doubt

Conflict Serializability (Cont.)

- Example of a schedule that is not conflict serializable:

T_3	T_4
read (Q)	
	write (Q)
write (Q)	

T_3	T_4
read(Q)	
write(Q)	
	write(Q)

$\langle T_1 T_2 \rangle$

T_3	T_4
	write(Q)
read(Q)	
write(Q)	

- We are unable to swap instructions in the above schedule to obtain either the serial schedule $\langle T_3, T_4 \rangle$, or the serial schedule $\langle T_4, T_3 \rangle$

Conflict Serializable

A schedule is said to be conflict serializable if it is conflict equivalent to a serial schedule.

Same conflicting operation order in C_1 & S_1

\therefore Its $\{C_1\}$ conflict is conflict serializable.

T_i T_1	T_j T_2	T_1	T_2
read(A)		read(A)	
write(A)		write(A)	
	read(A)	read(B)	
	write(A)	write(B)	
read(B)			read(A)
write(B)			write(A)
	read(B)		read(B)
	write(B)		write(B)
	C_L		S_L

Conflicting Instructions

- Instructions l_i and l_j of transactions T_i and T_j respectively, conflict if and only if there exists some item Q accessed by both l_i and l_j , and at least one of these instructions writes Q . ($i \neq j$)

1. $l_i = \text{read}(Q)$, $l_j = \text{read}(Q)$. l_i and l_j don't conflict.

2. $l_i = \text{read}(Q)$ $l_j = \text{write}(Q)$. They conflict.

3. $l_i = \text{write}(Q)$ $l_j = \text{read}(Q)$. They conflict.

4. $l_i = \text{write}(Q)$ $l_j = \text{write}(Q)$. They conflict.

T_i	T_j
$R(Q)$	$W(Q)$
$W(Q)$	$R(Q)$
$W(Q)$	$W(Q)$

} Conflict

- Intuitively, a conflict between l_i and l_j forces a (logical) temporal order between them.

❖ If l_i and l_j are consecutive in a schedule and they do not conflict, their results would remain the same even if they had been interchanged in the schedule.

Testing for Conflict serializability

Precedence Graph method

$G(V, E)$

[Vertex] V : Set of Transaction

[Edge] E : Edge $(T_i) \rightarrow (T_j)$ edge occurs if

<u>T_i</u>	<u>T_j</u>
(i) <u>execute</u> <u>$W(A)$</u> <u>Before</u>	<u>$R(A)$</u>
(ii) "	<u>$W(A)$</u>
(iii) "	<u>$W(A)$</u>



edge : Conflict operation.

Testing for Serializability

□ Testing for conflict serializability.

- ❖ Consider some schedule of a set of transactions T_1, T_2, \dots, T_n
- ❖ **Precedence graph** — a direct graph where the vertices are the transactions (names).
- ❖ We draw an arc from T_i to T_j if the two transaction conflict, and T_i accessed the data item on which the conflict arose earlier.
- ❖ We may label the arc by the item that was accessed.

CNC

Cycle Not Conflict.

Conflict operation.

T_i

T_j

$R(Q)$

$W(Q)$

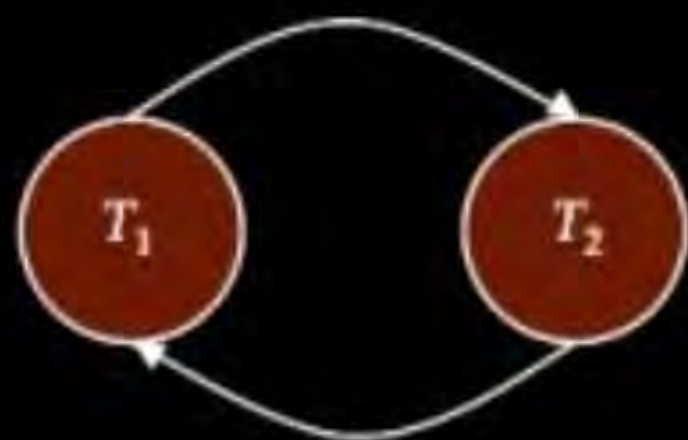
$W(Q)$

$R(Q)$

$W(Q)$

$W(Q)$

Example:



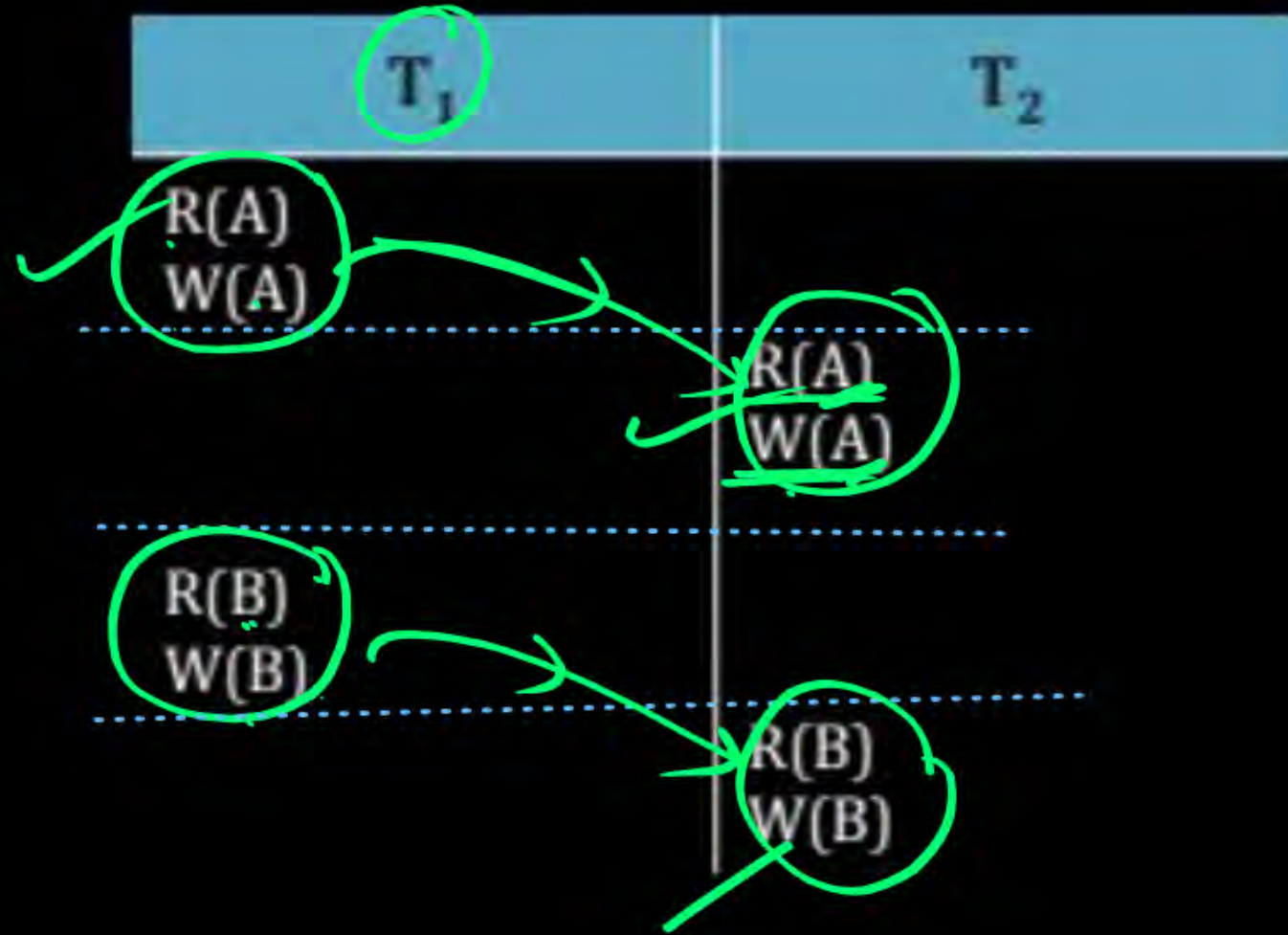
A schedule is conflict serializable if and only if its precedence graph is acyclic.

NOTE: CNC [Cycle not conflict serializable]

Q.1



S: $R_1(A)$ $W_1(A)$ $R_2(A)$ $W_2(A)$ $R_1(B)$ $W_1(B)$ $R_2(B)$ $W_2(B)$



Conflict Serializable
 $\langle T_1 T_2 \rangle$

C Consistent
S: $\langle T_1 T_2 \rangle$

T_1 followed by T_2 .

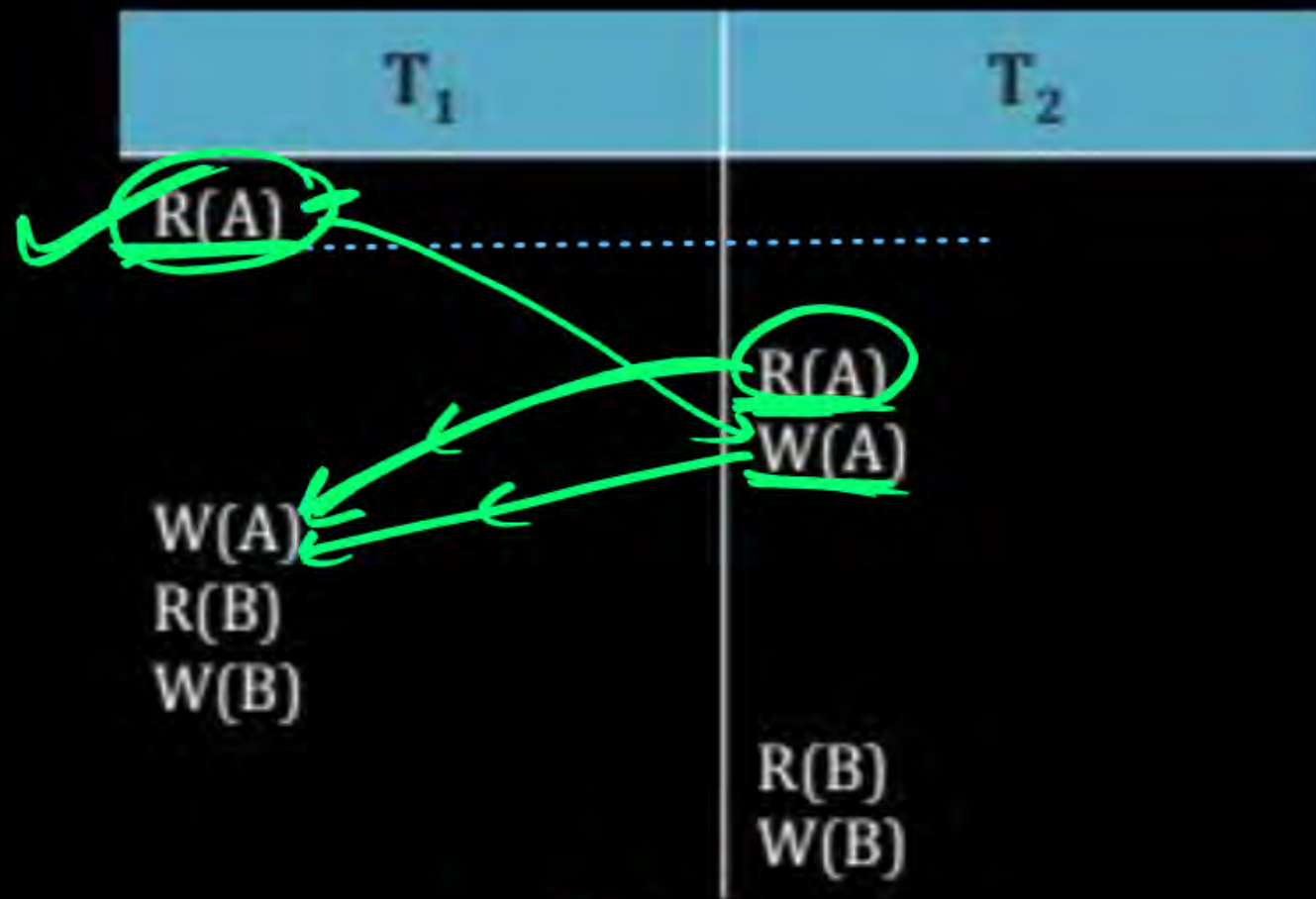
My Conflict operation



Q.2



$R_1(A) R_2(A) W_2(A) W_1(A) R_1(B) W_1(B) R_2(B) W_2(B)$



CNC



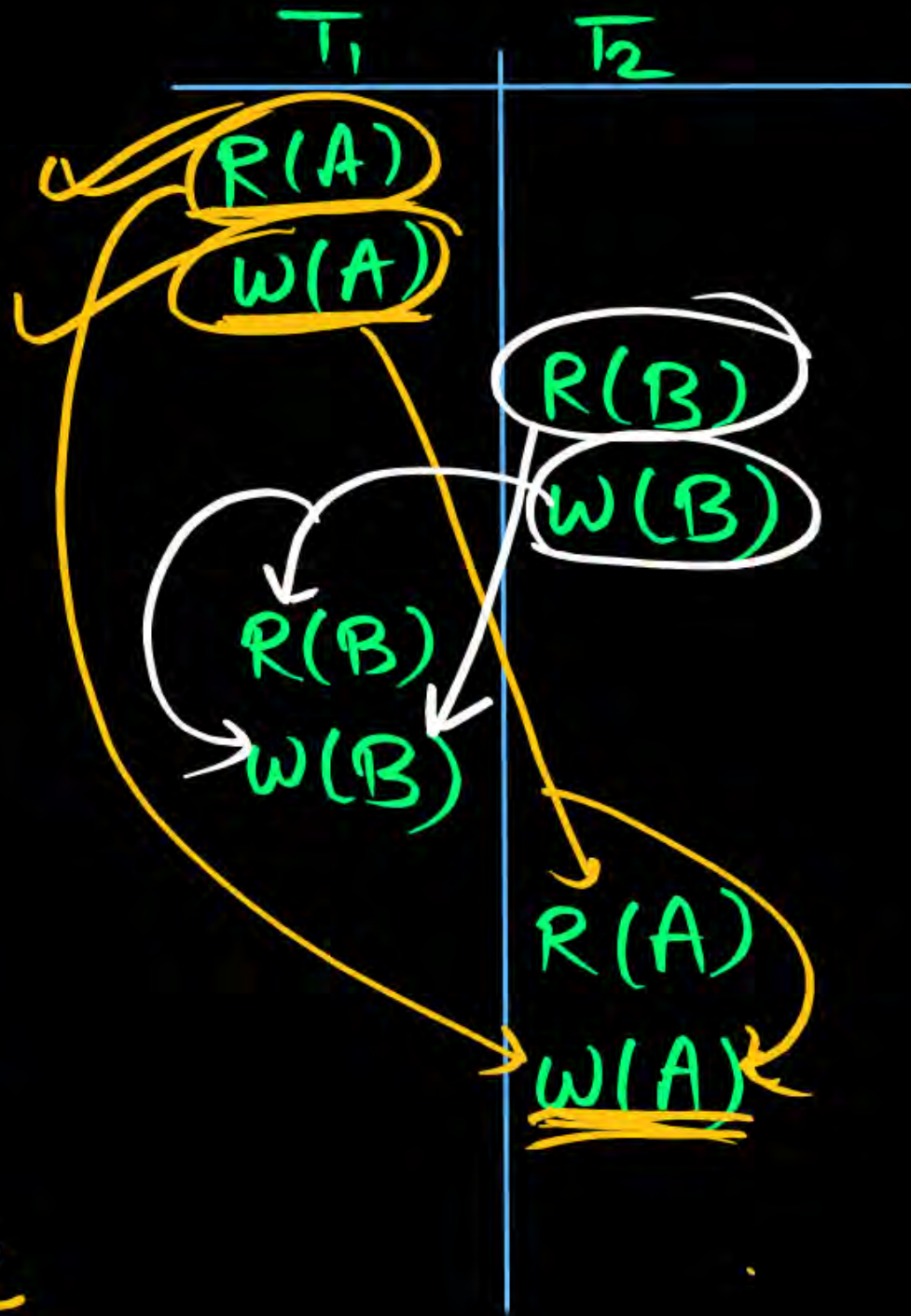
Cycle Not Conflict

C2

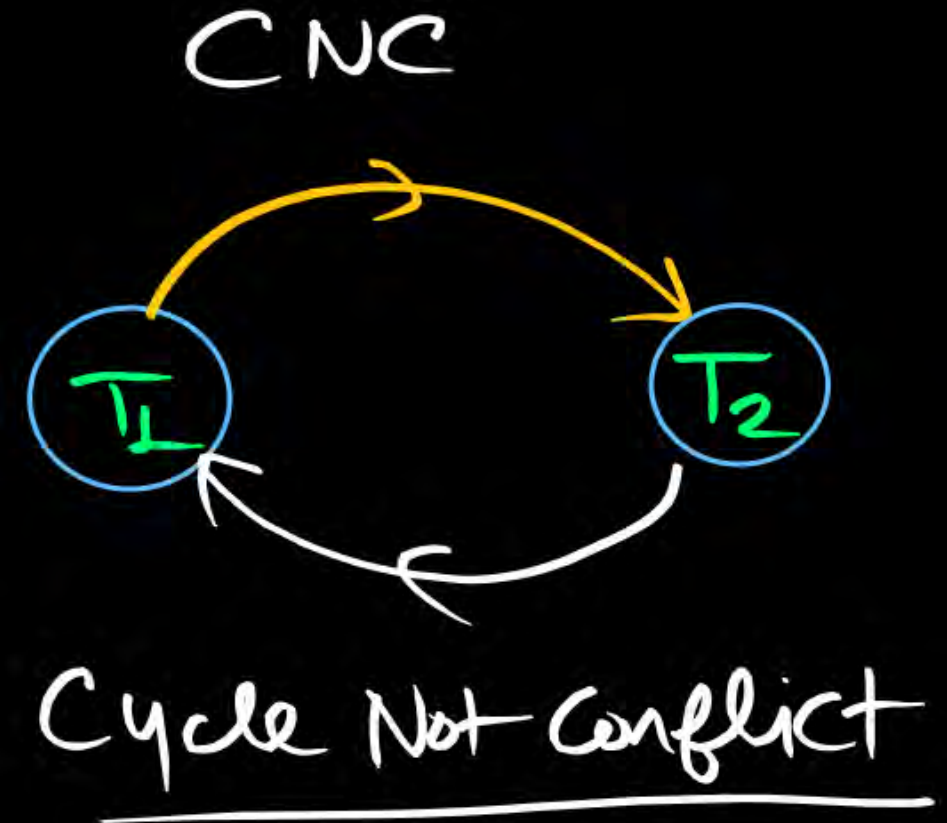
Inconsistent

Q.3

$R_1(A) W_1(A) R_2(B) W_2(B) R_1(B) W_1(B) R_2(A) W_2(A)$



$R(Q) \rightarrow W(Q)$
 $W(Q) \rightarrow R(Q)$
 $W(Q) \rightarrow W(Q)$
Create edge

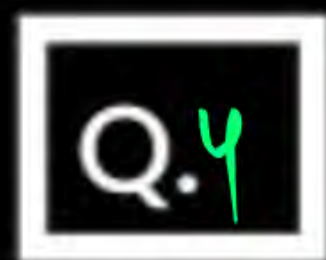




Serializability Order

Important Point 1:

1. If S_1 , S_2 Schedule are conflict equal then precedence graph of S_1 and S_2 must be same.
2. If S_1 and S_2 have same precedence graph then S_1 and S_2 may or may not conflict equal.



Consider the following schedules involving two transactions.
Which one of the following statements is TRUE?

S_1 : $r_1(X); r_1(Y); r_2(X); r_2(Y); w_2(Y); w_1(X)$

S_2 : $r_1(X); r_2(X); r_2(Y); W_2(Y); r_1(Y); w_1(X)$

[2007: 2 Marks]

- A** Both S_1 and S_2 are conflict serializable
- B** S_1 is conflict serializable and S_2 is not conflict serializable
- C** S_1 is not conflict serializable and S_2 is conflict serializable
- D** Both S_1 and S_2 are not conflict serializable

Q.5



Consider the following four schedules due to three transactions (indicated by the subscript) using read and write on a data item x , denoted by $r(x)$ and $w(x)$ respectively. Which one of them is conflict serializable?

[2014(Set-1): 2 Marks]

A

$r_1(x); r_2(x); w_1(x); r_3(x); w_2(x)$

B

$r_2(x); r_1(x); w_2(x); r_3(x); w_1(x)$

C

$r_3(x); r_2(x); r_1(x); w_2(x); w_1(x)$

D

$r_2(x); w_2(x); r_3(x); r_1(x); w_1(x)$

Q. 6.

Let $r_i(z)$ and $w_i(z)$ denote read and write operations respectively on a data item by a transaction T_i . Consider the following two schedules.

$S_1: r_1(x) r_1(y) r_2(x) r_2(y) w_2(y) w_1(x)$

$S_2: r_1(x) r_2(x) r_2(y) w_2(y) r_1(y) w_1(x)$

Which one of the following options is correct?

[MCQ: 2021: 2M]

- A** S_1 is conflict serializable, and S_2 is not conflict serializable.
- B** S_1 is not conflict serializable, and S_2 is conflict serializable.
- C** Both S_1 and S_2 are conflict serializable.
- D** Neither S_1 nor S_2 is conflict serializable.

Any Doubt ?



**THANK
YOU!**

