			ASSERTION COVERAGE SCENARIOS Check ERR is asserted during the rotate operation									
SL .NO	FEATURED Error detection in ROR	SIGNALS	SCENARIOS	STATUS								
1	Error detection in ROR	ERR	Check ERR is asserted during the rotate operation	not started								
	Error		Check ERR is asserted during the inp_valid = 00									
2	detection in inp valid 00	ERR	operation	not started								
1	Eroor detection_in_inp_valid_01_s ingle operand Eroor		Check ERR is asserted during the inp_valid = 01									
3	ingle operand	FRR	operation	not started								
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	detection in inp valid 01		Check ERR is asserted during the inp_valid = 01									
4	multiple operand	ERR	operation	not started								
	detection_in_inp_valid_10_s ingle operand	EDD	Check ERR is asserted during the inp_valid = 10	not started								
- 5	ingle operand	ERR	operation	not started								+
	detection in inn valid 11 s		Check ERR is asserted during the inp_valid = 11									
6	Error detection_in_inp_valid_11_s multiple operand Comparision less	ERR	operation	not started								
	Comparision less	L G	Check L is asserted during the comparision	not started								
8	Comparision greater	G	Check G is asserted during the comparision	not started								
9	Comparision eqaul	E	Check E is asserted during the comparision	not started								
	Overflow detection	OFLOW	Check whether OFLOW is asserted during subtraction									
10	Overnow detection	COLIT	Check whether COUT is asserted during subtraction Check whether COUT is asserted during the addition	n not started								
11	CMD validation arithmetic	MODE CMD	Check whether CMD is within the valid range	not started								+
13	carry_out detection CMD validation arithmetic CMD validation logical	MODE,CMD	Check whether CMD is within the valid range	not started								
14	Priority handling	В	Latest operand priority	not started								
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