

①

NETWORK THEOREMS

Different electric ckt (according to their properties) are:

1. Ckt: - A ckt is a conducting path through which an electric current either flows or is intended to flow.
2. Parameters: - The various elements of an electric ckt. are called its parameters like resistance, inductance & capacitance. These parameters may be lumped or distributed.

3. Linear ckt: - A linear ckt. is one whose parameters are constants i.e. they do not change with voltage or current. (It comprises independent sources, linear dependent source & linear passive elements like R, L, C, transformers.)

4. Non-linear ckt: - A non-linear ckt. is one whose parameters change with voltage or current. (See below)

5. Bilateral ckt: - A bilateral ckt. is one whose parameters or characteristics are the same in either direction. The usual transmission line is bilateral because it can be made to perform its fun: equally well in either dirⁿ.
Ch. means voltage-current relationship of a linear element in pt. line
Resistor is an example of bilateral ckt. element

6. Unilateral ckt: - It is that ckt. whose properties or characteristics change with the dirⁿ of its operation. A diode rectifier is a unilateral ckt. because it cannot perform rectification in both dirⁿ.
(vacuum or semiconductor)

7. Electric Network: A combination of various electric elements, connected in any manner whatsoever, is called an electric network.

8. Passive Network: - is one which contains no source of e.m.f. in it. (passive elements → Resistor, inductor, capacitor & transformer)

9. Active Network: - is one which contains one or more sources of emf. (Active elements → generator, amplifiers, & oscillators.)

10. Node: - is a junⁿ in a ckt, where two or more ckt. elements are connected together.

11. Branch: - is that part of a network which lies between two junⁿs.

It should be noted that, unless stated otherwise, an electric network would be assumed passive.

A network is said to be completely solved or analyzed when all voltages and all currents in its different elements are determined.

Non-linear ckt: ① When the current through a resistor is so large that the power dissipation changes its temp., the value of the resistance changes. ② An iron-core inductor is also non-linear, the permeability of iron depending on the current through the coil. ③ The capacitance of a p-n junⁿ is an example of a non-linear capacitance, the value of C depending on the voltage applied across the junⁿ. ④ Many vacuum and semiconductor devices like triodes, transistors are non-linear.

There are two general approaches to network analysis:

- ① Direct Method:- Here, the network is left in its original form ~~which~~ while ~~not~~ determining its different voltages and currents. Such methods are usually restricted to fairly simple ckt. and include Kirchhoff's laws, loop analysis, Nodal analysis, Superposition Theorem, Compensation Theorem, Reciprocity theorem etc.
- ② Network Reduction Method:- Here, the original network is converted into a much simpler equivalent ckt. for rapid Calculation of different quantities. This method can be applied to simple as well as complicated networks. Examples of this method are: delta / Star and Star/Delta conversions, Thevenin's Theorem and Norton's Theorem etc.

✓ Kirchhoff's laws: These laws are more comprehensive than Ohm's law and are used for solving electrical networks which may not be readily solved by the latter.

Kirchhoff's laws, two in numbers, are particularly useful

- (a) in determining the equivalent resistance of a complicated network of conductors, and (b) for calculating the currents flowing in the various conductors.

① Kirchhoff's Current Law (KCL) a Kirchhoff's Point Law.

It states as follows:

In any electrical network, the algebraic sum of the currents meeting at a point or junⁿ must be zero. It simply means that total current leaving a junⁿ equal to the total current entering. That junⁿ at a junction

In other words $\sum I = 0$

In this case incoming currents = outgoing currents.

Assuming the incoming currents to be a +ve & the outgoing currents -ve, we have $I_1 + (-I_2) + (-I_3) + (+I_4) + (-I_5) = 0 \Rightarrow I_1 + I_4 - I_2 - I_3 - I_5 = 0$

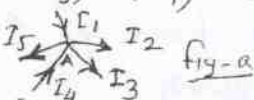
$$\Rightarrow I_1 + I_4 = I_2 + I_3 + I_5$$

[I.C = O.C]

Similarly, in fig-(b) for node-A

$$+I + (-I_1) + (-I_2) + (-I_3) + (-I_4) = 0 \Rightarrow I = I_1 + I_2 + I_3 + I_4$$



[incoming current = outgoing current]



② Kirchhoff's Mesh Law or Voltage Law (KVL): It states as follows: the algebraic sum of the products of currents and resistances in each of the conductors in any closed path (or mesh) in a network plus the algebraic sum of the emf's in that path is zero.

In other words $\sum IR + \sum e.m.f = 0$... round a mesh

The basis of this law: If we start from a particular point and go round the mesh till we come back to the starting pt. Then we must be at the same potential with which we started.

V_s  voltage source  current source

determination of sign:

(a) Sign of Battery emf: A rise in voltage should be given a +ve sign and a fall in voltage a -ve sign.

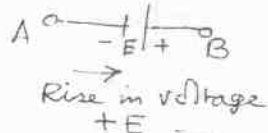


Fig-1

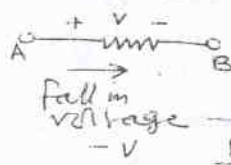
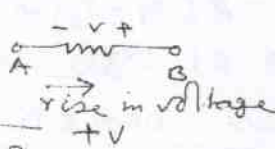


Fig-2



It is important to note that the sign of the battery emf is independent of the direction of current through the branch. (Fig-1) it depends only on the polarity.

(b) Sign of IR drop (Fig-2): If we go through a resistor in the same dirⁿ as the current, then there is a fall in potential because current flows from a higher to a lower potential. Hence this voltage fall should be taken -ve. However, if we go in a dirⁿ opposite to that of the current, then there is a rise in voltage. Hence, this voltage rise should be given a +ve sign.

It is clear that the sign of voltage drop across a resistor depends on the dirⁿ of current through that resistor but is independent of the polarity of any other source of emf in the circuit under consideration.

Consider the closed path ABCDA

$I_1 R_1$ is -ve (fall in potential)

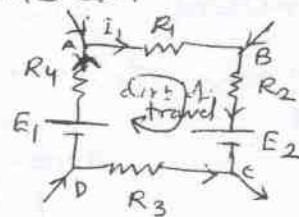
$I_2 R_2$ " " "

$I_3 R_3$ +ve (rise ")

$I_4 R_4$ -ve (fall ")

E_2 -ve (" ")

E_1 is +ve (rise ")



Using KVL, we get $-I_1 R_1 - I_2 R_2 + I_3 R_3 - I_4 R_4 - E_2 + E_1 = 0$
 $\Rightarrow I_1 R_1 + I_2 R_2 - I_3 R_3 + I_4 R_4 = E_1 - E_2$

Ex: determine the currents in the unbalanced bridge ckt.
Also determine the p.d. across BD and the resistance from B to D.

Sol: Applying KVL to ckt DACD:

$$-i_1 - 4i_3 + 2i_2 = 0 \Rightarrow i_1 - 2i_2 + 4i_3 = 0 \quad \text{--- (1)}$$

ckt. ABEA gives

$$-2(i_1 - i_3) + 3(i_2 + i_3) + 4i_3 = 0$$

$$\Rightarrow 2i_1 - 3i_2 - 9i_3 = 0 \quad \text{--- (2)}$$

ckt. DABED gives $i_1 - i_1 - 2(i_1 - i_3) - 2(i_1 + i_2) + 2 = 0$

$$\Rightarrow 5i_1 + 2i_2 - 2i_3 = 2 \quad \text{--- (3)}$$

By Cramer's rule

By solving, Current in DA = $i_1 = 30/91$ A

" " DC = $i_2 = 17/91$ "

" " AC = $i_3 = 1/91$ "

" " AB = $i_1 - i_3 = 29/91$ A

" " CB = $i_2 + i_3 = 18/91$ A

" " external ckt. = $i_1 + i_2 = \frac{47}{91}$ A

Internal voltage drop in cell = $2(i_1 + i_2) = \frac{94}{91}$ V

\therefore p.d. across pts. D & B = $2 - \frac{94}{91} = \frac{88}{91}$ V

Equivalent res of the bridge bet. D & B

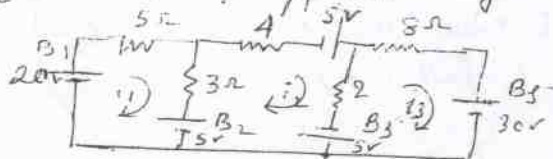
$$= \frac{\text{p.d. bet. pts. B \& D}}{\text{current through BD}} = \frac{88/91}{47/91} = \frac{88}{47} = 1.87 \Omega$$

Maxwell's Loop current Method:

If b be the no. of branches & j be the no. of junctions in a given network, then the no. of independent eqs $b - (j - 1)$

Ex: determine the current supplied by each battery in the ckt.

Sol:



for loop 1, $20 - 5i_1 - 3(i_1 - i_2) - 5 = 0 \Rightarrow 8i_1 - 3i_2 = 15 \quad \text{--- (1)}$

for loop 2, $-4i_2 + 5 - 2(i_2 - i_3) + 5 + 5 - 3(i_2 - i_1) = 0 \Rightarrow 3i_1 - 9i_2 + 2i_3 = -15 \quad \text{--- (2)}$

for loop 3, $-8i_3 - 30 - 5 - 2(i_3 - i_2) = 0 \Rightarrow 2i_2 - 10i_3 = 35 \quad \text{--- (3)}$

solving by Cramer's rule, $i_1 = 765/299$ A

$$i_2 = 545/299$$

$$i_3 = -1875/598$$

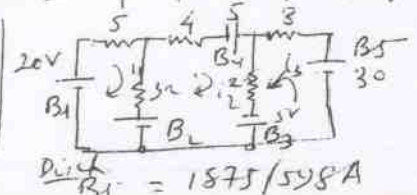
Since i_3 turns out to be -ve, actual dir's of flow of loop currents are as shown below

Discharge of current of $B_1 = 765/299$ A

charging " " $B_2 = i_1 - i_2 = 220/299$ A

Discharge " " $B_3 = i_2 + i_3 = \frac{2965}{598}$ A

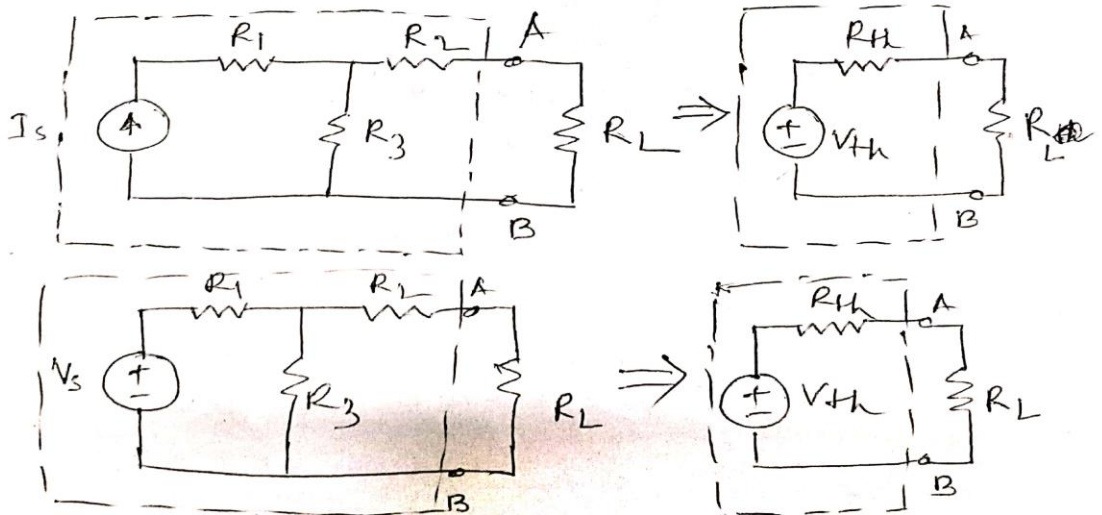
" " $B_4 = i_2 = 545/299$ A



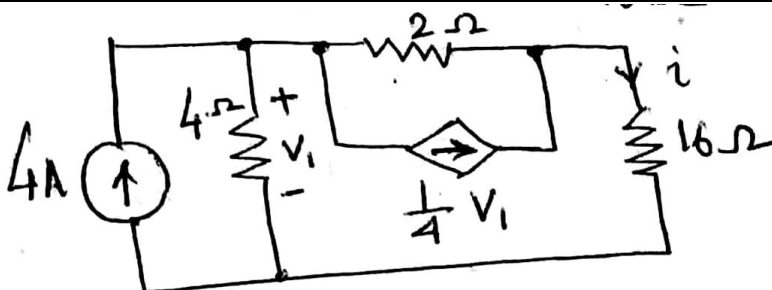
1. State Thevenin's theorem and draw the equivalent circuit.

Thevenin's Theorem:- The current flowing through a load resistance connected across any two terminals A & B of a linear, active, bilateral network is given by $V_{th}/(R_{th} + R_L)$ where V_{th} = the o/c voltage (i.e. the voltage across the two terminals when R_L is removed).

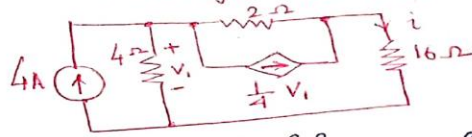
R_{th} = internal resistance of the network as viewed back into the open-circuited network from the terminals with all sources have been replaced by their internal resistances.



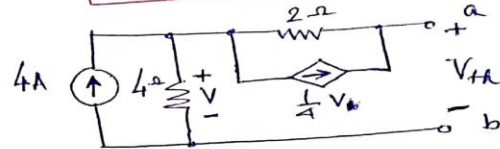
Determine the current i through the 16 ohm resistor by using Thevenin's theorem.



Q.8 Determine the current i through the 16Ω resistor by Thevenin Theorem.



Solution: For finding V_{th} :



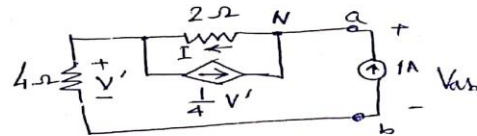
4A current passes through 4Ω only,

$$\therefore V = 4 \times 4 = 16 \text{ Volts}$$

$$\therefore \text{drop across } 2\Omega = 2 \times \frac{1}{4} \times 16 = 8 \text{ Volts. [with a (+ve)]}$$

$$\therefore V_{th} = 16 + 8 = 24 \text{ Volts}$$

For finding R_{th} , we connect 1A source across a & b and 4A source is opened.



$$V' = 4 \times 1 = 4 \text{ Volts}$$

$$\begin{aligned} \text{Applying KCL at node-N: } I &= \frac{1}{4} V' + 1 \\ &= 1 + 1 \\ \therefore I &= 2 \text{ A} \end{aligned}$$

$$\therefore \text{drop across } 2\Omega = 2 \times 2 = 4 \text{ Volts}$$

$$\therefore V_{ab} = 4 + 4 = 8 \text{ Volts}$$

$$\therefore R_{th} = 8 \Omega$$

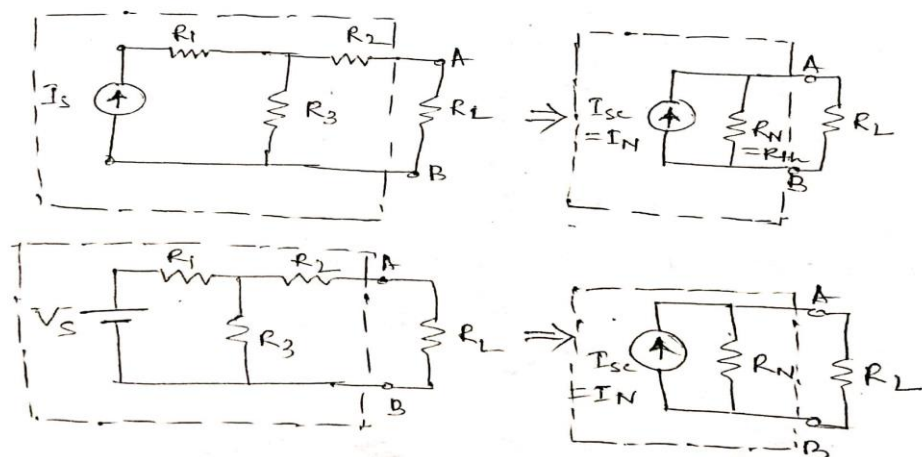
\therefore Thevenin equivalent ckt:

$$\therefore \text{Current through } 16\Omega = i = \frac{24}{8+16} = \frac{24}{24} = 1 \text{ A}$$

$$\therefore i = 1 \text{ A}$$

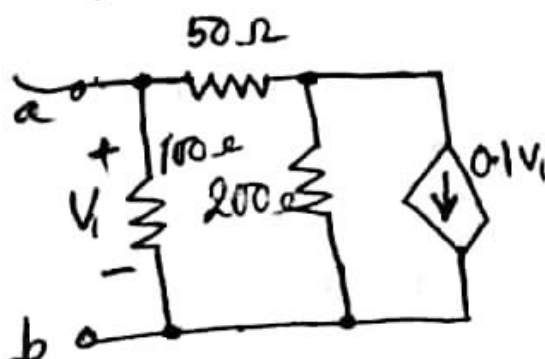
2. State Norton's theorem and draw the equivalent circuit.

Norton's Theorem:— Any two-terminal active network when viewed from its o/p terminals is equivalent to a constant current source and a parallel resistance. The constant current is equal to the current which would flow in a short ckt. placed across the terminals and parallel resistance is the resistance of the network when viewed from these o/p terminals after all sources have been replaced by their internal resistances.

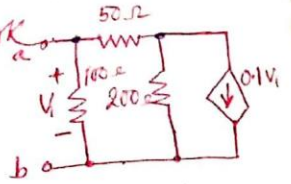


CS Scanned with CamScanner

Find the Norton equivalent of the network.



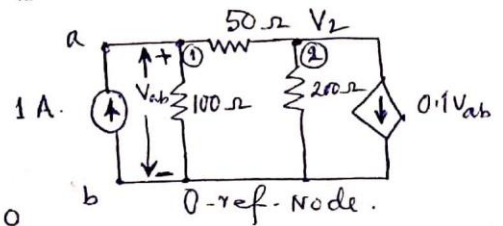
Q. find the Norton equivalent of the network



Solution: As there is no independent source in the circuit, so $I_{sc} = 0$

For finding R_N , we connect 1 A current source to the terminals a & b.

Applying KCL to Node-1:



$$1 - \frac{V_{ab}}{100} - \frac{V_{ab} - V_2}{50} = 0$$

$$\Rightarrow \frac{100 - V_{ab} - 2V_{ab} + 2V_2}{100} = 0$$

$$\Rightarrow 100 - 3V_{ab} + 2V_2 = 0$$

$$\Rightarrow 3V_{ab} - 2V_2 = 100 \quad \text{--- (1)}$$

Applying KCL to Node-2:

$$\frac{V_{ab} - V_2}{50} - \frac{V_2}{200} - 0.1V_{ab} = 0$$

$$\Rightarrow \frac{4V_{ab} - 4V_2 - V_2 - 20V_{ab}}{200} = 0$$

$$\Rightarrow -16V_{ab} - 5V_2 = 0$$

$$\Rightarrow 16V_{ab} + 5V_2 = 0 \quad \text{--- (2)}$$

For solving, (1) $\times 5 \Rightarrow 15V_{ab} - 10V_2 = 500$

(2) $\times 2 \Rightarrow 32V_{ab} + 10V_2 = 0$

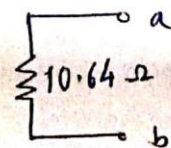
$$\text{(Add)} \quad 47V_{ab} = 500$$

$$\Rightarrow V_{ab} = \frac{500}{47}$$

$$\therefore V_{ab} = 10.64 \text{ volts}$$

$$\therefore R_N = 10.64 \Omega$$

\therefore Norton equivalent ckt.



3. State and prove Maximum Power transfer theorem.

Max^m power Transfer Theorem :-

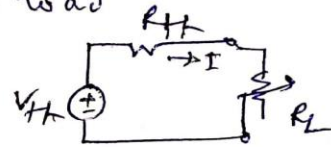
A resistive load will consume max^m power from a network when the load resistance is equal to the resistance of the network as viewed from the open terminals, with all energy sources replaced by their internal resistances (i.e. called R_{th})

So condⁿ of Max^m power transfer, $R_L = R_{th}$
and Max^m power, $P_{max} = \frac{V_{th}^2}{4R_{th}}$

Proof: Power consumed by load

$$P = I^2 R_L$$

$$= \frac{V_{th}^2 R_L}{(R_L + R_{th})^2}$$

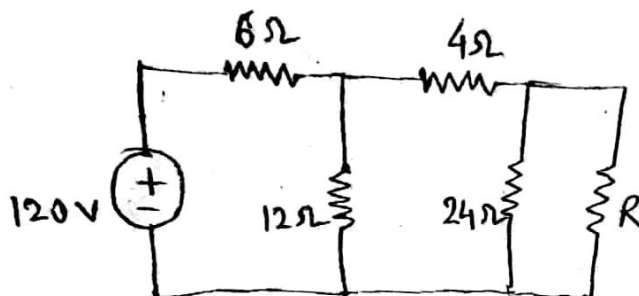


For P to max^m, $\frac{dP}{dR_L} = 0$

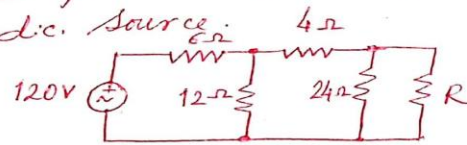
$$\Rightarrow \boxed{R_L = R_{th}}$$

CS Scanned with CamScanner

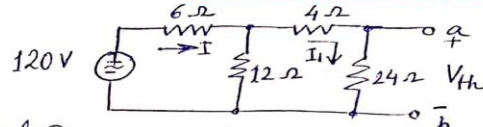
Determine the maximum power dissipated by the resistor R from the dc source.



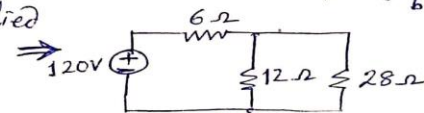
25 Determine the Maximum power dissipated by the resistor R from the d.c. source.



Solution: for finding V_{th} ,



To find the total current supplied by 120V source, I



$$\Rightarrow 120V \text{ source, } R = 8.4\Omega \left[= \frac{12 \times 28}{40} \right]$$

$$\Rightarrow 120V \text{ source, } I = 14.4\Omega$$

$$\therefore I = \frac{120}{14.4} \text{ Amp.}$$

$$\therefore \text{Current Through } 24\Omega, I_1 = \frac{120}{14.4} \times \frac{12}{40} = \frac{10}{4} = 2.5 \text{ A}$$

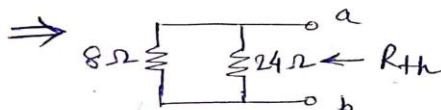
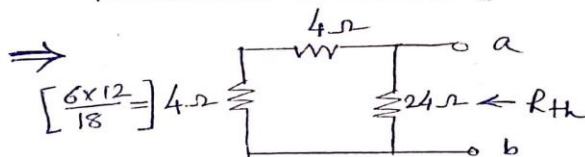
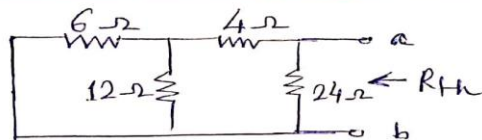
$$\therefore V_{th} = \text{drop across } 24\Omega \\ = 2.5 \times 24 \\ = 60 \text{ Volts.}$$

$$V_{th} = 60 \text{ Volts}$$



Scanned with CamScanner

for finding R_{th} ,



$$\therefore R_{th} = \frac{8 \times 24}{32} \\ R_{th} = 6\Omega$$

\therefore Thevenin equivalent ext. 60V source, 6Ω

For Maximum power dissipation, $R = R_{th} = 6\Omega$

$$\therefore \text{Maximum power dissipated} = \frac{V_{th}^2}{4R_{th}} \\ = \frac{60 \times 60}{4 \times 6}$$

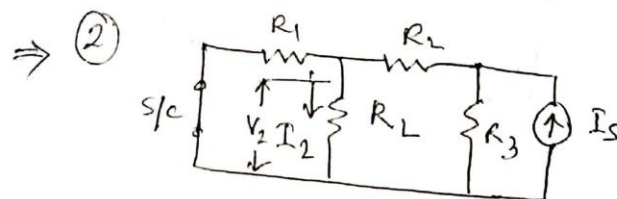
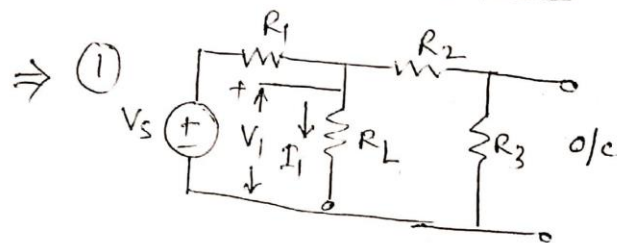
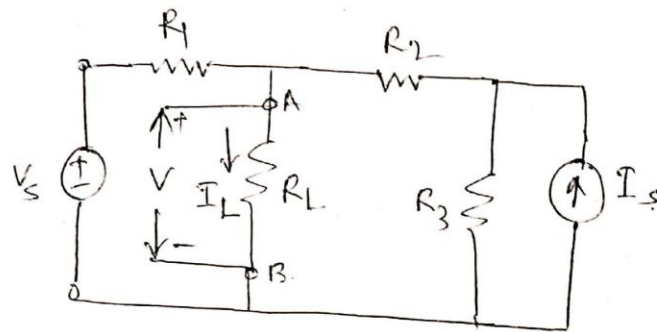
$$\therefore P_{max} = 150 \text{ W}$$



Scanned with CamScanner

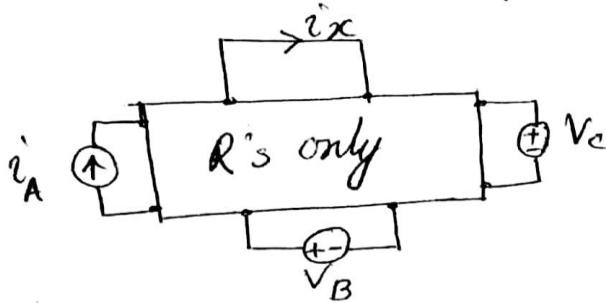
4. State Superposition theorem.

Superposition Theorem:- In any linear resistive network containing several sources, the voltage across or the current through any resistor or source may be calculated by adding algebraically all the individual voltage or current caused by the separate sources acting alone, with all other sources replaced by their internal resistances.



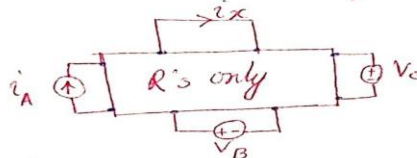
$$\therefore \left. \begin{aligned} I_L &= I_1 + I_2 \\ V_L &= V_1 + V_2 \end{aligned} \right\}$$

With source i_a and v_b on in the circuit and $v_c=0$ and $i_x=20$ amp. With i_a and v_c on and $v_b=0$, $i_x=5$ amp. ; and finally with all the three sources on, $i_x=12$ amp. Find i_x if the only source operating is (a) i_a (b) v_b (c) v_c (d) find i_x if i_a and v_c are doubled in amplitude and v_b is reversed.



2. With sources i_a and v_b on in the circuit and $v_c=0$, $i_x=20$ A. With i_a and v_c on and $v_b=0$, $i_x=-5$ A; and finally, with all three sources on, $i_x=12$ A. Find i_x if the only source operating is (a) i_a ; (b) v_b ; (c) v_c . (d) Find i_x if i_a and v_c are doubled in amplitude and v_b is reversed.

CEM-3



Solution: Let the value of i_x when only i_a is operating be i_1
 Similarly " " " i_x " " v_b " " " i_2
 and " " " i_x " " v_c " " " i_3

From Superposition theorem and according to the question,

$$i_1 + i_2 = 20 \quad \text{--- (1)}$$

$$i_1 + i_3 = -5 \quad \text{--- (2)}$$

$$i_1 + i_2 + i_3 = 12 \quad \text{--- (3)}$$

from ① & ③, $\Rightarrow i_3 = 12 - (i_1 + i_2)$
 $= 12 - 20 = -8$

$\therefore \boxed{i_3 = -8 \text{ A}}$ Am ----- (c)

from ② & ③, $\Rightarrow i_2 = 12 - (i_1 + i_3)$
 $= 12 - (-5) = 17$

$\therefore \boxed{i_2 = 17 \text{ A}}$ Am ----- (b)

from ①, $i_1 = 20 - i_2$
 $= 20 - 17 = 3$

$\therefore \boxed{i_1 = 3 \text{ A}}$ Am ----- (a)

(d). If i_a and v_c are doubled in amplitude and v_b is reversed

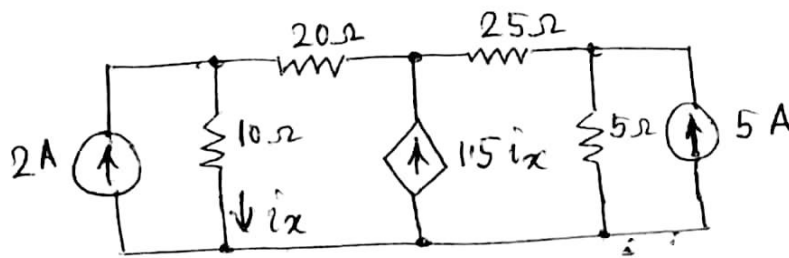
Then $i_x = 2i_1 - i_2 + 2i_3$
 $= 2 \times 3 - 17 + 2(-8)$
 $= 6 - 17 - 16$
 $= 6 - 33 = -27$

$\therefore \boxed{i_x = -27 \text{ A}}$ Am ----- (d)

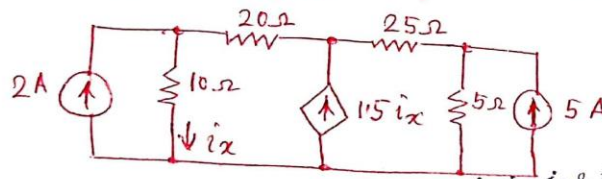


Scanned with CamScanner

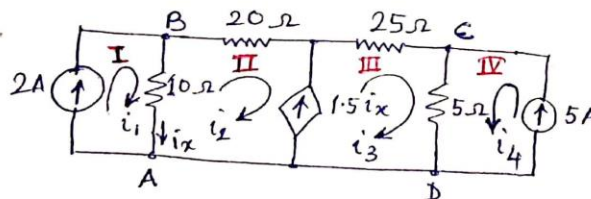
5. Use Mesh analysis to find the power generated by each of the five sources.



6. Use mesh analysis to find i_x in the circuit.



Solution: Assume mesh currents i_1, i_2, i_3 & i_4 in respective meshes



Applying KVL to loop ABEDA

$$-20i_2 - 25(i_3) - 5(i_3 + i_4) - 10(i_2 - i_1) = 0$$

$$\Rightarrow 20i_2 + 25i_3 + 5i_3 + 5 \times 5 + 10i_2 - 10 \times 2 = 0$$

$$\Rightarrow 30i_2 + 30i_3 + 5 = 0$$

$$\Rightarrow 6i_2 + 6i_3 = -1 \quad \text{--- (2)}$$

Here $i_1 = 2A$

$i_4 = 5A$

$i_1 - i_2 = i_x$

$1.5i_x = i_3 - i_2$

or, $1.5(i_1 - i_2) = i_3 - i_2$

or, $1.5 \times 2 - 1.5i_2 - i_3 + i_2 = 0$

or, $0.5i_2 + i_3 = 3 \quad \text{--- (1)}$

for solving, (1) $\times 6 \Rightarrow 3i_2 + 6i_3 = 18$

(2) $\times 1 \Rightarrow 6i_2 + 6i_3 = -1$

(Sub.) $-3i_2 = 19$

$\Rightarrow i_2 = -\frac{19}{3} = -6.33$

$\therefore i_2 = -6.33A$

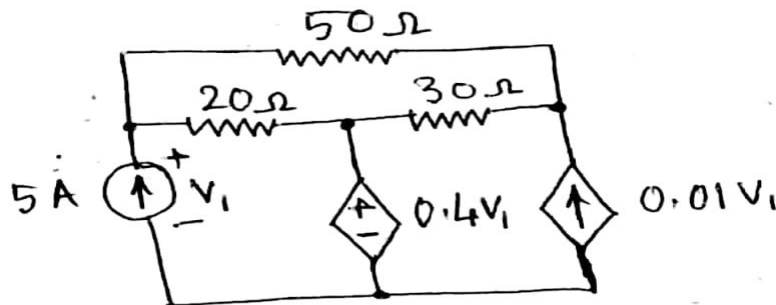
$\therefore i_x = i_1 - i_2$

$= 2 - (-6.33) = 8.33$

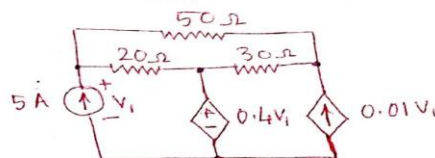
$i_x = 8.33A$

Ans:

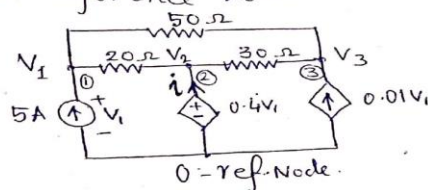
6. Use Nodal analysis to determine V_1 and power being supplied by the dependent current source in the circuit.



3. Use nodal analysis to determine V_1 and power being supplied by the dependent current source in the circuit.



Solution: Defining nodal voltages V_1 , V_2 & V_3 with respect to the reference node '0'.



Here $V_2 = 0.4V_1$ [constrained equation]

Applying KCL at node-1: $5 - \frac{V_1 - V_2}{20} - \frac{V_1 - V_3}{50} = 0$

$$\text{or, } 50 - \frac{V_1 - V_2}{2} - \frac{V_1 - V_3}{5} = 0$$

$$\text{or, } \frac{500 - 5V_1 + 5V_2 - 2V_1 + 2V_3}{10} = 0$$

$$\text{or, } 7V_1 - 5V_2 - 2V_3 = 500 \quad \text{--- (1)}$$

$$\text{or, } 7V_1 - 2V_1 - 2V_3 = 500$$

$$\text{or, } 5V_1 - 2V_3 = 500 \quad \text{--- (1)}$$

Applying KCL at node-3: $\frac{V_1 - V_3}{50} + \frac{V_2 - V_3}{30} + 0.01V_1 = 0$

$$\text{or, } \frac{3V_1 - 3V_3 + 5V_2 - 5V_3 + 1.5V_1}{150} = 0$$

$$\text{or, } 4.5V_1 + 5V_2 - 8V_3 = 0$$

$$\text{or, } 4.5V_1 + 2V_1 - 8V_3 = 0$$

$$\text{or, } 6.5V_1 - 8V_3 = 0 \quad \text{--- (2)}$$

for Solving, $(1) \times 4 \Rightarrow 20V_1 - 8V_3 = 2000$

$(2) \times 1 \Rightarrow 6.5V_1 - 8V_3 = 0$

$$\begin{array}{r} \text{(Sub.)} \\ \hline 13.5V_1 = 2000 \end{array}$$

$$\text{or, } V_1 = \frac{2000}{13.5} = 148.15$$

$$\therefore \boxed{V_1 = 148.15 \text{ Volts.}} \text{ Am.}$$

from (2) ; $V_3 = \frac{6.5V_1}{8}$

$$= \frac{6.5 \times 148.15}{8}$$

$$\therefore V_3 = 120.37 \text{ Volts.}$$

Power Supplied by the dependent current source $0.01V_1$

$$P_{0.01V_1} = V_3 \times 0.01V_1$$

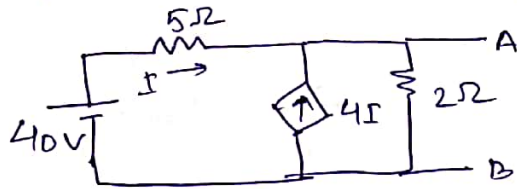
$$= 120.37 \times 0.01 \times 148.15 = 178.33$$



Scanned with
CamScanner

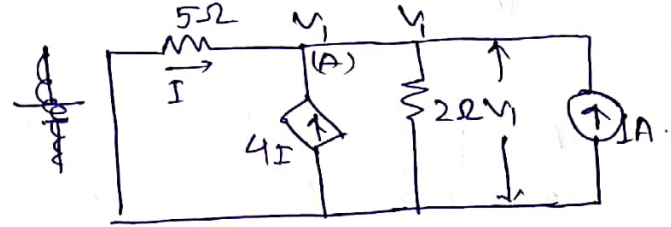
$$\therefore \boxed{P_{0.01V_1} = 178.33 \text{ Watts.}} \text{ Am.}$$

Q Find the Norton's equivalent to the left of terminal AB of the circuit shown in Fig. 2.



sol For the calculation of R_N we will short the terminal AB with 1A current source and short circuit the voltage source.

Now, applying nodal analysis at node A.



$$\frac{V_1}{2} - I - 4I - 1 = 0 \quad \text{--- (1)}$$

$$V_1 = -5I \quad \text{--- (2)}$$

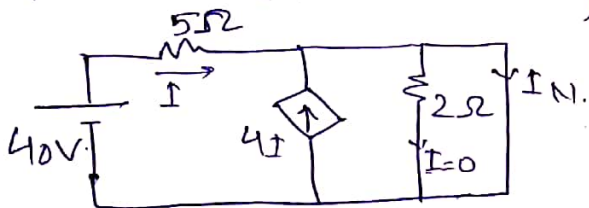
using equ. (2) in (1) we get.

$$\frac{-5I}{2} - 5I = 1 \Rightarrow I = -\frac{2}{15} \text{ A}$$

$$\text{so, } V_1 = -5I = \frac{2}{3} \text{ V}$$

$$R_N = \frac{V_1}{1} = \frac{2}{3} \Omega$$

For the calculation of I_N , we will short the terminal AB.

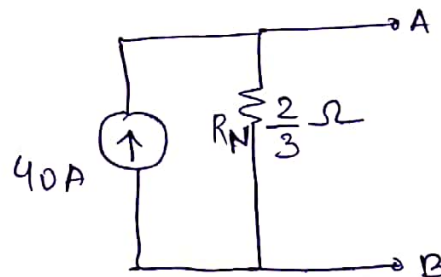


$$I_N = I + 4I = 5I$$

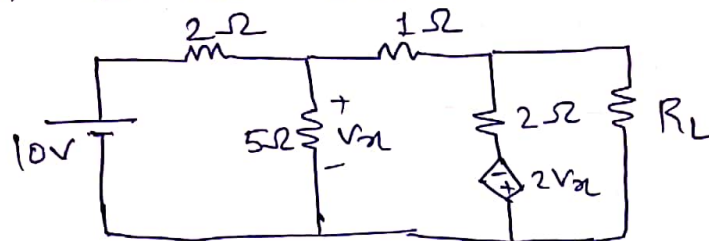
$$I = \frac{40}{5} = 8 \text{ A}$$

$$\Rightarrow I_N = 5 \times 8 = 40 \text{ A}$$

\Rightarrow Norton's equivalent ckt



Q Find R_{th} across load resistor R_L .



Sol Since this circuit consists of dependent voltage sources, we will apply a 1A current source across load resistance & short circuit the ideal voltage source.

Applying nodal analysis at node A, we get

$$\frac{V_1 + 2V_x}{2} + \frac{V_1 - V_x}{1} - 1 = 0$$

$$V_1 + 2V_x + 2V_1 - 2V_x = 2$$

$$3V_1 = 2$$

$$V_1 = \frac{2}{3} \text{ V}$$

$$R_{th} = \frac{V_1}{1} = \frac{2}{3} \Omega$$

